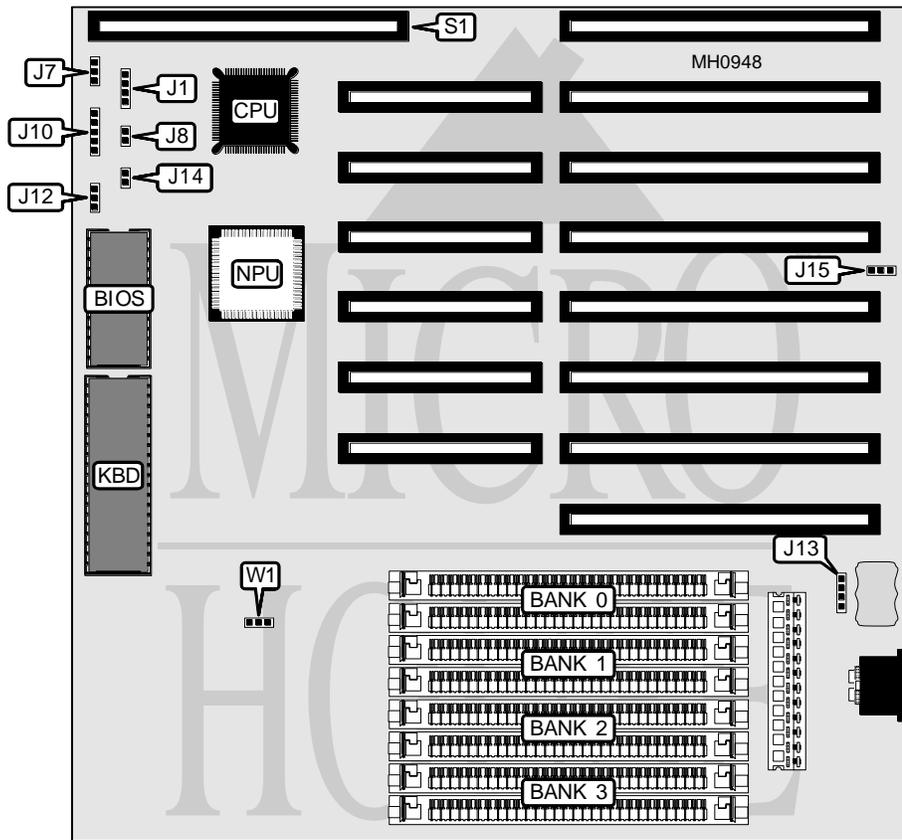


AUSTIN COMPUTER SYSTEMS

486/SLC 25

Processor	80386SX/CX486SLC
Processor Speed	25MHz
Chip Set	C & T
Max. Onboard DRAM	16MB
Cache	None
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	16-bit external cache memory card slot
NPU Options	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J1	External battery	J13
Turbo switch	J7	Reset switch	J14
Turbo LED	J8	16-bit external cache memory card	S1
Power LED & keylock	J10		

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AUSTIN COMPUTER SYSTEMS

486/SLC 25

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CPU speed select forced high	J7	pins 1 & 2 closed
í Monitor type select color	J12	pins 1 & 2 closed
Monitor type select monochrome	J12	pins 2 & 3 closed
í CMOS memory normal operation	J15	pins 2 & 3 closed
CMOS memory clear	J15	pins 1 & 2 closed
í Cache card absent	W1	pins 2 & 3 closed
Cache card present	W1	pins 1 & 2 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
2MB	(2) 256K x 9			
2MB	(2) 1M x 9	NONE	NONE	NONE
3MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
5MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	NONE
8MB	(2) 1M x 9			
9MB	(2) 256K x 9	(2) 256K x 9	(2) 4M x 9	NONE
10MB	(2) 1M x 9	(2) 4M x 9	NONE	NONE
12MB	(2) 1M x 9	(2) 1M x 9	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9	NONE	NONE

CACHE JUMPER CONFIGURATION FOR CACHE CARD					
Size	J2	J3	J4	J5	J6
64KB/2-Way Set Associative	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
64KB/Directly Mapped	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.