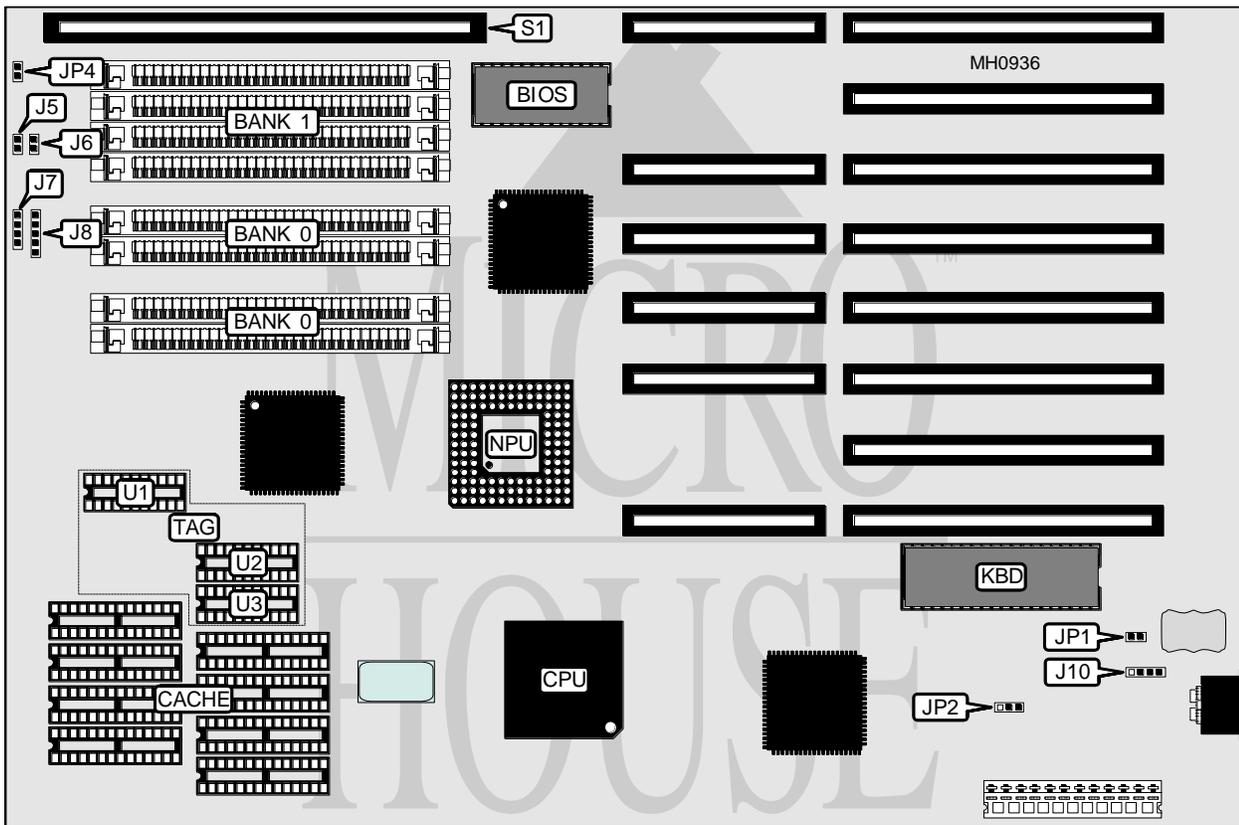


# JC INFORMATION SYSTEMS CORPORATION

## 386DX WBC 2025

<b>Processor</b>	80386DX
<b>Processor Speed</b>	25/33MHz
<b>Chip Set</b>	OPTI
<b>Max. Onboard DRAM</b>	32MB
<b>Cache</b>	64KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	32-bit external memory card slot
<b>NPU Options</b>	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo switch	J5	External battery	J10
Reset switch	J6	Turbo LED	JP4
Speaker	J7	32-bit external memory card	S1
Power LED & keylock	J8		

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# JC INFORMATION SYSTEMS CORPORATION

## 386DX WBC 2025

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP1	Closed
Monitor type select monochrome	JP1	Open
í CMOS memory normal operation	JP2	pins 2 & 3 closed
CMOS memory clear	JP2	pins 1 & 2 closed
í Factory configured - do not alter	JP3	Closed
í Factory configured - do not alter	JP5	pins 2 & 3 closed
í Factory configured - do not alter	JP6	pins 1 & 2 closed
í Factory configured - do not alter	JP7	pins 1 & 2 closed
í Factory configured - do not alter	JP8	pins 1 & 2 closed
í Factory configured - do not alter	JP9	pins 2 & 3 closed
í Factory configured - do not alter	JP10	pins 1 & 2 closed
Note : The locations of jumpers JP3, JP5, JP6, JP7, JP8, JP9 and JP10 are unidentified.		

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1 M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	TAG (U1)	TAG (U2 & U3)
64KB	(8) 8K x 8	(1) 16K x 4	(2) 4K x 4