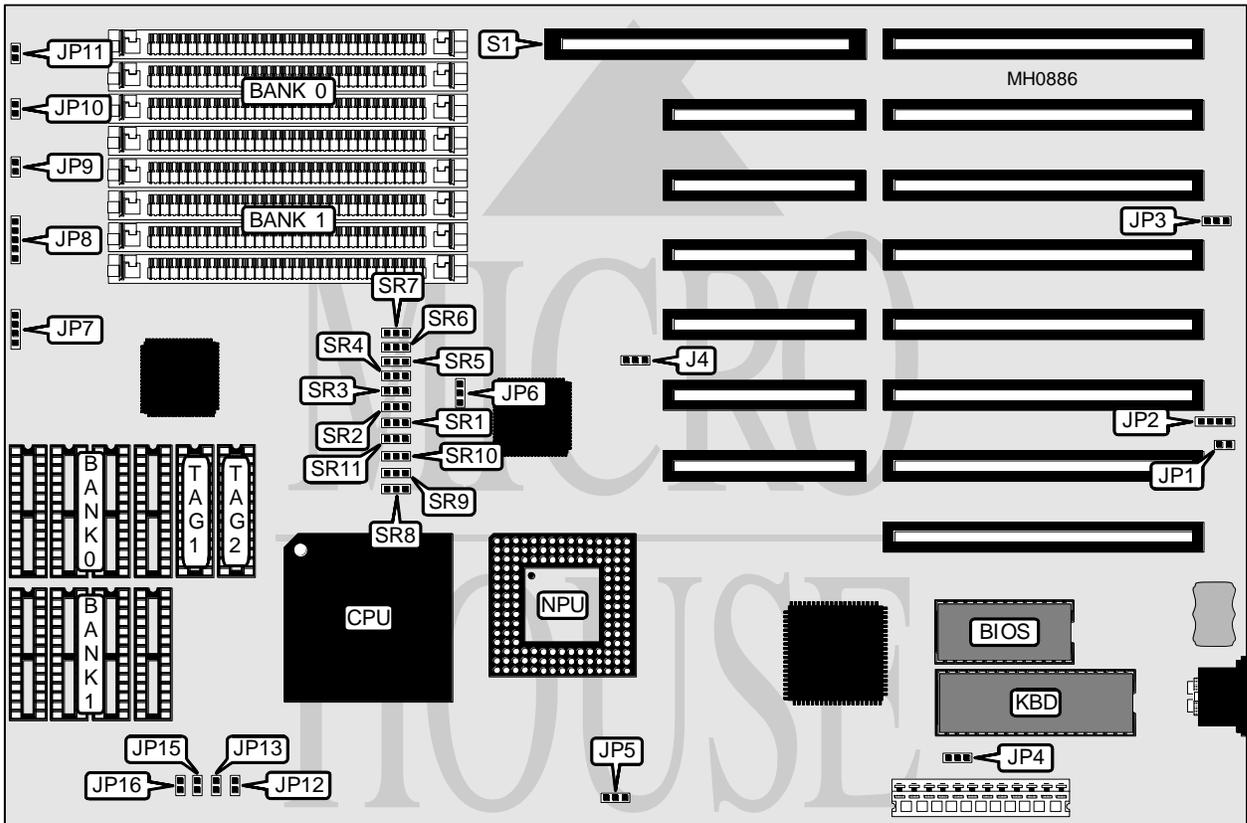


# AUSTIN COMPUTER SYSTEMS

## 486/DX2 331/501(TC-4)

<b>Processor</b>	80486DX/80486DX2
<b>Processor Speed</b>	25/33/50(internal)/66(internal)MHz
<b>Chip Set</b>	C & T
<b>Max. Onboard DRAM</b>	32MB
<b>Cache</b>	64/128/256KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	32-bit external memory card slot
<b>NPU Options</b>	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	JP7	Turbo LED	JP10
Power LED & keylock	JP8	Turbo switch	JP11
Reset switch	JP9	External battery	JP2

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# AUSTIN COMPUTER SYSTEMS

## 486/DX2 331/501(TC-4)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	J3	N/A
í Factory configured - do not alter	J4	N/A
í CMOS memory normal operation	JP3	pins 1 & 2 closed
CMOS memory clear	JP3	pins 2 & 3 closed
í Monitor type select color	JP4	pins 1 & 2 closed
Monitor type select monochrome	JP4	pins 2 & 3 closed
í Factory configured - do not alter	JP5	N/A
í DRAM type select identical	JP6	pins 1 & 2 closed
DRAM type select mixed	JP6	pins 2 & 3 closed
í Factory configured - do not alter	JP12	N/A
í Factory configured - do not alter	JP13	N/A
í Factory configured - do not alter	JP15	N/A
í Factory configured - do not alter	JP16	N/A

Note : The location of J3 is unidentified.

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG 1	TAG 2
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 32K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION											
Size	SR1	SR2	SR3	SR4	SR5	SR6	SR7	SR8	SR9	SR10	SR11
64KB	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
128KB	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2
256KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

BATTERY CONFIGURATION		
Type	JP1	JP2
Internal	Closed	Open
External	Open	Closed