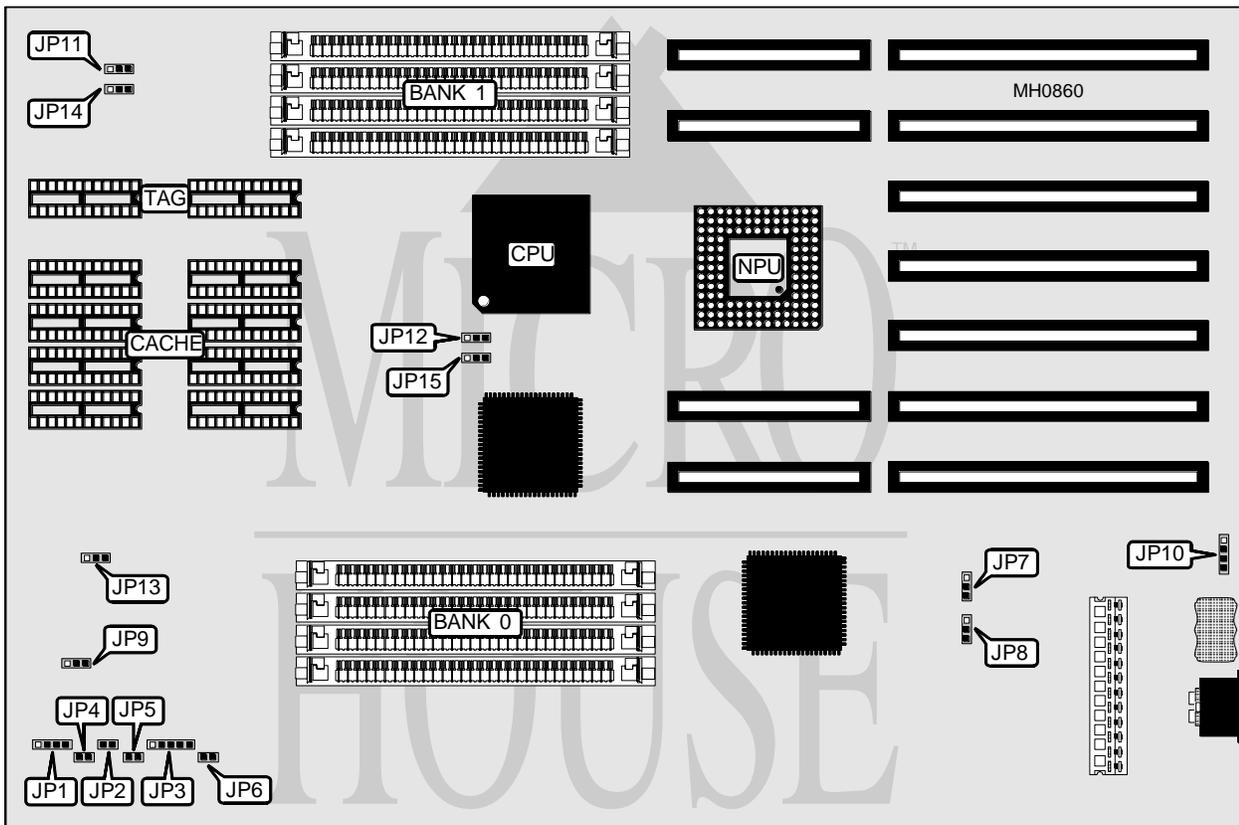


# EMPAC RESEARCH INTERNATIONAL ERI MAGNUM CACHE 386 HT

<b>Processor</b>	80386DX
<b>Processor Speed</b>	40MHz
<b>Chip Set</b>	C & T/OPTI
<b>Max. Onboard DRAM</b>	32MB
<b>Cache</b>	64/256/1024KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	330mm x 221mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	JP1	Turbo switch	JP4
Reset switch	JP2	Turbo LED	JP5
Power LED & keylock	JP3	External battery	JP10

Continued on next page . . .

# EMPAC RESEARCH INTERNATIONAL

## ERI MAGNUM CACHE 386 HT

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP6	Closed
Monitor type select monochrome	JP6	Open
í Factory configured - do not alter	JP9	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION		
Size	Bank 0	TAG
64KB	(8) 64K x 1	(1) 64K x 1
256KB	(8) 64K x 4	(1) 64K x 4
1024KB	(8) 256K x 4	(1) 256K x 4

CACHE JUMPER CONFIGURATION							
Size	JP7	JP8	JP11	JP12	JP13	JP14	JP15
64KB	1 & 2	1 & 2	1 & 2	Open	2 & 3	2 & 3	1 & 2
256KB	2 & 3	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	2 & 3
1024KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.