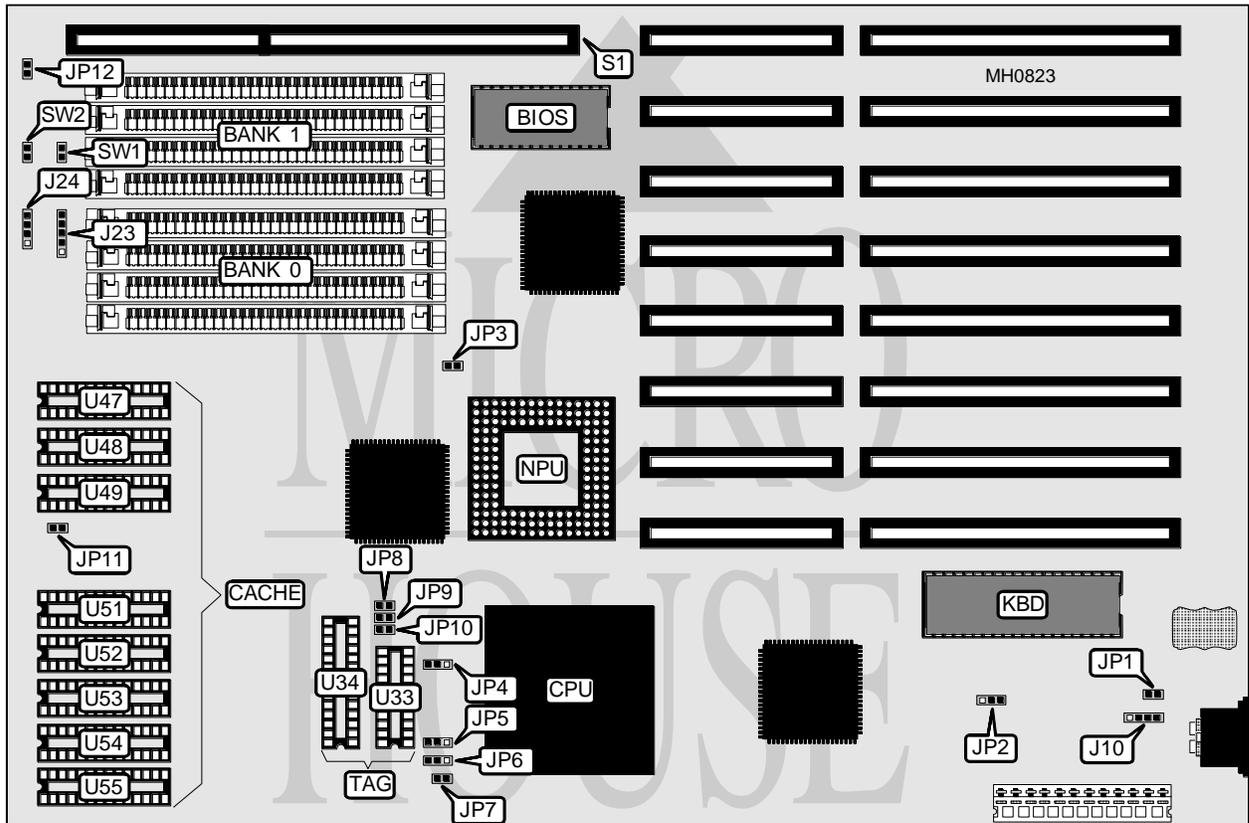


SILICON VALLEY COMPUTER, INC.

486WB

Processor	80486DX
Processor Speed	33/50MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	64/256KB
BIOS	C & T
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card slot
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J10	32-bit external memory card	S1
Power LED & keylock	J23	Reset switch	SW1
Speaker	J24	Turbo switch	SW2
Turbo LED	JP12		

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SILICON VALLEY COMPUTER, INC.**4 8 6 W B**

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select CGA	JP1	Closed
Monitor type select monochrome, EGA, or VGA	JP1	Open
í CMOS memory normal operation	JP2	pins 2 & 3 closed
CMOS memory clear	JP2	pins 1 & 2 closed
í Bus speed select ATCLK/4	JP3	Closed
Bus speed select ATCLK/6	JP3	Open
í Factory configured - do not alter	JP4	N/A
í Factory configured - do not alter	JP5	N/A
í Factory configured - do not alter	JP6	N/A
í Factory configured - do not alter	JP7	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	TAG (U33)	TAG (U34)
64KB	(8) 8K x 8	(1) 64K x 1	(1) 8K x 8
256KB	(8) 8K x 8	(1) 64K x 1	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP8	JP9	JP10	JP11
64KB	Open	Open	Open	Open
256KB	Closed	Closed	Closed	Closed