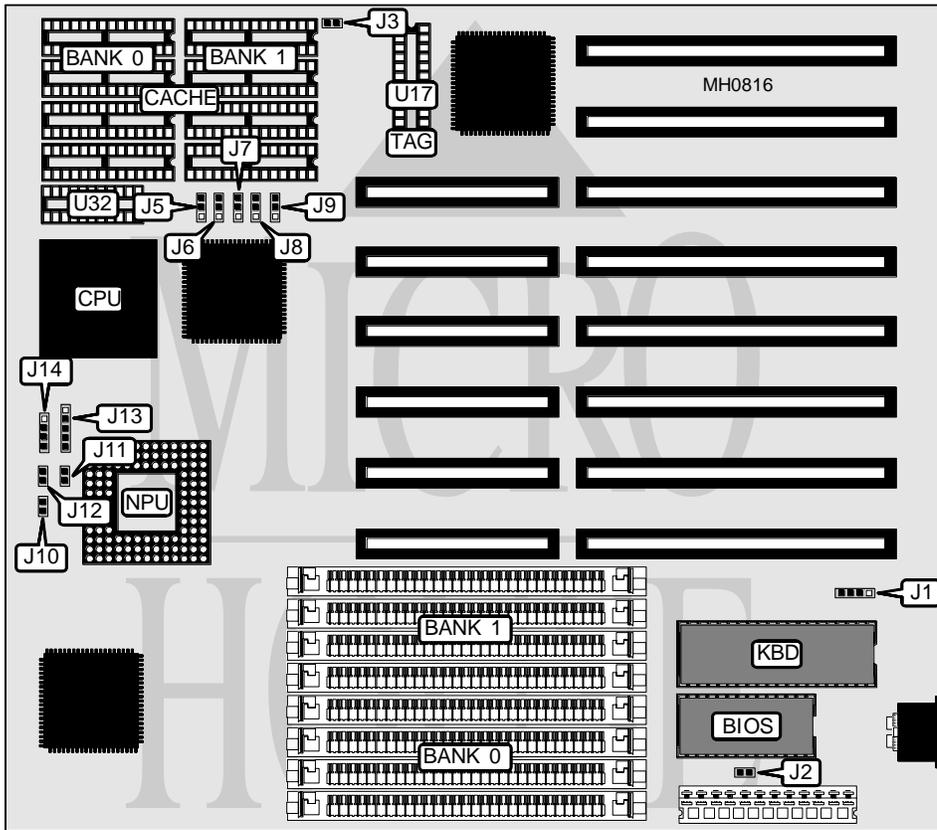


# ILON USA, INC.

## M - 3 2 1

**Processor** 80386DX  
**Processor Speed** 25/33/40MHz  
**Chip Set** PC CHIPS  
**Max. Onboard DRAM** 32MB  
**Cache** 32/64/128/256KB  
**BIOS** AMI  
**Dimensions** 250mm x 220mm  
**I/O Options** None  
**NPU Options** 80387/3167



### CONNECTIONS

Purpose	Location	Purpose	Location
External battery	J1	Turbo switch	J12
Reset switch	J10	Power LED & keylock	J13
Turbo LED	J11	Speaker	J14

Continued on next page . . .

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## M - 3 2 1

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	J2	Closed
Monitor type select monochrome	J2	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1 M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(1) 8K x 8
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

Note: Installing a 16K x 4 SRAM chip in U32 will enable write-back mode.

CACHE JUMPER CONFIGURATION						
Size	J3	J5	J6	J7	J8	J9
32KB	Open	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
64KB	Open	1 & 2	1 & 2	2 & 3	2 & 3	1 & 2
128KB	Closed	1 & 2	2 & 3	2 & 3	2 & 3	1 & 2
256KB	Closed	2 & 3	2 & 3	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.