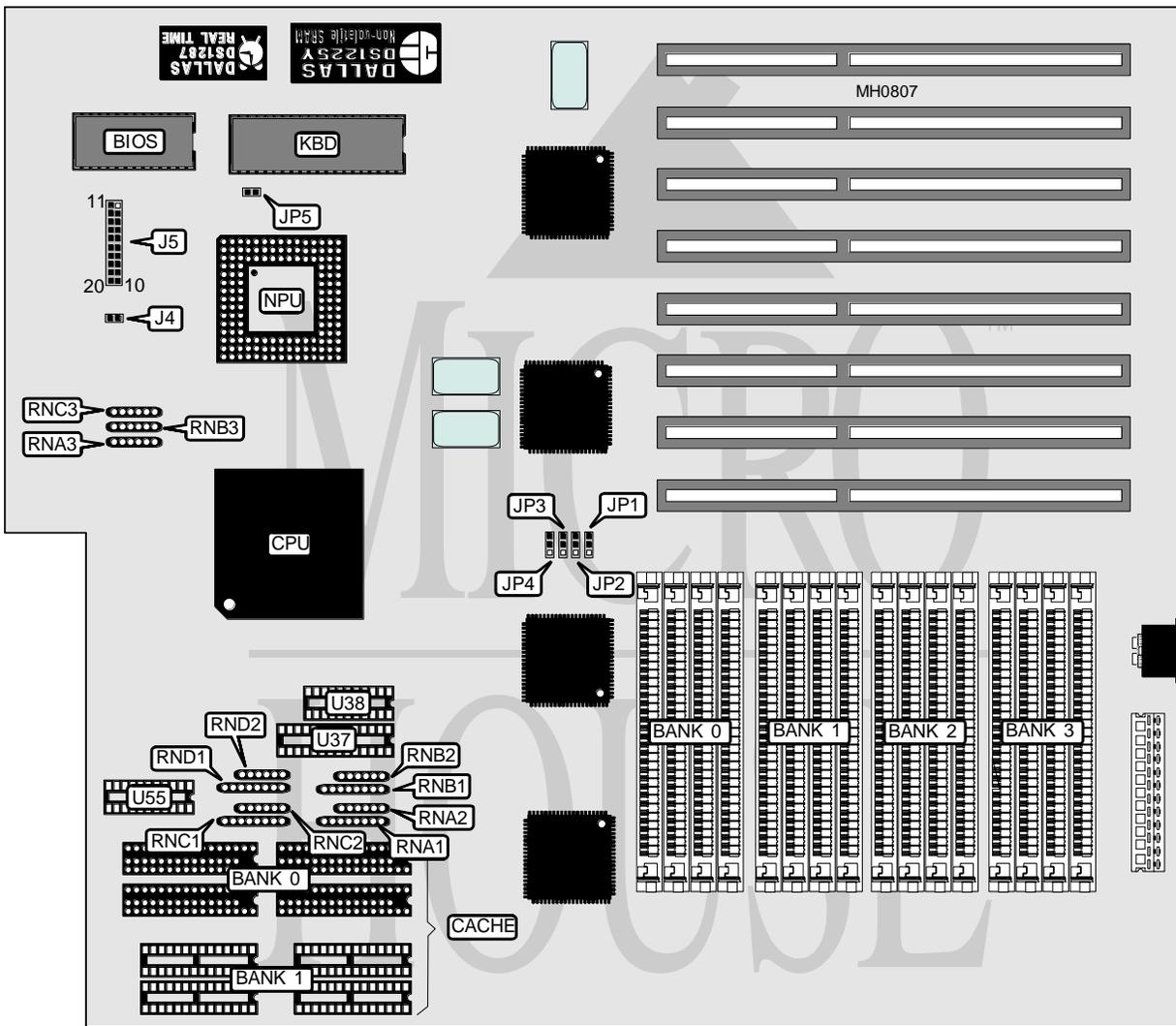


TMC RESEARCH CORPORATION

MAT48PX (EISA)

Processor	80486SX/80487SX/80486DX
Processor Speed	16/20/25/33/50MHz
Chip Set	OPTI
Max. Onboard DRAM	256MB
Cache	64/128/256/512KB
BIOS	AMI
Dimensions	350mm x 304mm
I/O Options	None
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
IDE LED out	J4	Reset switch	J5 pins 9 & 19
Speaker	J5 pins 1 - 4	IDE LED in	J5 pins 10 & 20
Turbo switch	J5 pins 7 & 17	Power LED & keylock	J5 pins 11 - 15
Turbo LED	J5 pins 8 & 18		

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MAT48PX (EISA)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP5	Closed
Monitor type select monochrome	JP5	Open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
4MB	(4) 1M x 9	NONE	NONE	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9			
16MB	(4) 4M x 9	NONE	NONE	NONE
20MB	(4) 4M x 9	(4) 1M x 9	NONE	NONE
24MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	NONE
28MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
36MB	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9	NONE
40MB	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE
52MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9
64MB	(4) 4M x 9			
64MB	(4) 16M x 9	NONE	NONE	NONE
128MB	(4) 16M x 9	(4) 16M x 9	NONE	NONE
192MB	(4) 16M x 9	(4) 16M x 9	(4) 16M x 9	NONE
256MB	(4) 16M x 9			

CACHE CONFIGURATION					
Size	Bank 0	Bank 1	TAG (U37)	TAG (U38)	TAG (U55)
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	(1) 16K x 1	(1) 16K x 1
128KB	(4) 32K x 8	NONE	(1) 8K x 8	(1) 16K x 1	(1) 16K x 1
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	(1) 16K x 1	(1) 16K x 1
512KB	(4) 128K x 8	NONE	(1) 32K x 8	(1) 64K x 1	(1) 64K x 1

Notes: If DRAM is ≤ 32 MB, all cache sizes may be installed; if DRAM is > 32 MB and ≤ 64 MB
128, 256, or 512KB cache must be installed; if DRAM is > 64 MB and ≤ 128 MB
256 or 512KB cache must be installed; if DRAM is > 128 MB, 512KB cache must be installed.

CACHE JUMPER CONFIGURATION				
Size	RNA1 & RNA2	RNB1 & RNB2	RNC1 & RNC2	RND1 & RND2
64KB	Installed	Not installed	Not installed	Not installed
128KB	Not installed	Installed	Not installed	Not installed
256KB	Not installed	Not installed	Installed	Not installed
512KB	Not installed	Not installed	Not installed	Installed

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TMC RESEARCH CORPORATION
MAT48PX (EISA)

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CPU TYPE CONFIGURATION				
Type	JP1	JP2	JP3	JP4
80486SX-16MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
80486SX-20MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
80486SX-25MHz	pins 1 & 2	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
80487SX-16MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
80487SX-20MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
80487SX-25MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
80486DX-25MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
80486DX-33MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
80486DX-50MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed

CPU TYPE CONFIGURATION			
Type	RNA3	RNB3	RNC3
80486SX-16MHz	Not installed	Not installed	Installed
80486SX-20MHz	Not installed	Not installed	Installed
80486SX-25MHz	Not installed	Not installed	Installed
80487SX-16MHz	Not installed	Not installed	Not installed
80487SX-20MHz	Not installed	Installed	Not installed
80487SX-25MHz	Not installed	Installed	Not installed
80486DX-25MHz	Installed	Not installed	Not installed
80486DX-33MHz	Installed	Not installed	Not installed
80486DX-50MHz	Installed	Not installed	Not installed