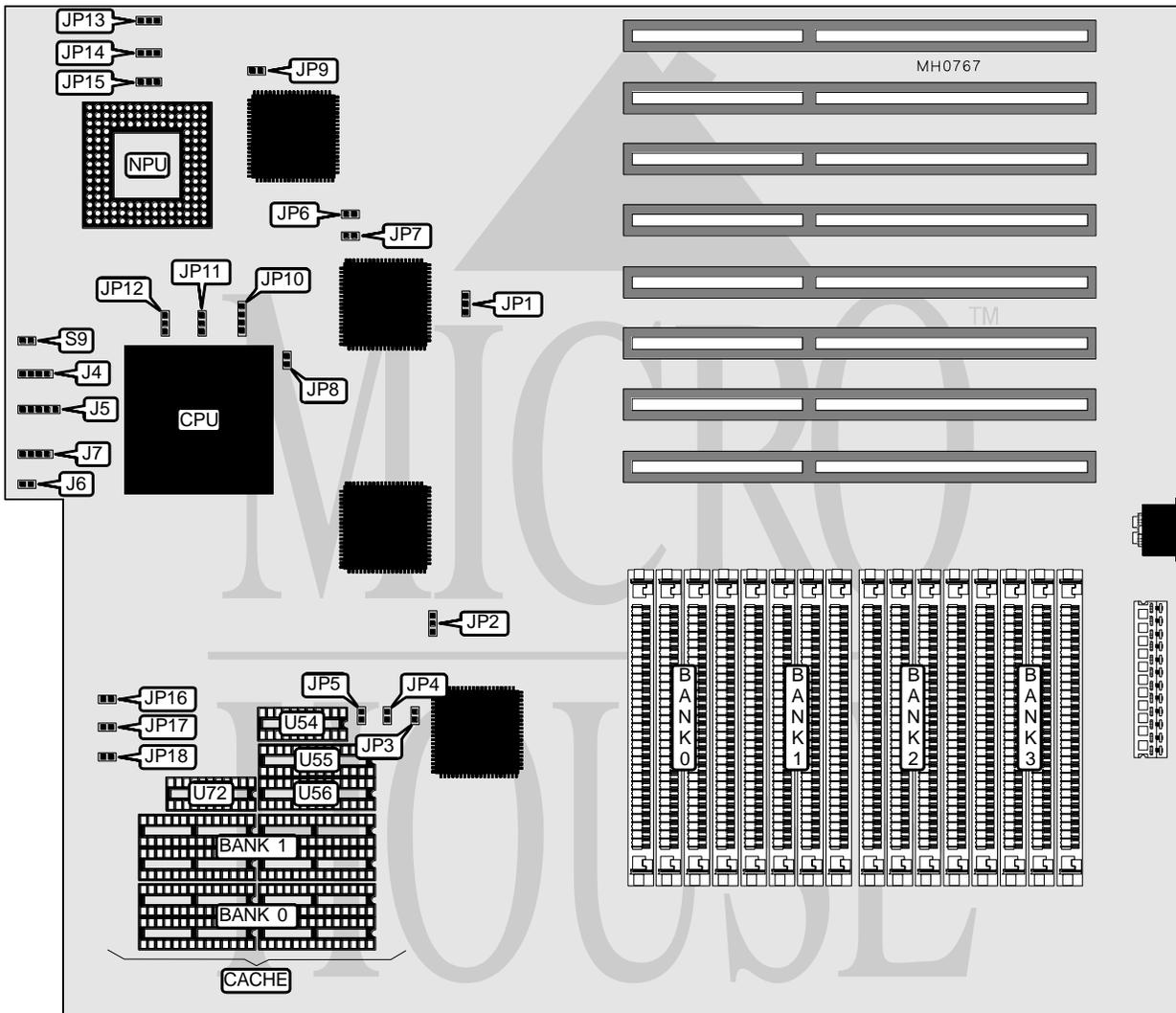


INFOMATIC POWER SYSTEMS CORPORATION

486 EISA FULL SIZE

Processor	80486SX/80487SX/80486DX/80486DX2
Processor Speed	20/25/33/50/66(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	256MB
Cache	64/128/256/512KB
BIOS	AMI
Dimensions	355mm x 304mm
I/O Options	None
NPU Options	4167



CONNECTIONS

Purpose	Location	Purpose	Location
Speaker	J4	Turbo LED	J7
Power LED & keylock	J5	Reset switch	S9
Turbo switch	J6		

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INFOMATIC POWER SYSTEMS CORPORATION

486 EISA FULL SIZE

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í External BCLK buffering 80486DX (33/50MHz)	JP1	pins 1 & 2 closed
External BCLK buffering 80486SX/80487SX (20/25MHz)	JP1	pins 2 & 3 closed
í Cache bank select single (128/512KB)	JP2	pins 1 & 2 closed
Cache bank select double (64/256KB)	JP2	pins 2 & 3 closed
í Snooping/Flush 20MHz CPU	JP8	Closed
Snooping/Flush > 20MHz CPU	JP8	Open
í Monitor type select color	JP9	Closed
Monitor type select monochrome	JP9	Open
í CPU clock mode select single frequency	JP12	pins 1 & 2 closed
CPU clock mode select double frequency	JP12	pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
4MB	(4) 1M x 9	NONE	NONE	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9			
16MB	(4) 4M x 9	NONE	NONE	NONE
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE
24MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	NONE
28MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
36MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	NONE
40MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE
52MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9
64MB	(4) 4M x 9			
64MB	(4) 16M x 9	NONE	NONE	NONE
128MB	(4) 16M x 9	(4) 16M x 9	NONE	NONE
192MB	(4) 16M x 9	(4) 16M x 9	(4) 16M x 9	NONE
256MB	(4) 16M x 9			

CPU TYPE CONFIGURATION		
Type	JP10	JP11
80486SX	pins 1 & 2 & 3 & 4 closed	pins 2 & 3 closed
80487SX	pins 1 & 2 & 3 & 4 closed	pins 1 & 2 closed
80486DX	pins 2 & 3 closed	Open

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INFOMATIC POWER SYSTEMS CORPORATION

486 EISA FULL SIZE

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EBC SPEED CONFIGURATION

Speed	JP6	JP7
20MHz	Open	Open
25MHz	Open	Open
33MHz	Open	Closed
40MHz	Closed	Open
50MHz	Closed	Closed

CACHE CONFIGURATION

Size	Bank 0	Bank 1
64KB	(4) 8K x 8	(4) 8K x 8
128KB	(4) 32K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8
512KB	(4) 128K x 8	NONE

Note: In order to configure your cache for 512K, your board needs to have been specifically ordered. The difference will be that bank 1 will be made up of (4) 32 pin DIP sockets, instead of (4) 28 pin DIP sockets.

CACHE TAG CHIP CONFIGURATION

Size	JP3	JP4	JP5	U55	U56
64KB	Open	Open	Open	(1) 8K x 8	N/A
128KB	Closed	Open	Open	(1) 8K x 8	N/A
256KB	Closed	Closed	Open	(1) 8K x 8	(1) 8K x 8
512KB	Closed	Closed	Closed	(1) 32K x 8	N/A

CACHE DIRTY BIT CHIP CONFIGURATION

Size	JP16	JP17	JP18	U72 & U54
64KB	open	open	open	(2) 16K x 1 or (2) 64K x 1
128KB	closed	open	open	(2) 16K x 1 or (2) 64K x 1
256KB	closed	closed	open	(2) 16K x 1 or (2) 64K x 1
512KB	closed	closed	closed	(2) 64K x 1

CLOCK CONFIGURATION

Type	JP13	JP14	JP15
DS1287/1287A	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
DS1387/1488	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed