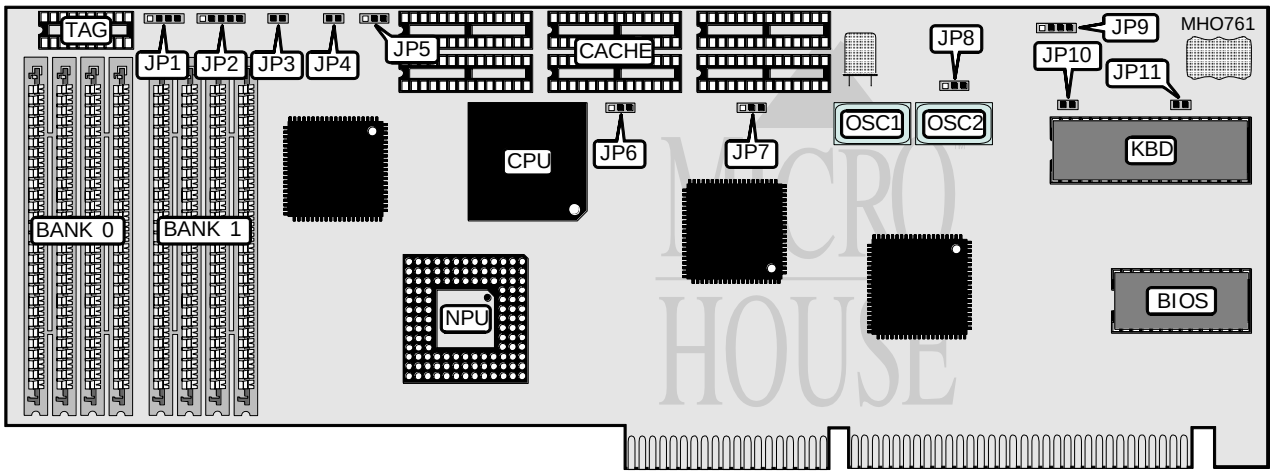
MULTI MICRO SYSTEMS, INC.
N W 3 8 6 D X C

Processor	80386DX
Processor Speed	33/40MHz
Chip Set	C & T
Max. Onboard DRAM	32MB
SRAM Cache	32/128KB
BIOS	C & T
Dimensions	334mm x 122mm
I/O Options	None
NPU Options	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	JP1	Turbo LED	JP5
Power LED & keylock	JP2	External battery	JP9
Reset switch	JP3	Keyboard clock & data	JP10
Turbo switch	JP4		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP8	pins 1 & 2 closed
CMOS memory clear	JP8	pins 2 & 3 closed
í Monitor type select color	JP11	closed
Monitor type select monochrome	JP11	open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 258K x 9	NONE
2MB	(4) 258K x 9	(4) 258K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

SRAM JUMPER CONFIGURATION				
Jumper	Cache 32K x 8	Cache 8K x 8	TAG 32K x 8	TAG 8K x 8
JP6	pins 1 & 2 closed	pins 2 & 3 closed	N/A	N/A
JP7	N/A	N/A	pins 1 & 2 closed	pins 2 & 3 closed

SRAM CONFIGURATION		
Size	Cache SRAM	TAG
32KB	(6) 8K x 8	(1) 8K x 8
128KB	(6) 32K x 8	(1) 32K x 8

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