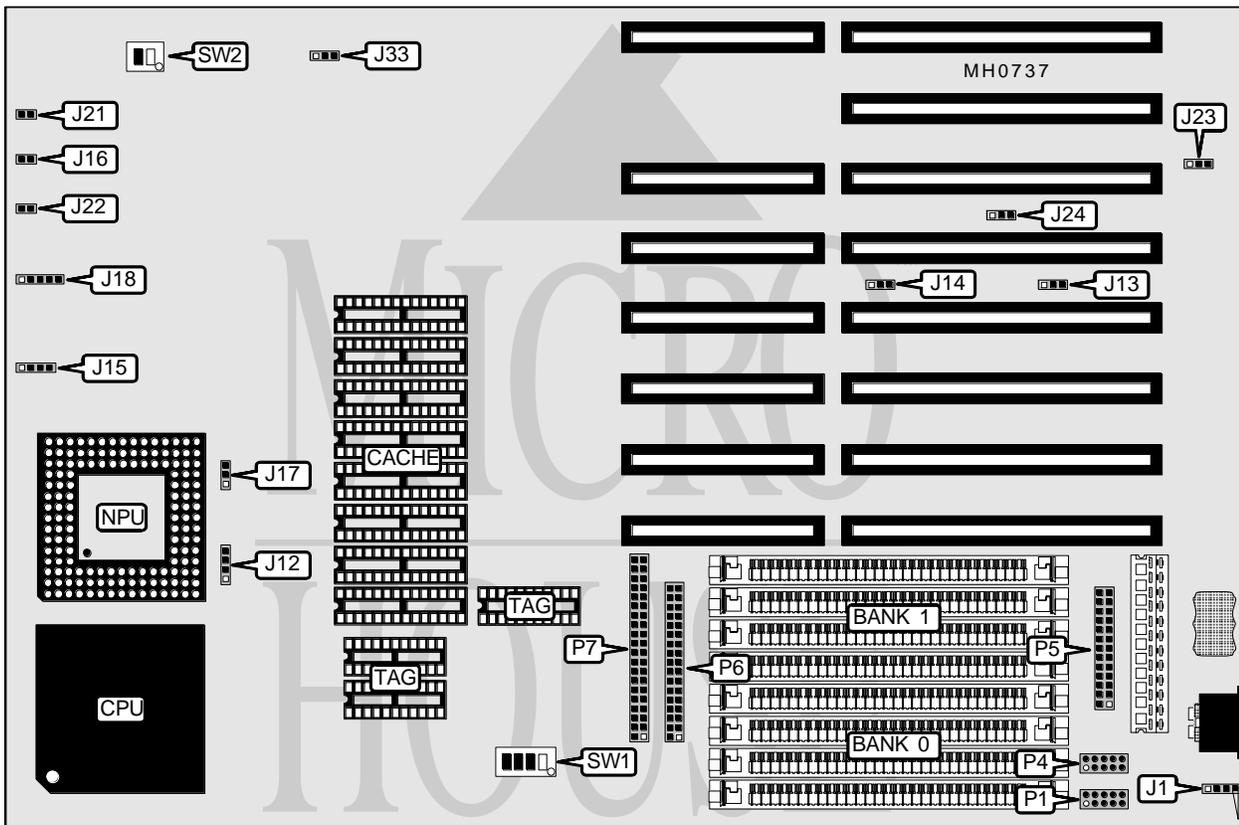


AMERICAN MEGATRENDS, INC. SERIES 50 SUPER VOYAGER

Processor	80486SX/80486DX/ODP486SX/80486DX2
Processor Speed	20/25/33/50/50(internal)/66(internal)MHz
Chip Set	AMI
Max. Onboard DRAM	32MB
Cache	64/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	Floppy drive interface, IDE interface, parallel port, serial ports (2)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Serial port 1	P1
Speaker	J15	Serial port 2	P4
Turbo LED	J16	Parallel port	P5
Power LED & keylock	J18	Floppy drive interface	P6
Turbo switch	J21	IDE interface	P7
Reset switch	J22		

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AMERICAN MEGATRENDS, INC.

SERIES 50 SUPER VOYAGER

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í CPU select type 80486DX or 80486DX2	J12	1 & 2,3 & 4 closed
CPU select type 80486SX	J12	pins 2 & 3 closed
í Serial port 1 interrupt select IRQ3	J13	pins 1 & 2 closed
Serial port 1 interrupt select IRQ4	J13	pins 2 & 3 closed
í Serial port 2 interrupt select IRQ3	J14	pins 2 & 3 closed
Serial port 2 interrupt select IRQ4	J14	pins 1 & 2 closed
í Power good signal detect from power supply	J23	pins 2 & 3 closed
Power good signal detect from board	J23	pins 1 & 2 closed
í Parallel port select interrupt IRQ7	J24	pins 2 & 3 closed
Parallel port select interrupt IRQ5	J24	pins 1 & 2 closed
í CMOS memory normal operation	J33	pins 1 & 2 closed
CMOS memory clear	J33	pins 2 & 3 closed
í Monitor type select monochrome	SW2/1	Off
Monitor type select color	SW2/1	On
í Manufacturing diagnostics	SW2/2	Off
Note: SW2/switch 2 is factory configured - do not alter		

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION		
Size	Bank 0	TAG
64KB	(8) 8K x 8	(3) 16K x 4
256KB	(8) 32 K x 8	(3) 16K x 4

CACHE SWITCH CONFIGURATION				
Size	SW1/1	SW1/2	SW1/3	SW1/4
64KB	Off	Off	Off	Off
256KB	On	On	On	On

CPU SPEED CONFIGURATION	
Size	J17
20MHz	pins 2 & 3 closed
25MHz	pins 1 & 2 closed
33MHz	pins 1 & 2 closed
50iMHz	pins 1 & 2 closed
50MHz	pins 1 & 2 closed
66iMHz	pins 1 & 2 closed