COMPAQ COMPUTER CORPORATION
D E S K P R O   2 8 6   P O R T A B L E

Processor	80286
Processor Speed	8MHz
Chip Set	ETEQ
Max. Onboard DRAM	640KB
Cache	None
BIOS	Compaq
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80287
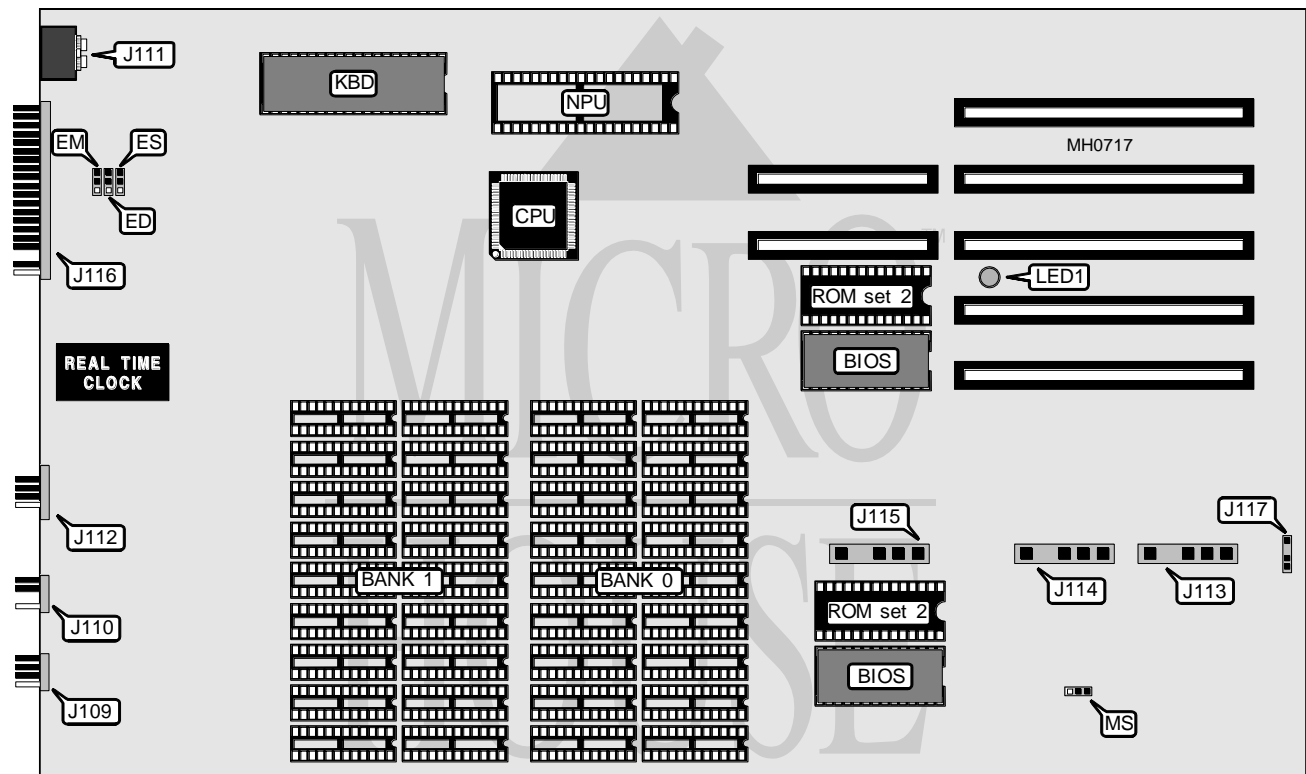
CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J109	Floppy drive power	J114
External battery	J110	Floppy drive power	J115
Proprietary keyboard connector	J111	Main power	J116
Monitor power	J112	Keylock	J117
Fixed drive power	J113	Onboard power LED	LED1

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DESKPRO 286 PORTABLE

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select Compaq graphics/color	ED	pins 2 & 3 closed
Monitor type select monochrome other than Compaq	ED	pins 1 & 2 closed
í Factory configured - do not alter	EM	pins 1 & 2 closed
í CPU speed select switchable at keyboard	ES	pins 1 & 2 closed
CPU speed select forced slow	ES	pins 2 & 3 closed

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	Jumper MS
256KB	(18) 4164	(18) 4164	pins 1 & 2 closed
512KB	NONE	(18) 41256	pins 1 & 2 closed
640KB	(18) 4164	(18) 41256	pins 2 & 3 closed

Note: Boards that come from the manufacturer with 640KB installed have a different memory PAL chip installed than those that come with 256KB. This chip allows the user to disable bank 0 and use only bank 1, thus enabling the system to use older software that will not work on systems with more than 512KB.

BIOS (ROM SET 1) JUMPER CONFIGURATION			
ROM type	Size	Speed	Jumper block E
Static	8K x 8	250ns	pins 10 & 11, 13 & 14, and 16 & 17 closed
í Static	16K x 8	250ns	pins 10 & 11, 14 & 15, and 16 & 17 closed
Static	32K x 8	250ns	pins 11 & 12, 14 & 15, and 16 & 17 closed
Dynamic	8K x 8	150ns	pins 10 & 11, 13 & 14, and 17 & 18 closed
Dynamic	16K x 8	150ns	pins 10 & 11, 14 & 15, and 17 & 18 closed
Dynamic	32K x 8	150ns	pins 11 & 12, 14 & 15, and 17 & 18 closed

Note: Settings are accomplished by cutting the conductors on the bottom of the board and re-soldering the indicated locations

WARNING: Modifying the factory configurations will void the manufacturers warranty!

ROM SET 2 JUMPER CONFIGURATION			
ROM type	Size	Speed	Jumper block E
Static	8K x 8	250ns	pins 4 & 5, 7 & 8, and 19 & 20 closed
Static	16K x 8	250ns	pins 4 & 5, 8 & 9, and 19 & 20 closed
Static	32K x 8	250ns	pins 5 & 6, 8 & 9, and 19 & 20 closed
Dynamic	8K x 8	150ns	pins 4 & 5, 7 & 8, and 20 & 21 closed
Dynamic	16K x 8	150ns	pins 4 & 5, 8 & 9, and 20 & 21 closed
í Dynamic	32K x 8	150ns	pins 5 & 6, 8 & 9, and 20 & 21 closed

Note: Settings are accomplished by cutting the conductors on the bottom of the board and re-soldering the connections at the indicated locations

WARNING: Modifying the factory configurations will void the manufacturers warranty!