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S C A T   T U R B O   2 8 6

Processor	80286
Processor Speed	12/16/20MHz
Chip Set	C & T
Max. Onboard DRAM	8MB
Cache	None
BIOS	C & T
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80287
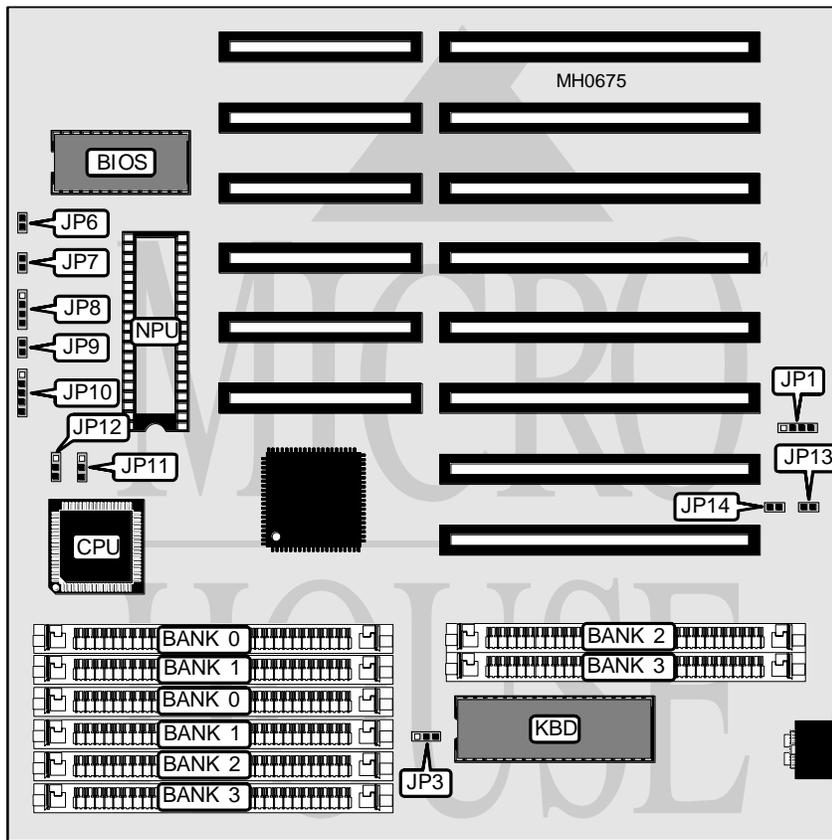
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP1	Speaker	JP8
Turbo switch	JP6	Reset switch	JP9
Turbo LED	JP7	Power LED & keylock	JP10

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SCAT TURBO 286

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP3	pins 1 & 2 closed
Monitor type select monochrome	JP3	pins 2 & 3 closed
í NPU speed select synchronous with CPU	JP11 & JP12	pins 1 & 2 closed
NPU speed select asynchronous with CPU	JP11 & JP12	pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(2) 256K x 9	NONE	NONE	NONE
640KB	(2) 256K x 9	(2) 64K x 9	NONE	NONE
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
1.5MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	NONE
2MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE	NONE	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	NONE
8MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9

CMOS MEMORY CONFIGURATION		
Setting	JP13	JP14
CMOS memory normal operation	Open	Closed
CMOS memory clear	Closed	Open