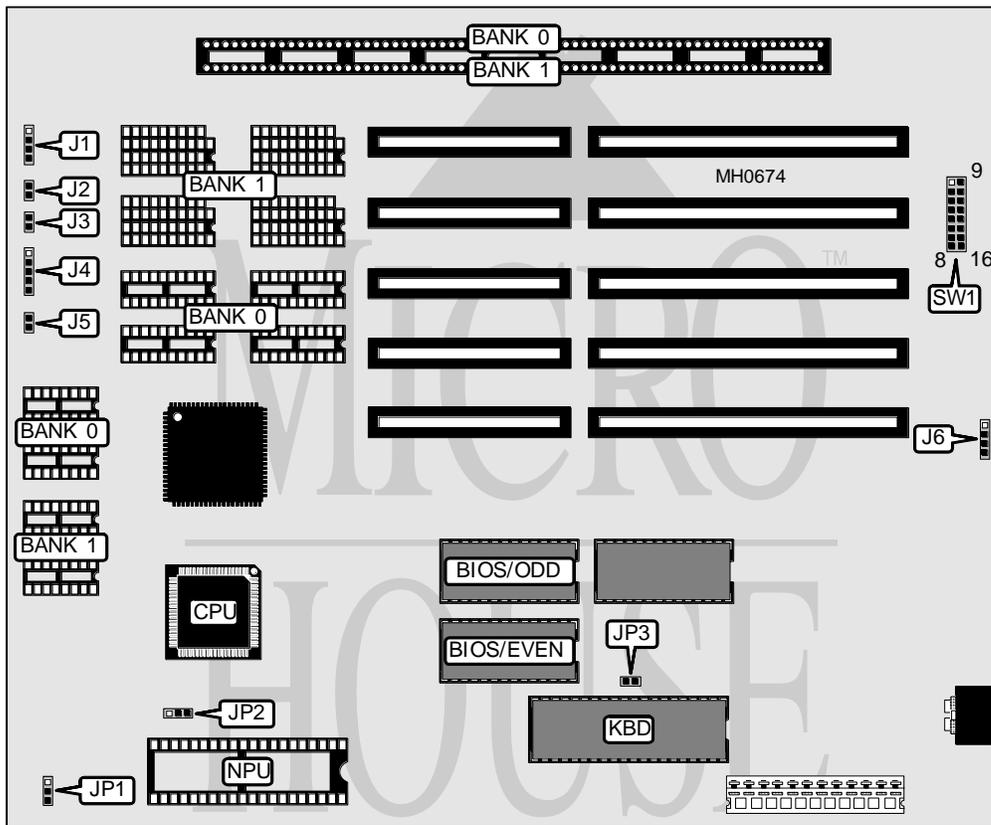


UNIDENTIFIED

G 2 - 2 8 6

Processor 80286
Processor Speed 6/8/12/16MHz
Chip Set Unidentified
Max. Onboard DRAM 4MB
Cache None
BIOS AMI/Award/Phoenix
Dimensions 220mm x 220mm
I/O Options None
NPU Options 80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J1	Power LED & keylock	J4
Turbo LED	J2	Reset switch	J5
Turbo switch	J3	External battery	J6

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery type select internal	J6	pins 2 & 3 closed
Battery type select external	J6	Closed
í Monitor type select color	JP4	Closed
Monitor type select monochrome	JP4	Open

DRAM CONFIGURATION (DIP)		
Size	Bank 0	Bank 1
512KB	(4) 44256 (2) 41256	NONE
640KB	(4) 44256 (2) 4164	(4) 4464 (2) 41256
1MB	(4) 44256 (2) 41256	(4) 44256 (2) 41256

DRAM JUMPER CONFIGURATION (DIP)	
Size	SW1
512KB	pins 7 & 15 closed
640KB	pins 6 & 14, 7 & 15 closed
1MB	pins 6 & 14 closed

DRAM CONFIGURATION (SIPP)		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
2.5MB	(2) 256K x 9	(2) 1M x 9
4MB	(2) 1M x 9	(2) 1M x 9

Note: SIPP and DIP memory may not be used concurrently.

DRAM JUMPER CONFIGURATION (SIPP)	
Size	SW1
512KB	pins 7 & 15 closed
1MB	pins 6 & 14, 7 & 15 closed
2MB	pins 5 & 13, 7 & 15 closed
2.5MB	pins 5 & 13 closed
4MB	pins 5 & 13, 6 & 14 closed

NPU CONFIGURATION		
Setting	JP1	JP2
Synchronous	pins 2 & 3 closed	pins 1 & 2 closed
Asynchronous	pins 1 & 2 closed	pins 2 & 3 closed