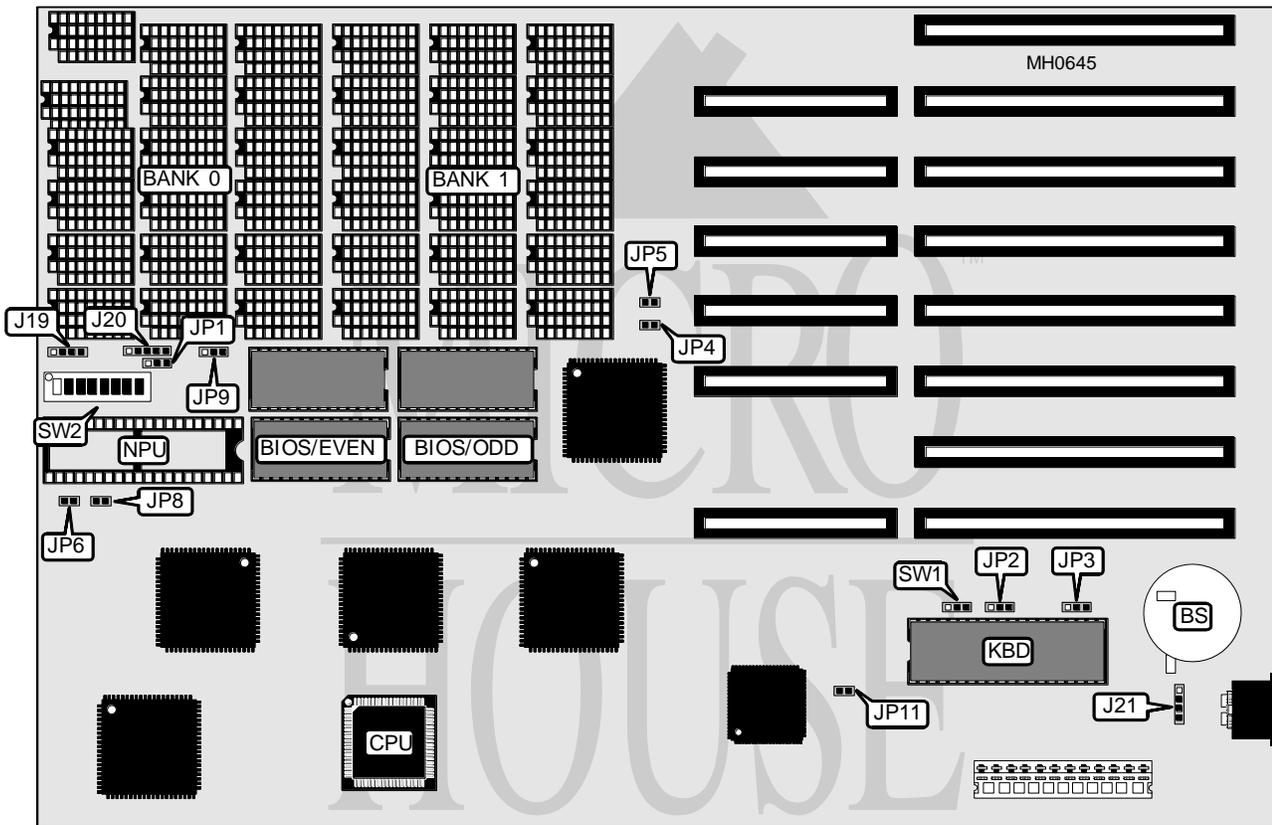


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SUNTAC 286-12

Processor	80286
Processor Speed	12MHz
Chip Set	Suntac
Max. Onboard DRAM	4MB
Cache	None
BIOS	AMI/Award/ERSO/Phoenix
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J19	Turbo switch	JP4
Power LED & keylock	J20	Turbo LED	JP5
External battery	J21	Reset switch	JP6
External EMS selection	JP2		

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Internal battery switch	BS	N/A
í Factory configured - do not alter	J18	N/A
í Keyboard BIOS type select Award	JP7	pins 2 & 3 closed
Keyboard BIOS type select Phoenix	JP7	pins 1 & 2 closed
í Monitor type select color	SW1	pins 2 & 3 closed
Monitor type select monochrome	SW1	pins 1 & 2 closed
EMS port address select 0E8H- 0EFH	SW2/4	Off
EMS port address select 098H- 09FH	SW2/4	On
Note: The location of JP7 is unidentified.		

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(18) 41256	NONE
640KB	(18) 41256	(18) 4164
1MB	(18) 41256	(18) 41256
640KB+EMS (384KB)	(18) 41256	(18) 41256
2MB	(18) 411000	NONE
640KB+EMS (1408KB)	(18) 411000	NONE
4MB	(18) 411000	(18) 411000
640KB+EMS (3456KB)	(18) 411000	(18) 411000

DRAM SWITCH CONFIGURATION			
Size	SW2/6	SW2/7	SW2/8
512KB	On	On	On
640KB	On	On	Off
1MB	On	Off	On
640KB+EMS (384KB)	On	Off	Off
2MB	Off	On	On
640KB+EMS (1408KB)	Off	On	Off
4MB	Off	Off	On
640KB+EMS (3456KB)	Off	Off	Off

CPU SPEED CONFIGURATION			
Speed	JP1	JP4	SW2/1
CPU speed forced high	pins 2 & 3 closed	N/A	Off
CPU speed forced low	pins 2 & 3 closed	N/A	On
CPU speed forced high	pins 1 & 2 closed	Closed	N/A
CPU speed selectable at keyboard	pins 1 & 2 closed	Open	Closed

I/O WAIT STATE CONFIGURATION			
Wait state	JP1	JP3	SW2/3
4	pins 2 & 3 closed	pins 2 & 3 closed	On
6	pins 2 & 3 closed	pins 2 & 3 closed	Off

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BIOS CONFIGURATION		
Type	JP9	SW2/5
i27256	pins 2 & 3 closed	On
27128	pins 1 & 2 closed	Off

EMS TOGGLE CONFIGURATION		
Setting	JP2	SW2/8
On	Closed	Off
Off	Open	On