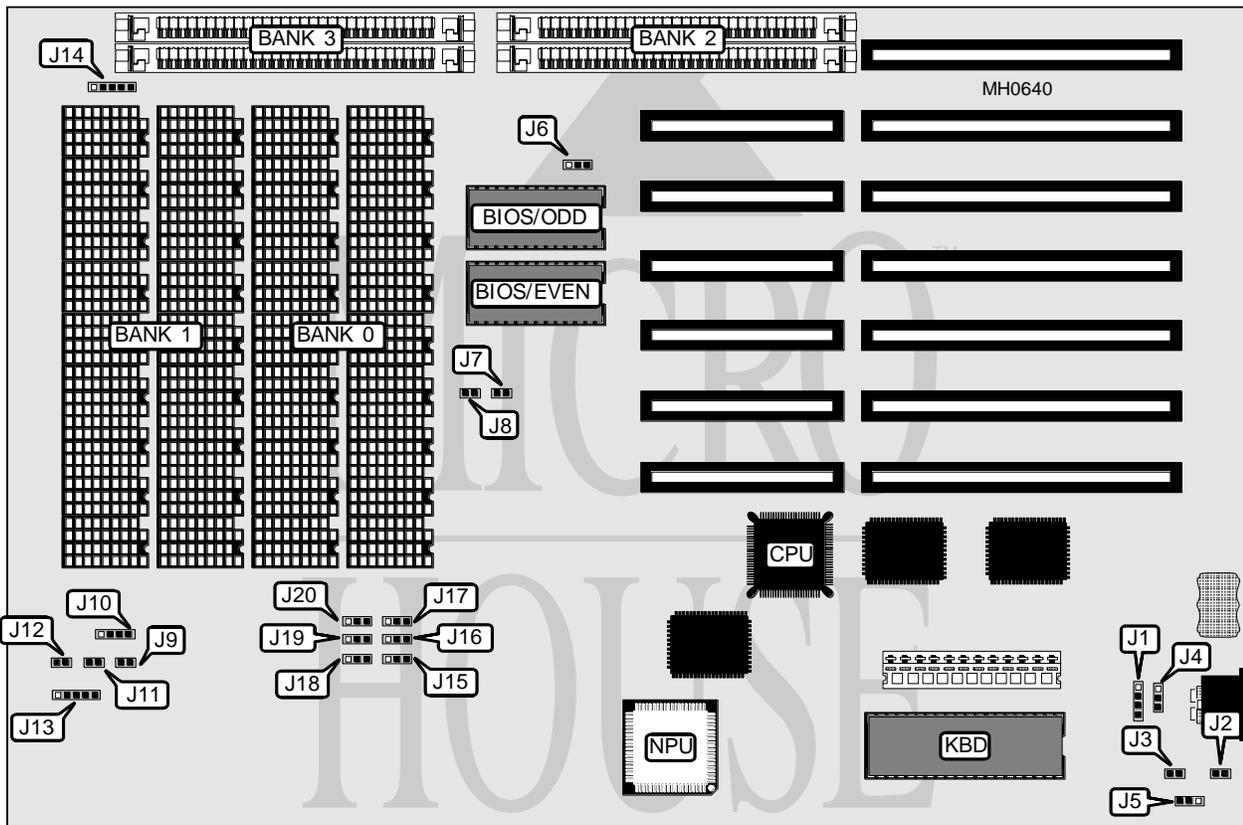


DIAMOND FLOWER, INC.

DFI-386SX

Processor	80386SX
Processor Speed	16MHz
Chip Set	Intel
Max. Onboard DRAM	8MB
Cache	None
BIOS	DFI
Dimensions	330mm x 220mm
I/O Options	None
NPU Options	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo LED	J12
Turbo switch	J2/J11	Power LED & keylock	J13
Reset switch	J3/J9	Auxiliary keyboard	J14
Speaker	J10		

Continued on next page . . .

DIAMOND FLOWER, INC.

DFI-386SX

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery select internal	J4	pins 2 & 3 closed
Battery select external	J4	pins 1 & 2 closed
í Monitor type select monochrome	J5	pins 2 & 3 closed
Monitor type select color	J5	pins 1 & 2 closed
í BIOS type select 27256	J6	pins 2 & 3 closed
BIOS type select 27512	J6	pins 1 & 2 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(18) 41256	NONE	NONE	NONE
512KB	NONE	NONE	(2) 256K x 9	NONE
1MB	(18) 41256	(18) 41256	NONE	NONE
1MB	NONE	NONE	(2) 256K x 9	(2) 256K x 9
2MB	(18) 41256	(18) 41256	(2) 256K x 9	(2) 256K x 9
2MB	(18) 411000	NONE	NONE	NONE
2MB	NONE	NONE	(2) 1M x 9	NONE
4MB	(18) 411000	(18) 411000	NONE	NONE
4MB	NONE	NONE	(2) 1M x 9	(2) 1M x 9
8MB	(18) 411000	(18) 411000	(2) 1M x 9	(2) 1M x 9

DRAM JUMPER CONFIGURATION						
Size	J15	J16	J17	J18	J19	J20
512KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
512KB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
1MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
1MB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
2MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
2MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
2MB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
4MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
4MB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
8MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

DRAM MODE CONFIGURATION				
Mode	Maximum active pages	DRAM type	J7	J8
Fast page 1 (FP1)	4	100ns	Open	Open
Fast page 2 (FP2)	1	100ns	Open	Closed
Slow page 1 (SP1)	4	100ns	Closed	Open
Slow page 2 (SP2)	4	120ns	Closed	Closed

Note: In order to use fast page modes, the DRAM chips must meet the following specifications:
tCAS (access time)=35ns, tCAC (pulse width)=35ns, tCP (precharge time)=20ns.