DEICO ELECTRONICS, INC.
P C - S X i

Processor	80386SX
Processor Speed	16/20MHz
Chip Set	ACC/VIA
Max. Onboard DRAM	8MB
Cache	None
BIOS	Quadtel
Dimensions	330mm x 218mm
I/O Options	Parallel port, serial ports (2), floppy drive interface, IDE interface
NPU Options	80387SX
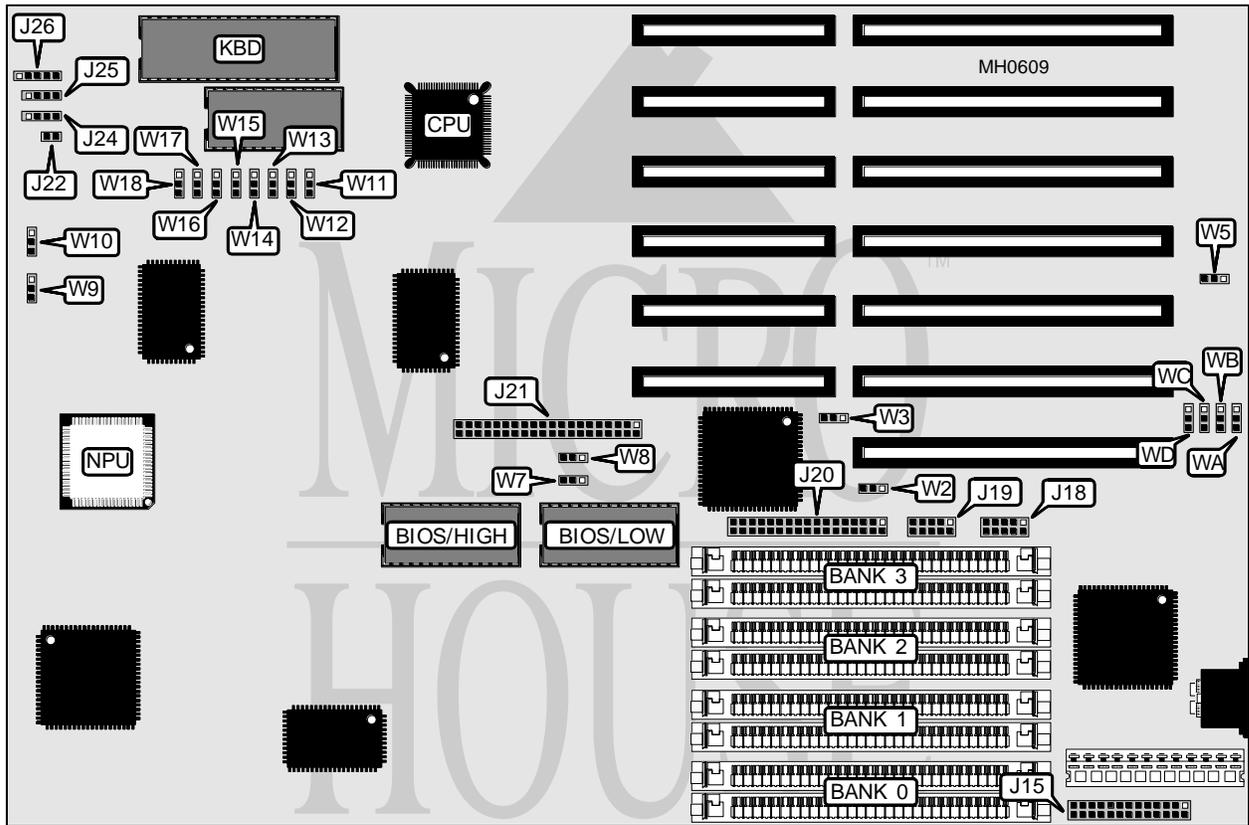
CONNECTIONS			
Purpose	Location	Purpose	Location
Parallel port	J15	IDE interface LED	J22
Serial port 1	J18	Turbo LED	J24 pins 3 & 4
Serial port 2	J19	Reset switch	J24 pins 1 & 2
Floppy drive interface	J20	Speaker	J25
IDE interface	J21	Power LED & keylock	J26

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Serial port 1 enabled	WA	pins 2 & 3 closed
Serial port 1 disabled	WA	pins 1 & 2 closed
í Serial port 2 enabled	WB	pins 2 & 3 closed
Serial port 2 disabled	WB	pins 1 & 2 closed
í Parallel port enabled	WC	pins 2 & 3 closed
Parallel port disabled	WC	pins 1 & 2 closed
í Parallel port select LPT1	WD	pins 1 & 2 closed
Parallel port select LPT2	WD	pins 2 & 3 closed
í Parallel port select IRQ7	W2	pins 1 & 2 closed
Parallel port select IRQ5	W2	pins 2 & 3 closed
í Floppy drive interface enabled	W3	pins 1 & 2 closed
Floppy drive interface disabled	W3	pins 2 & 3 closed
í IDE interface enabled	W5	pins 1 & 2 closed
IDE interface disabled	W5	pins 2 & 3 closed
í CPU speed select 16MHz	W9	pins 1 & 2 closed
CPU speed select 20MHz	W9	pins 2 & 3 closed
í Bus wait state select 4/memory card wait state select 1	W10	pins 1 & 2 closed
Bus wait state select 6/memory card wait state select 2	W10	pins 2 & 3 closed
í Monitor type select color	W11	pins 1 & 2 closed
Monitor type select monochrome	W11	pins 2 & 3 closed
í NPU disabled	W12	pins 1 & 2 closed
NPU enabled	W12	pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(2) 256K x 9	NONE	NONE	NONE
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
1.5MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	NONE
2MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE	NONE	NONE
3MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
5MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	NONE
8MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9

DRAM WAIT STATE CONFIGURATION		
Wait state	W13	W14
1	pins 2 & 3 closed	pins 2 & 3 closed
2	pins 1 & 2 closed	pins 1 & 2 closed

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DRAM JUMPER CONFIGURATION						
Size	W7	W8	W15	W16	W17	W18
512KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
1MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
1.5MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
2MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
2MB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
3MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
4MB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
5MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
6MB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
8MB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
Note: Pins designated should be in the closed position.						