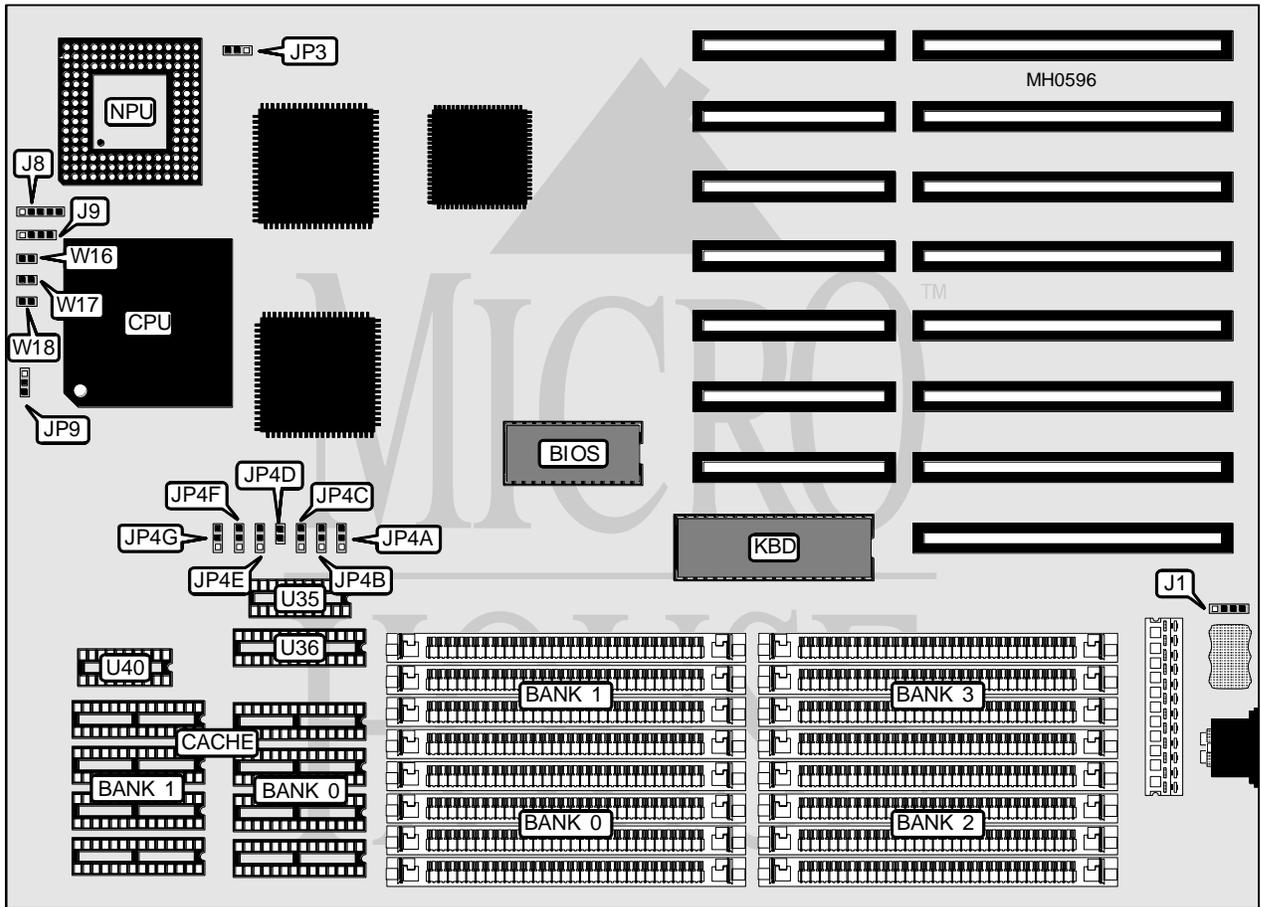


GENESYS ATE, INC.

GP486

Processor	80486DX
Speed	25/33MHz
Chip Set	UMC
Max. Onboard DRAM	64MB
Cache	32/64/128/256KB
BIOS	AMI/Quadtel
Dimensions	333mm x 222mm
I/O Options	None
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo switch	W16
Power LED & keylock	J8	Reset switch	W17
Speaker	J9	Turbo LED	W18
Note: The location of connectors are unidentified.			

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GP486

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í AT command recovery time select stretch	JP3	pins 2 & 3 closed
AT command recovery time select normal	JP3	pins 1 & 2 closed
í Monitor type select monochrome	JP9	pins 1 & 2 closed
Monitor type select color	JP9	pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
3MB	(4) 256K x 9	(4) 256K x 9	(4) 256K x 9	NONE
4MB	(4) 256K x 9			
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 256K x 9	(4) 1M x 9	NONE	NONE
6MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
9MB	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9	NONE
10MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9			
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE
24MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	NONE
28MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
36MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	NONE
40MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE
64MB	(4) 4M x 9			

DRAM JUMPER CONFIGURATION		
Size	TAG (U35)	JP4D
1MB	NONE	Closed
2MB	NONE	Closed
3MB	NONE	Closed
4MB	NONE	Closed
4MB	NONE	Closed
5MB	NONE	Closed
6MB	NONE	Closed
8MB	NONE	Closed
9MB	NONE	Closed
10MB	NONE	Closed
12MB	NONE	Closed
16MB	(1) 16KB x 4	Open
20MB	(1) 16KB x 4	Open

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DRAM JUMPER CONFIGURATION (CON'T)		
Size	TAG (U35)	JP4D
24MB	(1) 16KB x 4	Open
28MB	(1) 16KB x 4	Open
32MB	(1) 16KB x 4	Open
36MB	(1) 16KB x 4	Open
40MB	(1) 16KB x 4	Open
48MB	(1) 16KB x 4	Open
64MB	(1) 16KB x 4	Open

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG (U36)	TAG (U40)
32KB	(4) 8K x 8	NONE	(1) 8K x 8	(1) 16K x 1
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	(1) 16K x 1
128KB	(4) 32K x 8	NONE	(1) 8K x 8	(1) 16K x 1
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	(1) 16K x 1

Notes: U40 can be removed to disable cache write-back mode and enable cache write-through mode.
See DRAM jumper configuration table for configuration of TAG (U35).

CACHE JUMPER CONFIGURATION						
Size	JP4A	JP4B	JP4C	JP4E	JP4F	JP4G
32KB	1 & 2	1 & 2	1 & 2	2 & 3	Open	2 & 3
64KB	2 & 3	1 & 2	1 & 2	1 & 2	Open	1 & 2
128KB	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
256KB	2 & 3	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2

Note: Pins designated should be in the closed position.