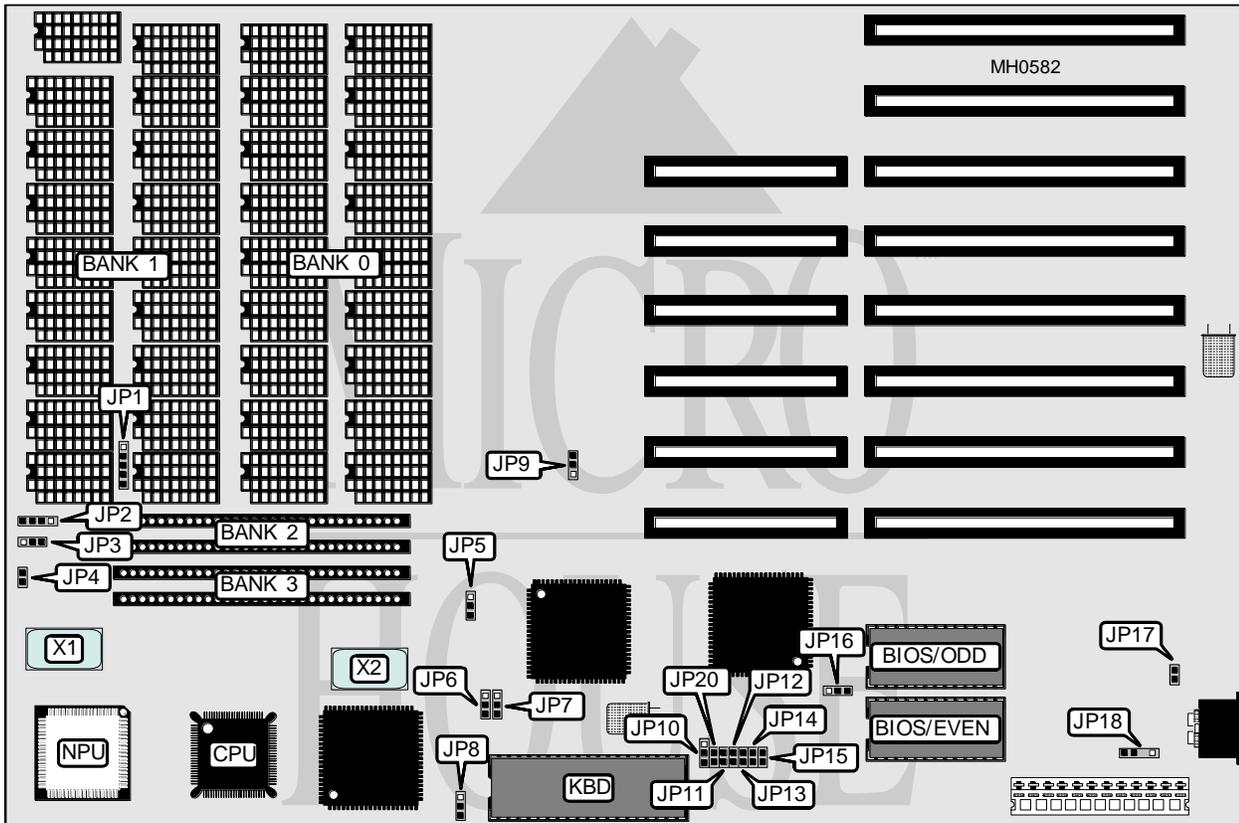


UNIDENTIFIED 401 386SX

Processor	80386SX
Processor Speed	16MHz
Chip Set	VLSI
Max. Onboard DRAM	8MB
Cache	None
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	JP1	Turbo switch	JP15
Speaker	JP2	External battery	JP18
Reset switch	JP3 or JP17	Turbo LED	JP20

Continued on next page . . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í NPU mode select synchronous with CPU	JP4	Open
NPU mode select synchronous with oscillator installed at X1	JP4	Closed
í Factory configured - do not alter	JP5	N/A
í Factory configured - do not alter	JP8	pins 2 & 3 closed
í Factory configured - do not alter	JP9	N/A
í Monitor type select color	JP10	pins 1 & 2 closed
Monitor type select monochrome	JP10	pins 2 & 3 closed
í Factory configured - do not alter	JP11	Open
í Factory configured - do not alter	JP12	Open
í Factory configured - do not alter	JP13	Closed
í Factory configured - do not alter	JP14	Open
í BIOS type select 27256	JP16	pins 2 & 3 closed
BIOS type select 27512	JP16	pins 1 & 2 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(18) 41256	NONE	NONE	NONE
512KB	NONE	NONE	(2) 256K x 9	NONE
1MB	(18) 41256	(18) 41256	NONE	NONE
1MB	NONE	NONE	(2) 256K x 9	(2) 256K x 9
2MB	(18) 411000	NONE	NONE	NONE
2MB	NONE	NONE	(2) 1M x 9	NONE
4MB	(18) 411000	(18) 411000	NONE	NONE
4MB	NONE	NONE	(2) 1M x 9	(2) 1M x 9
5MB	(18) 411000	(18) 411000	(2) 256K x 9	(2) 256K x 9
6MB	(18) 411000	(18) 411000	(2) 1M x 9	NONE
8MB	(18) 411000	(18) 411000	(2) 1M x 9	(2) 1M x 9

DRAM JUMPER CONFIGURATION					
DRAM mode	Max. Pages	Wait States	DRAM speed	JP6	JP7
F4	4	0 or 1	80ns	2 & 3	2 & 3
F1	1	0 or 1	100ns	1 & 2	2 & 3
W01	4	1 or 2	100ns	2 & 3	1 & 2
W02	4	2 or 3	120ns	1 & 2	1 & 2

Note: Pins designated should be in the closed position.