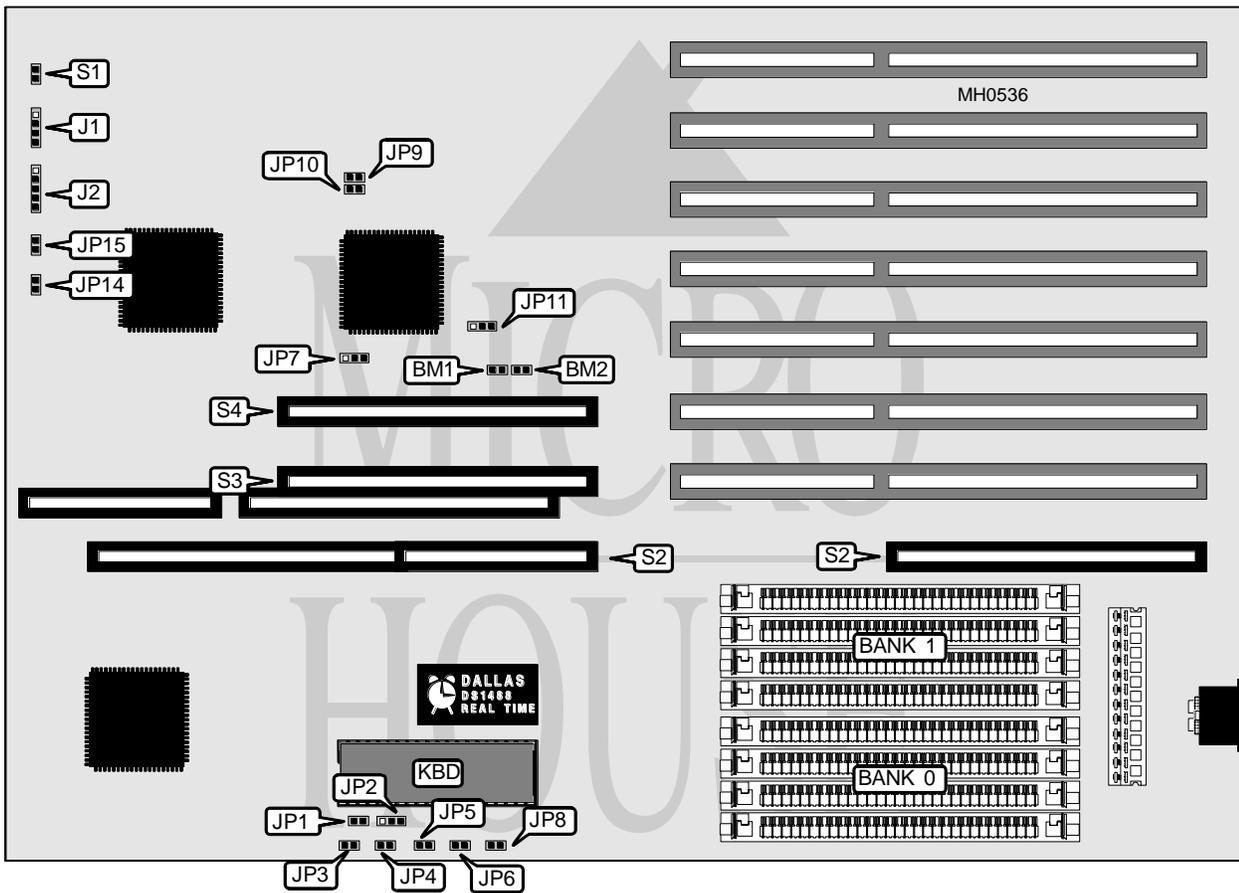


ELISA TECHNOLOGY, INC.

LM - 486 EISA (BASEBOARD)

Processor	80486SX/80487SX/80486DX/80486DX2
Processor Speed	16/20/25/33/50(internal)/66(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	128MB
Cache	256KB
BIOS	AMI
Dimensions	330mm x 225mm
I/O Options	CPU expansion card slot, 32-bit VESA local bus slots (2)
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J1	Reset switch	S1
Power LED & keylock	J2	CPU expansion card	S2
Turbo switch	JP14	32-bit VESA local bus slots	S3 & S4
Turbo LED	JP15		

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LM - 486EISA (BASEBOARD)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	BM1	N/A
í Factory configured - do not alter	BM2	N/A
í Factory configured - do not alter	JP1 - JP6	Open
í Factory configured - do not alter	JP2	Open
í Factory configured - do not alter	JP3	Open
í Factory configured - do not alter	JP4	Open
í Factory configured - do not alter	JP5	Open
í Factory configured - do not alter	JP6	Open
í Bus master on baseboard (U26) enabled	JP7	pins 1 & 2 closed
Bus master on baseboard (U26) disabled	JP7	pins 2 & 3 closed
í Monitor type select monochrome	JP8	Open
Monitor type select color	JP8	Closed

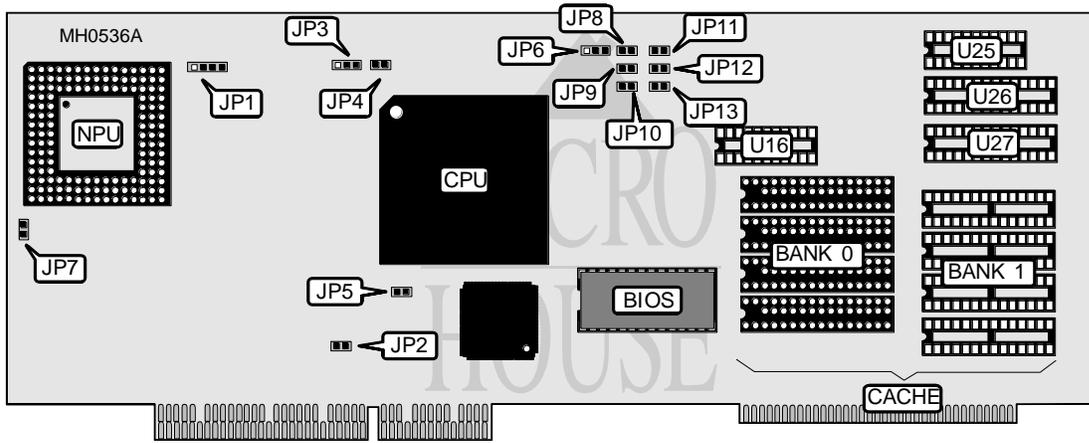
DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9
68MB	(4) 1M x 9	(4) 16M x 9
80MB	(4) 4M x 9	(4) 16M x 9
128MB	(4) 16M x 9	(4) 16M x 9

CPU TYPE CONFIGURATION			
Type	JP9	JP10	JP11
80487SX-16	Closed	Closed	pins 2 & 3 closed
80487SX-20	Closed	Closed	pins 2 & 3 closed
80486SX-16	Closed	Closed	pins 2 & 3 closed
80486SX-20	Closed	Closed	pins 2 & 3 closed
80486SX-25	Closed	Open	pins 1 & 2 closed
80486DX-25	Closed	Open	pins 1 & 2 closed
80486DX-33	Open	Open	pins 1 & 2 closed
80486DX2-50	Closed	Open	pins 1 & 2 closed
80486DX2-66	Open	Open	pins 1 & 2 closed

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ELISA TECHNOLOGY, INC.
LM-486EISA (CPU CARD)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP2	pins 2 & 3 closed
í Factory configured - do not alter	JP4	Open
í Bus master on CPU card enabled	JP5	Open
Bus master on CPU card disabled	JP5	Closed

CACHE CONFIGURATION						
Size	Bank 0	Bank 1	TAG (U16)	Dirty bit (U25)	TAG (U26)	TAG (U27)
64KB	(4) 8K x 8	(4) 8K x 8	(1) 16K x 1	(1) 8K x 8	NONE	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 16K x 1	(1) 8K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 16K x 1	(1) 8K x 8	(1) 8K x 8	(1) 8K x 8
512KB	(4) 128K x 8	NONE	(1) 64K x 1	(1) 8K x 8	NONE	(1) 32K x 8

CACHE JUMPER CONFIGURATION							
Size	JP6	JP8	JP9	JP10	JP11	JP12	JP13
64KB	2 & 3	Open	Open	Open	Open	Open	Open
128KB	1 & 2	Closed	Open	Open	Closed	Open	Open
256KB	2 & 3	Closed	Closed	Open	Closed	Closed	Open
512KB	1 & 2	Closed	Closed	Closed	Closed	Closed	Closed

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION			
Type	JP1	JP3	JP7
80487SX-16	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed	Closed
80487SX-20	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed	Closed
80486SX-16	pins 2 & 3 closed	Open	Closed
80486SX-20	pins 2 & 3 closed	Open	Closed
80486SX-25	pins 2 & 3 closed	Open	Open
80486DX-25	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	Open
80486DX-33	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	Open
80486DX2-50	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	Open
80486DX2-66	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	Open