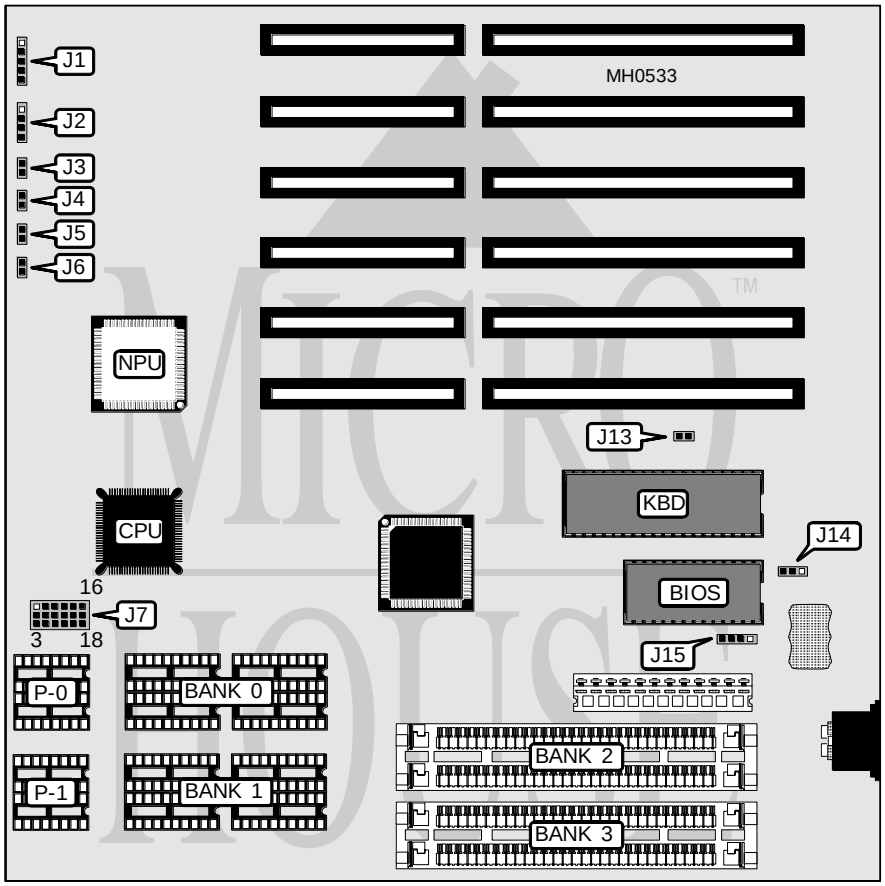
CHICONY, INC.
CH-386S-16/20/25G

Processor	80386SX
Processor Speed	16/20/25MHz
Chip Set	OPTI
Max. Onboard DRAM	16MB
Cache	None
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J1	Turbo switch	J4
Speaker	J2	Turbo LED	J5
Reset switch	J3	External battery	J15

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í NPU mode select asynchronous with CPU	J6	Closed
NPU mode select synchronous with CPU	J6	Open
í Monitor type select monochrome	J13	Open
Monitor type select color	J13	Closed
í CMOS memory normal operation	J14	pins 2 & 3 closed
CMOS memory clear	J14	pins 1 & 2 closed

DRAM CONFIGURATION						
Size	Bank 0	P-0	Bank 1	P-1	Bank 2	Bank 3
512KB[1]	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
512KB[2]	NONE	NONE	NONE	NONE	(2) 256K x 9	NONE
1MB[1]	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
1MB[2]	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 256K x 9
1.5MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 256K x 9	NONE
2MB[1]	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 256K x 9	(2) 256K x 9
2MB[2]	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
2.5MB	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 1M x 9
3MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 1M x 9	NONE
4MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9
5MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 1M x 9	(2) 1M x 9
9MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 4M x 9	NONE
10MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 4M x 9
16MB	NONE	NONE	NONE	NONE	(2) 4M x 9	(2) 4M x 9

Note: DIP locations and socket sizes are unverified.
Note [1] : See the next table for the corresponding setting for this configuration.
Note [2] : See the next table for the corresponding setting for this configuration.

DRAM JUMPER CONFIGURATION	
Size	J7
512KB[1]	pins 2 & 3, 5 & 6, 8 & 9, 11 & 12, 14 & 15, and 17 & 18 closed
512KB[2]	pins 1 & 2, 4 & 5, 7 & 8, 10 & 11, 13 & 14, and 16 & 17 closed
1MB[1]	pins 2 & 3, 5 & 6, 8 & 9, 11 & 12, 14 & 15, and 17 & 18 closed
1MB[2]	pins 1 & 2, 4 & 5, 7 & 8, 10 & 11, 13 & 14, and 16 & 17 closed
1.5MB	pins 2 & 3, 5 & 6, 8 & 9, 11 & 12, 14 & 15, and 17 & 18 closed
2MB[1]	pins 2 & 3, 5 & 6, 8 & 9, 11 & 12, 14 & 15, and 17 & 18 closed
2MB[2]	pins 1 & 2, 4 & 5, 7 & 8, 10 & 11, 13 & 14, and 16 & 17 closed
2.5MB	pins 1 & 2, 4 & 5, 7 & 8, 10 & 11, 13 & 14, and 16 & 17 closed
3MB	pins 2 & 3, 5 & 6, 8 & 9, 11 & 12, 14 & 15, and 17 & 18 closed
4MB	pins 1 & 2, 4 & 5, 7 & 8, 10 & 11, 13 & 14, and 16 & 17 closed
5MB	pins 2 & 3, 5 & 6, 8 & 9, 11 & 12, 14 & 15, and 17 & 18 closed
9MB	pins 2 & 3, 5 & 6, 8 & 9, 11 & 12, 14 & 15, and 17 & 18 closed
10MB	pins 1 & 2, 4 & 5, 7 & 8, 10 & 11, 13 & 14, and 16 & 17 closed
16MB	pins 1 & 2, 4 & 5, 7 & 8, 10 & 11, 13 & 14, and 16 & 17 closed

Note [1] : See the previous table for the corresponding setting for this configuration.
Note [2] : See the previous table for the corresponding setting for this configuration.