CHICONY, INC.
CH-48633C

Processor	80486SX/80487SX/80486DX
Processor Speed	20/25/33MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	4167
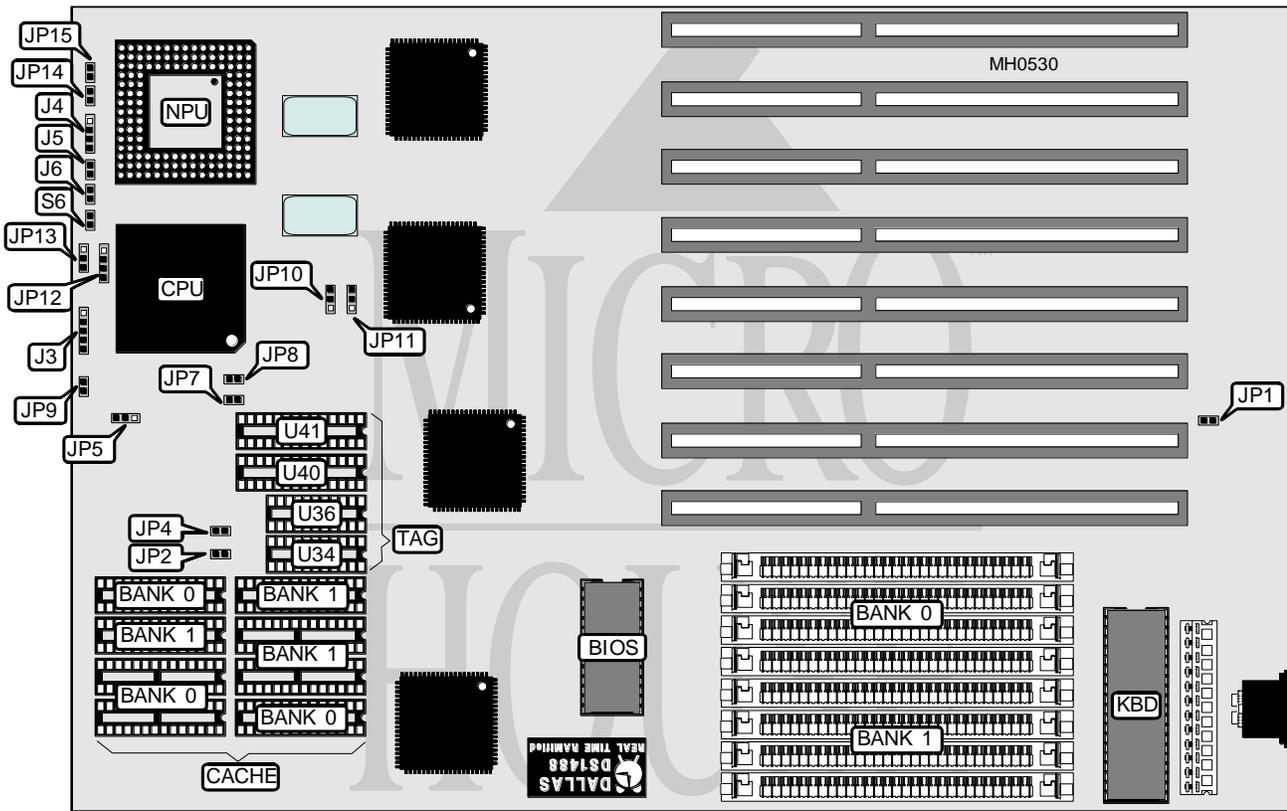
CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J3	External battery	J11
Speaker	J4	Turbo LED	JP1
Turbo switch	J5	Reset switch	S6

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP1	Closed
Monitor type select monochrome	JP1	Open
í Factory configured - do not alter	JP9	Open
í CPU speed select iOSC/2	JP10	pins 2 & 3 closed
CPU speed select iOSC/1	JP10	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION					
Size	Bank 0	Bank 1	TAG (U34,U36)	TAG (U40)	TAG (U41)
64KB	(4) 8K x 8	(4) 8K x 8	(2) 64K x 1	(1) 8K x 8	NONE
128KB	(4) 32K x 8	NONE	(2) 64K x 1	(1) 8K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8	(2) 64K x 1	(1) 8K x 8	(1) 8K x 8

CACHE JUMPER CONFIGURATION					
Size	JP2	JP4	JP5	JP7	JP8
64KB	Open	Open	2 & 3	Open	Open
128KB	Closed	Open	1 & 2	Open	Closed
256KB	Closed	Closed	2 & 3	Closed	Closed
Note: Pins designated should be in the closed position.					

CPU TYPE CONFIGURATION			
Type	JP11	JP12	JP13
80486SX	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed
80487SX	pins 2 & 3 closed	pins 1 & 2 and 3 & 4 closed	pins 1 & 2 closed
80486DX	pins 1 & 2 closed	pins 1 & 2 and 3 & 4 closed	pins 2 & 3 closed

CPU SPEED CONFIGURATION		
Type	JP14	JP15
25MHz	Open	Open
33MHz	Closed	Open