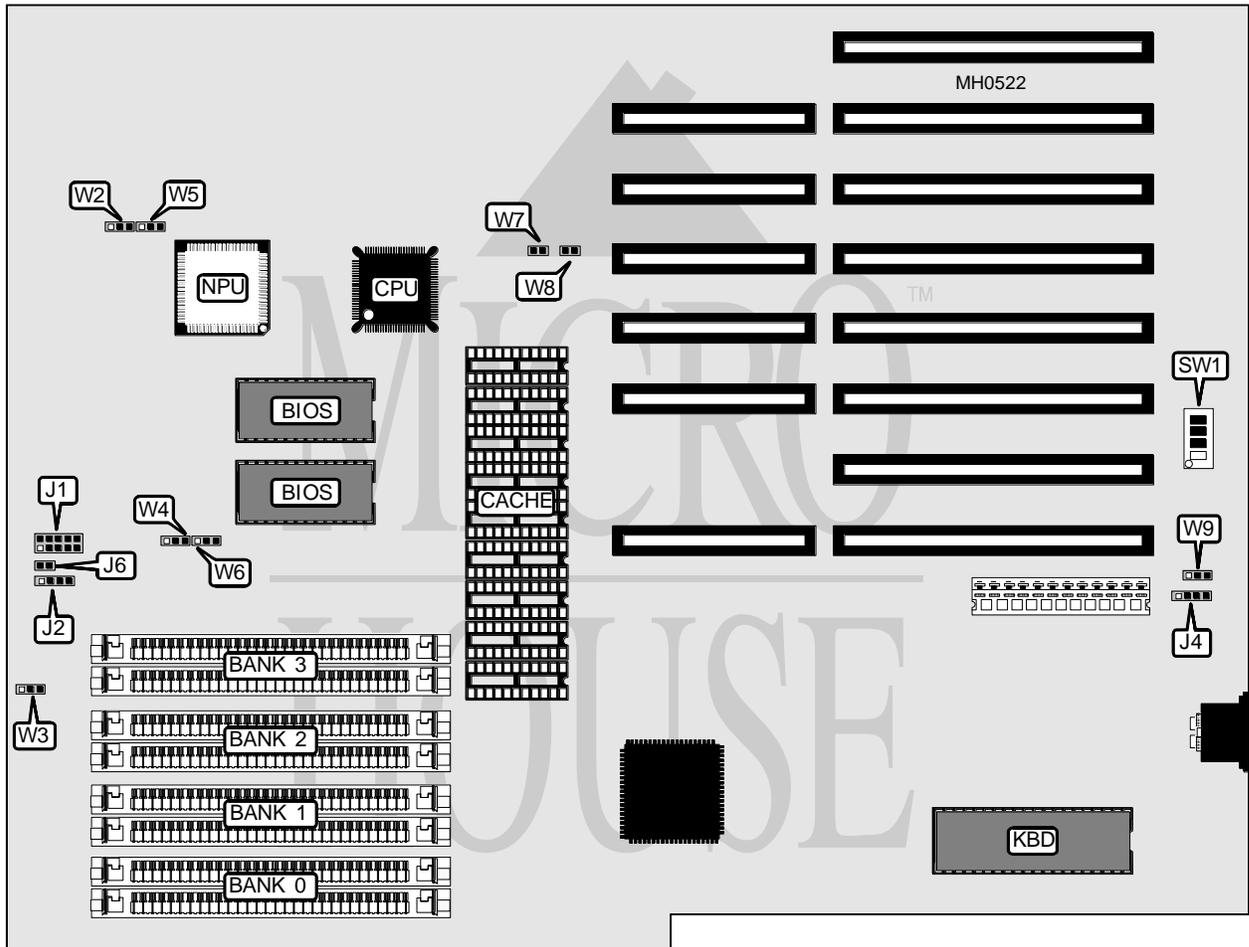


EVEREX SYSTEMS, INC.
3000B 80386SX-20

Processor	80386SX
Processor Speed	20MHz
Chip Set	Suntac
Max. Onboard DRAM	8MB
Cache	64KB
BIOS	AMI
Dimensions	355mm x 304mm
I/O Options	None
NPU Options	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Front panel display	J1	External battery	J4
Speaker	J2		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	J6	N/A
í Factory configured - do not alter	W7	Open
í Factory configured - do not alter	W8	Closed
í I/O bus (8-bit) speed select 5 wait states	W2	pins 1 & 2 closed
I/O bus (8-bit) speed select 4 wait states	W2	pins 2 & 3 closed
í Power on speed select fast/switchable from keyboard	W3	pins 2 & 3 closed
Power on speed select slow	W3	pins 1 & 2 closed
Power on speed select fast	W3	Open
í I/O bus (16-bit) speed select 2 wait states	W5	pins 2 & 3 closed
I/O bus (16-bit) speed select 1 wait states	W5	pins 1 & 2 closed
í Monitor type select monochrome	W9	Closed
Monitor type select color	W9	Open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
2MB	(2) 256K x 9			
2MB	(2) 1M x 9	NONE	NONE	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	NONE
8MB	(2) 1M x 9			

DRAM SWITCH CONFIGURATION				
Size	SW1/1	SW1/2	SW1/3	SW1/4
1MB	On	On	On	On
2MB	On	On	On	Off
2MB	On	Off	Off	On
4MB	On	Off	On	Off
6MB	On	Off	Off	Off
8MB	Off	Off	Off	Off

DRAM JUMPER CONFIGURATION		
Size	W4	W6
1MB	pins 1 & 2 closed	pins 1 & 2 closed
2MB	pins 1 & 2 closed	pins 1 & 2 closed
2MB	pins 2 & 3 closed	pins 2 & 3 closed
4MB	pins 2 & 3 closed	pins 2 & 3 closed
6MB	pins 2 & 3 closed	pins 2 & 3 closed
8MB	pins 2 & 3 closed	pins 2 & 3 closed

SRAM CONFIGURATION	
Size	Bank 3
64KB	(9) 64K x 1
Notes: Cache location & chip sizes are unidentified.	