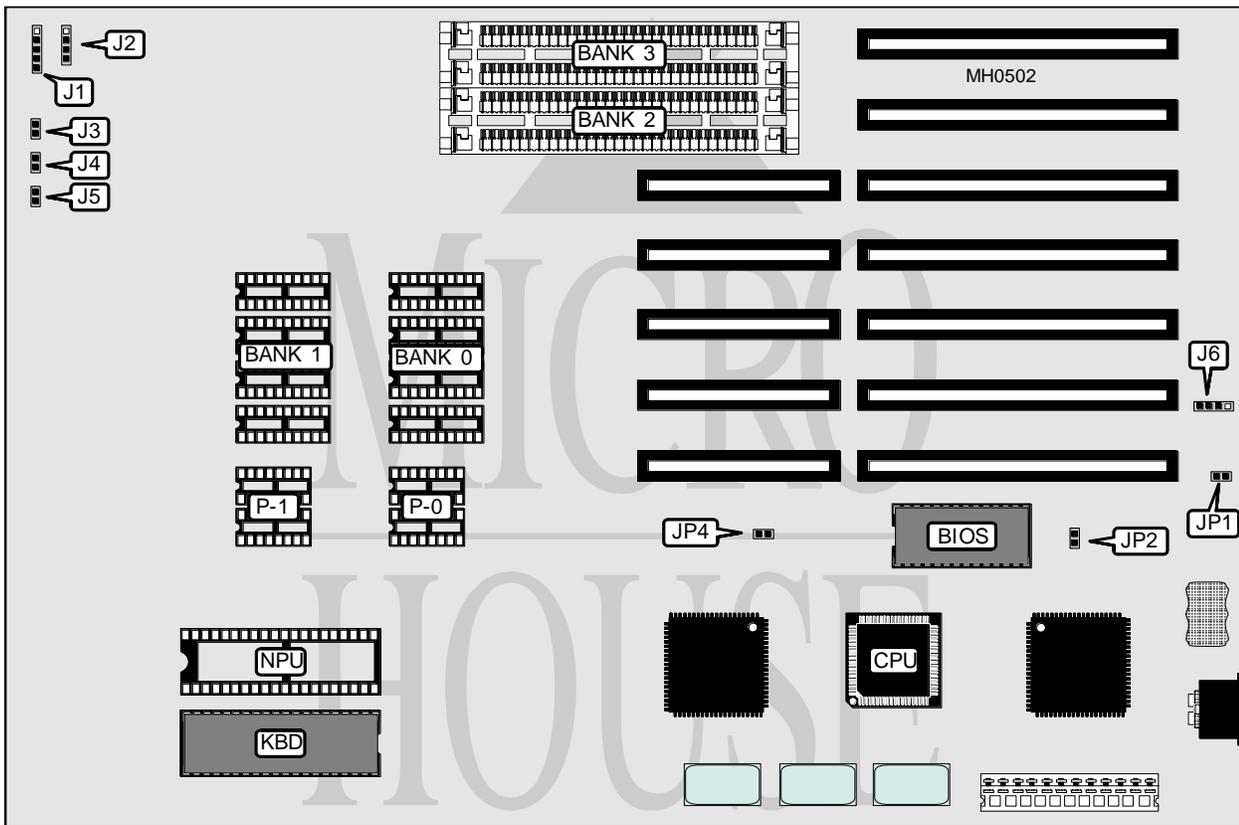


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MB - 1 2 1 2 V S

Processor 80286
Processor Speed 16/20MHz
Chip Set VLSI Scamp
Max. Onboard DRAM 16MB
Cache None
BIOS AMI
Dimensions 330mm x 218mm
I/O Options None
NPU Options 80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J1	Reset switch	J4
Speaker	J2	Turbo switch	J5
Turbo LED	J3	External battery	J6

Continued on next page . . .

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MB-1212VS

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery select internal	JP1	Closed
Battery select external	JP1	Open
í Monitor type select monochrome	JP2	Open
Monitor type select color	JP2	Closed
í Chipset select 82C311L installed at U23	JP4	Closed
Chipset select 82C310 installed at U23	JP4	Open

DRAM CONFIGURATION						
Size	Bank 0	P-0	Bank 1	P-1	Bank 2	Bank 3
512KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
512KB	NONE	NONE	NONE	NONE	(2) 256K x 9	NONE
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
1MB	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 256K x 9
2MB	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
4MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9
8MB	NONE	NONE	NONE	NONE	(2) 4M x 9	NONE
16MB	NONE	NONE	NONE	NONE	(2) 4M x 9	(2) 4M x 9