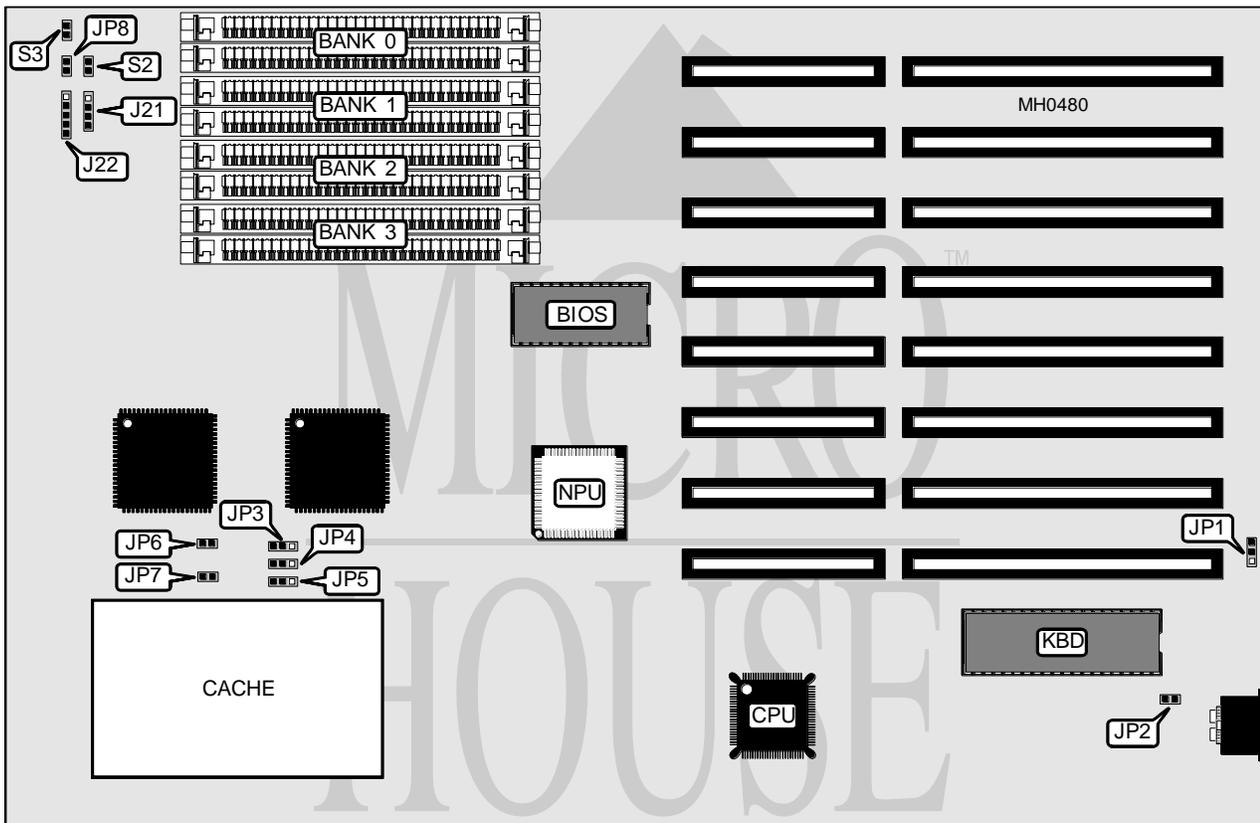


# SHUTTLE COMPUTER INTERNATIONAL, INC.

## 80386SX-20 CACHE MAINBOARD

<b>Processor</b>	80386SX
<b>Processor Speed</b>	20MHz
<b>Chip Set</b>	OPTI
<b>Max. Onboard DRAM</b>	16MB
<b>Cache</b>	64KB
<b>BIOS</b>	AMI
<b>Dimensions</b>	248mm x 330mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	JP8	Reset switch	S2
Power LED & keylock	J22	Turbo switch	S3
Speaker	J21		

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# SHUTTLE COMPUTER INTERNATIONAL, INC.

## 80386SX-20 CACHE MAINBOARD

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	pins 1 & 2 closed
í Monitor type select color	JP2	Closed
Monitor type select monochrome	JP2	Open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(2) 256K x 9	NONE	NONE	NONE
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
2MB	(2) 256K x 9			
3MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
5MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	NONE
8MB	(2) 1M x 9			
9MB	(2) 256K x 9	(2) 256K x 9	(2) 4M x 9	NONE
10MB	(2) 1M x 9	(2) 4M x 9	NONE	NONE
12MB	(2) 1M x 9	(2) 1M x 9	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9	NONE	NONE

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
16KB	(4) 4K x 8	(4) 4K x 8	(3) 16K x 4
32KB	(4) 8K x 8	NONE	(3) 16K x 4
64KB	(4) 8K x 8	(4) 8K x 8	(3) 16K x 4

Note: The location of the cache is unidentified.

CACHE JUMPER CONFIGURATION					
Size	JP3	JP4	JP5	JP6	JP7
16KB	1 & 2	1 & 2	1 & 2	Open	Open
32KB	2 & 3	1 & 2	1 & 2	Closed	Open
64KB	2 & 3	2 & 3	2 & 3	Open	Closed

Note: Pins designated should be in the closed position.