UNIDENTIFIED
CHARISMA 286 MINI AT
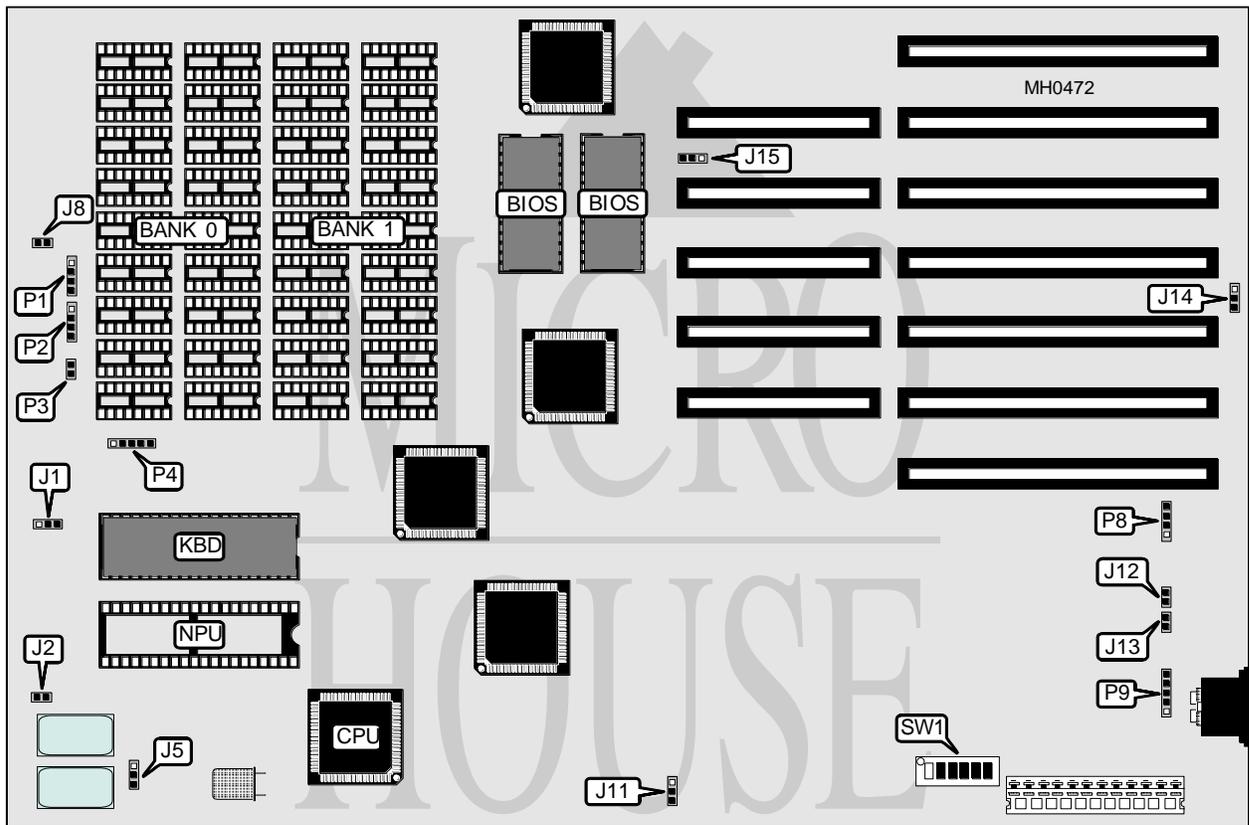
Processor   80286
Processor Speed   12MHz
Chip Set   Unidentified
Max. Onboard DRAM   1MB
Cache   None
BIOS   Award
Dimensions   330mm x 218mm
I/O Options   None
NPU Options   80287

CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	P1	Power LED & keylock	P4
Turbo LED	P2	External battery	P8
Reset switch	P3		

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í CPU speed select switchable at keyboard	J1	pins 2 & 3 closed
í Power good signal detect from board	JP14	pins 1 & 2 closed
Power good signal detect from power supply	JP14	pins 2 & 3 closed
í BIOS type select 27128	J15	pins 1 & 2 closed
BIOS type select 27256	J15	pins 2 & 3 closed
í Monitor type select monochrome	SW1/6	Off
Monitor type select CGA or EGA	SW1/6	On

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
256KB	(18) 4164	(18) 4164
512KB	(18) 41256	NONE
640KB	(18) 41256	(18) 4164
1MB	(18) 41256	(18) 41256

DRAM SWITCH CONFIGURATION		
Size	SW1/3	SW1/4
256KB	Closed	Closed
512KB	Open	Closed
640KB	Closed	Open
1MB	Open	Open

DRAM WAIT STATE CONFIGURATION		
Wait states	J2	J8
0	Closed	Closed
1	Open	Open

CPU SPEED CONFIGURATION			
Speed	J1	J5	J11
6MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
8MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
10MHz	Open	pins 1 & 2 closed	pins 1 & 2 closed
12MHz	Open	pins 2 & 3 closed	pins 2 & 3 closed

NPU SPEED CONFIGURATION			
NPU speed	CPU speed	J6	J7
6MHz	10MHz	pins 2 & 3 closed	Closed
6MHz	8MHz	pins 2 & 3 closed	Closed
8MHz	12MHz	pins 2 & 3 closed	Closed
10MHz	10MHz	pins 1 & 2 closed	Open

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BATTERY CONFIGURATION		
Type	J12	J13
External alkaline (6V)	Open	Open
External lithium (6V)	Open	pins 1 & 2 closed
Internal double lithium (3V each)	pins 1 & 2 closed	Closed

PROPRIETARY BUS CONFIGURATION		
Bus mode	SW1/1	SW1/2
0	On	On
1	Off	On
2	On	Off
3	Off	Off