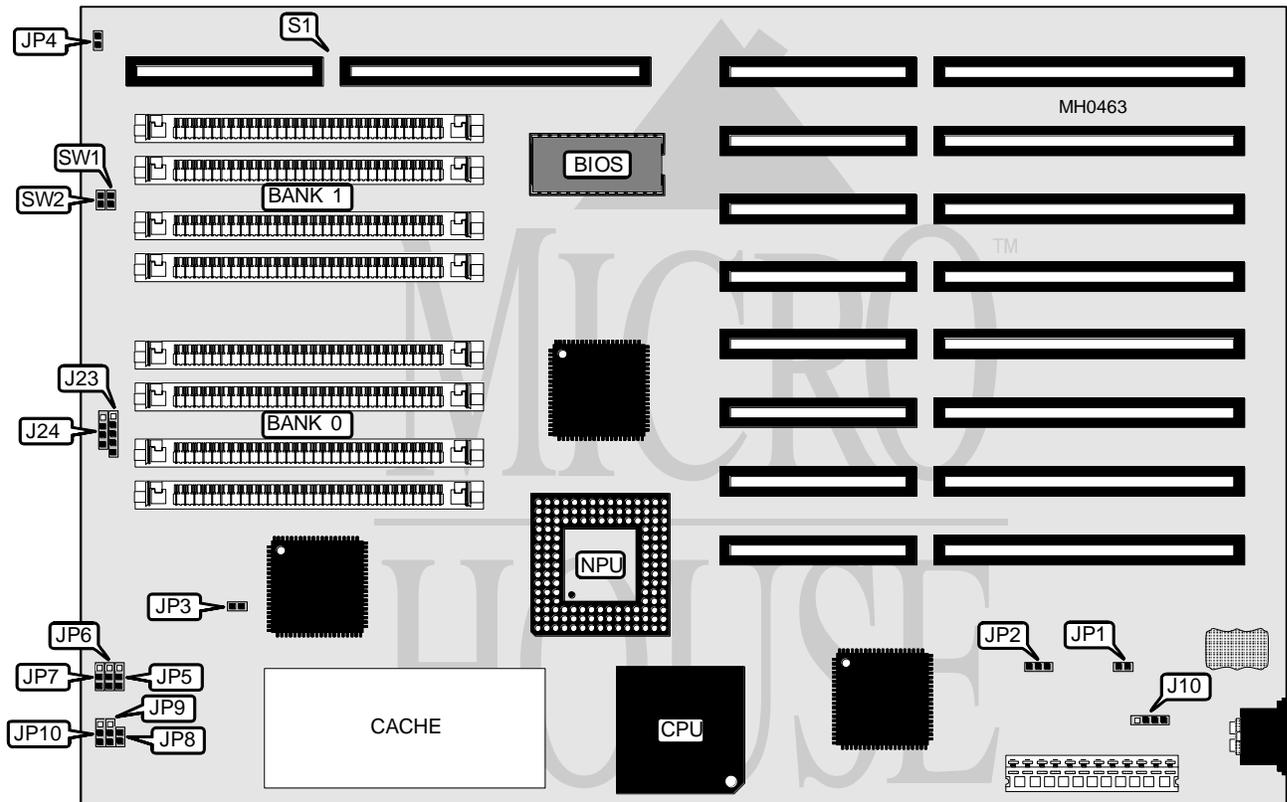


# QUICK TECHNOLOGY, INC. 80386 WRITE BACK CACHE

**Processor** 80386DX  
**Processor Speed** 33/40MHz  
**Chip Set** OPTI  
**Max. Onboard DRAM** 32MB  
**Cache** 256KB  
**BIOS** AMI  
**Dimensions** 355mm x 304mm  
**I/O Options** 32-bit external memory card slot  
**NPU Options** 80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	JP4	Power LED & keylock	J23
Reset switch	SW1	Speaker	J24
Turbo switch	SW2	32-bit external memory card	S1

Continued on next page . . .

# QUICK TECHNOLOGY, INC.

## 80386 WRITE BACK CACHE

... continued from previous page

### USER CONFIGURABLE SETTINGS

Function	Jumper	Position
í Battery select internal	J10	pins 2 & 3 closed
Battery select external	J10	Closed
í Monitor type select color	JP1	Closed
Monitor type select monochrome	JP1	Open
í CMOS memory normal operation	JP2	pins 2 & 3 closed
CMOS memory clear	JP2	pins 1 & 2 closed
í Bus speed select ATCLK/6	JP3	Open
Bus speed select ATCLK/8	JP3	Closed

### DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 256K x 9	(4) 1 M x 9	NONE	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE	NONE	NONE
20MB	(4) 4M x 9	(4) 1M x 9	NONE	NONE
24MB	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9	NONE
28MB	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
36MB	(4) 4M x 9	(4) 1M x 9	(4) 4M x 9	NONE
40MB	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9	(4) 1M x 9
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE
52MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 1M x 9
64MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9

### CACHE CONFIGURATION

Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	2K x 9
64KB	(4) 8K x 8	(4) 8K x 8	4K x 9
128KB	(4) 32K x 8	NONE	8K x 9
256KB	(4) 32K x 8	(4) 32K x 8	16K x 9

Note: The location of cache chips is unidentified.

### CACHE JUMPER CONFIGURATION

Size	JP5	JP6	JP7	JP8	JP9	JP10
32KB	1 & 2	1 & 2	1 & 2	Open	2 & 3	2 & 3
64KB	2 & 3	1 & 2	1 & 2	Open	2 & 3	1 & 2
128KB	2 & 3	1 & 2	2 & 3	Closed	2 & 3	1 & 2
256KB	2 & 3	2 & 3	2 & 3	Closed	1 & 2	1 & 2

Note: Pins designated should be in the closed position.