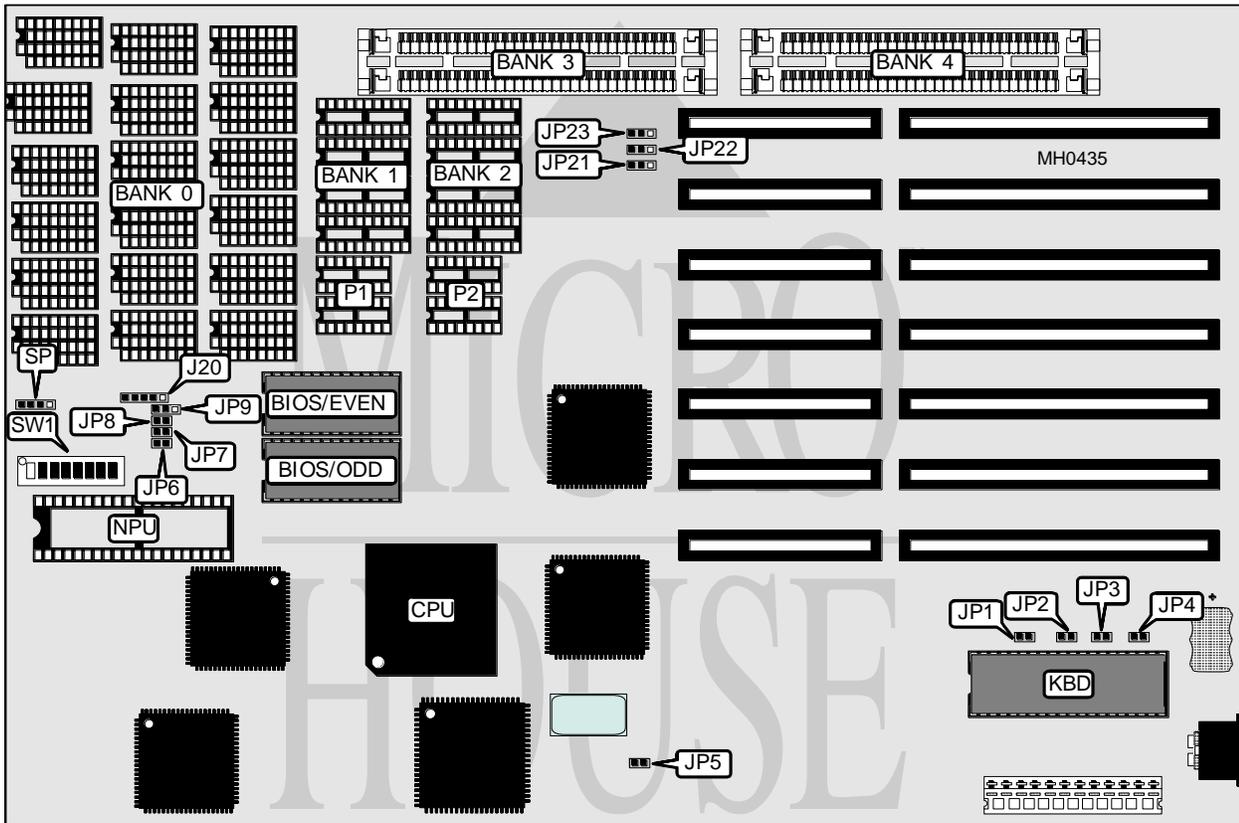


UNIDENTIFIED

A TEAM - 320A

Processor	80286
Processor Speed	6/12.5MHz
Chip Set	Unidentified
Max. Onboard DRAM	4MB
Cache	None
BIOS	AMI/Award/ERSO/Phoenix
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Reset switch	JP6	Power LED & keylock	J20
Turbo switch	JP7	Speaker	SP
Turbo LED	JP8		

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Select 0 wait states	JP5	Closed
Select 1 wait state	JP5	Open
í Clock speed select low	SW1/1	On
Clock speed select high	SW1/1	Off
í Monitor type select color	SW1/2	On
Monitor type select monochrome	SW1/2	Off
í Parity check enabled	SW1/3	On
Parity check disabled	SW1/3	Off
í EMS port address select OE8H	SW1/4	On
EMS port address select O98H	SW1/4	Off
í BIOS type select 27128	SW1/5	On
BIOS type select 27256	SW1/5	Off

DRAM CONFIGURATION					
Size	Bank 0	Bank 1	Bank 2	Bank 3	Bank 4
512KB	(18) 41256	NONE	NONE	NONE	NONE
512KB*	NONE	(4) 44256	NONE	NONE	NONE
512KB	NONE	NONE	NONE	(2) 256K x 9	NONE
640KB*	(18) 4164	(4) 44256	NONE	NONE	NONE
640KB	(18) 4164	NONE	NONE	(2) 256K x 9	NONE
1MB*	NONE	(4) 44256	(4) 44256	NONE	NONE
1MB	NONE	NONE	NONE	(2) 256K x 9	(2) 256K x 9
1MB*	(18) 41256	(4)44256	NONE	NONE	NONE
1MB*	(18) 41256	(4) 44256	NONE	NONE	NONE
1MB	(18) 41256	NONE	NONE	(2) 256K x 9	NONE
1MB	(18) 41256	NONE	NONE	NONE	(2) 256K x 9
2MB	(18) 411000	NONE	NONE	NONE	NONE
2MB	NONE	NONE	NONE	(2) 1M x 9	NONE
4MB	NONE	NONE	NONE	(4) 1M x 9	(4) 1M x 9
4MB	(18) 411000	NONE	NONE	(2) 1M x 9	NONE
4MB	(18) 411000	NONE	NONE	NONE	(2) 1M x 9

Note: The configurations with an *, see Parity Configuration below.

DRAM PARITY CONFIGURATION		
Size	P1	P2
512KB	(2) 41256	NONE
640KB	(2) 41256	NONE
1MB	(2) 41256	(2) 41256
1MB	(2) 41256	NONE
1MB	(2) 41256	NONE

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DRAM JUMPER CONFIGURATION			
Size	JP21	JP22	JP23
512KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
512KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
512KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
640KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
640KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
1MB	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
1MB	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
1MB	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
1MB	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed
1MB	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
1MB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
2MB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
2MB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
4MB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
4MB	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
4MB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed

DRAM SWITCH CONFIGURATION			
Size	SW1/6	SW1/7	SW1/8
512KB	On	On	On
640KB	On	On	Off
1MB	On	Off	On
640KB + EMS (384KB)	On	Off	Off
2MB	Off	On	On
640KB + EMS (1408KB)	Off	On	Off
4MB	Off	Off	On
640KB + EMS (3456KB)	Off	Off	Off

CPU SPEED CONFIGURATION		
Speed	JP7	JP9
Low	Open	pins 1 & 2 closed
High	Closed	pins 1 & 2 closed
Switchable by keyboard	Open	pins 2 & 3 closed