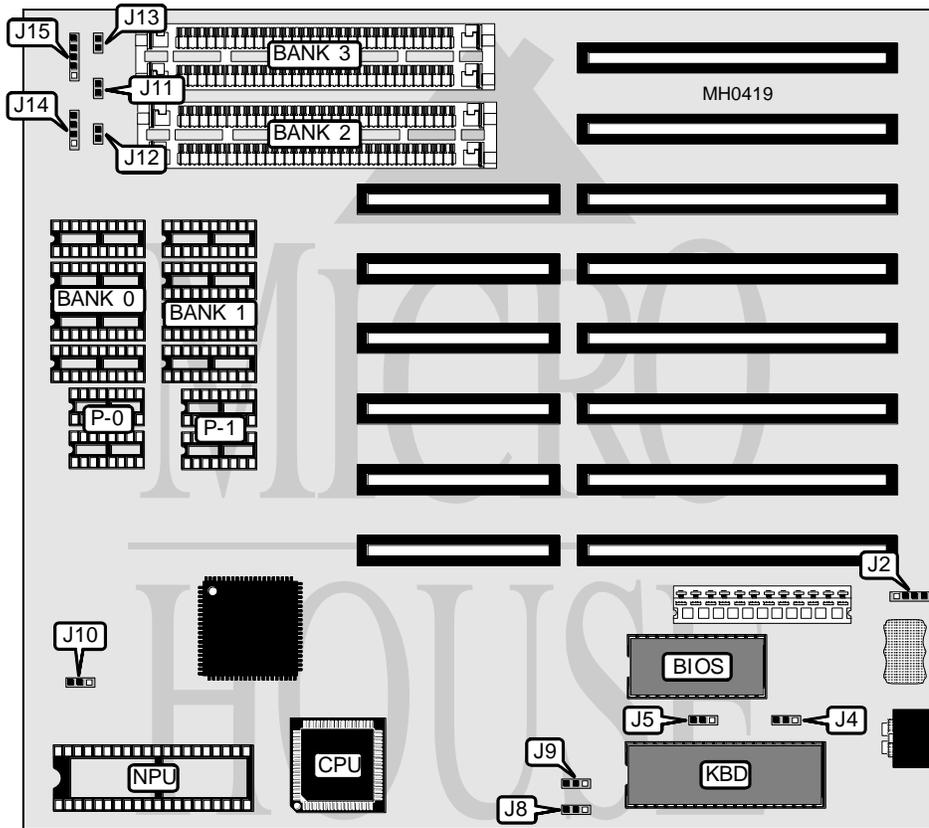


# UNIDENTIFIED

## 80286-12 TURBO

<b>Processor</b>	80286
<b>Processor Speed</b>	12MHz
<b>Chip Set</b>	Unknown
<b>Max. Onboard DRAM</b>	4MB
<b>Cache</b>	None
<b>BIOS</b>	AMI
<b>Dimensions</b>	243mm x 218mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Reset switch	JP13
Turbo LED	JP11	Speaker	JP14
Turbo switch	JP12	Power LED & keylock	JP15

Continued on next page . . .

# UNIDENTIFIED

## 80286-12 TURBO

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
Factory configured - do not alter	J8	N/A
Factory configured - do not alter	J9	N/A
í NPU mode select synchronous with CPU	J10	pins 2 & 3 closed
NPU mode select asynchronous with CPU	J10	pins 1 & 2 closed
í Monitor type select color	JP5	pins 2 & 3 closed
Monitor type select monochrome	JP5	pins 1 & 2 closed

DRAM CONFIGURATION						
Size	Bank 0	P-0	Bank 1	P-1	Bank 2	Bank 3
512KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
512KB	NONE	NONE	NONE	NONE	(2) 256K x 9	NONE
640KB	(4) 44256	(2) 41256	(4) 4464	(2) 4164	NONE	NONE
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
1MB	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 256K x 9
2MB	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
4MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9

Notes: Locations of Bank 0, Bank 1, P0, & P1 unidentified. Bank 0 consists of U26, U27, U31, & U32. Bank 1 consists of U35, U36, U37, & U38. P-0 consists of U41 & U42. P-1 consists of U43 & U44.