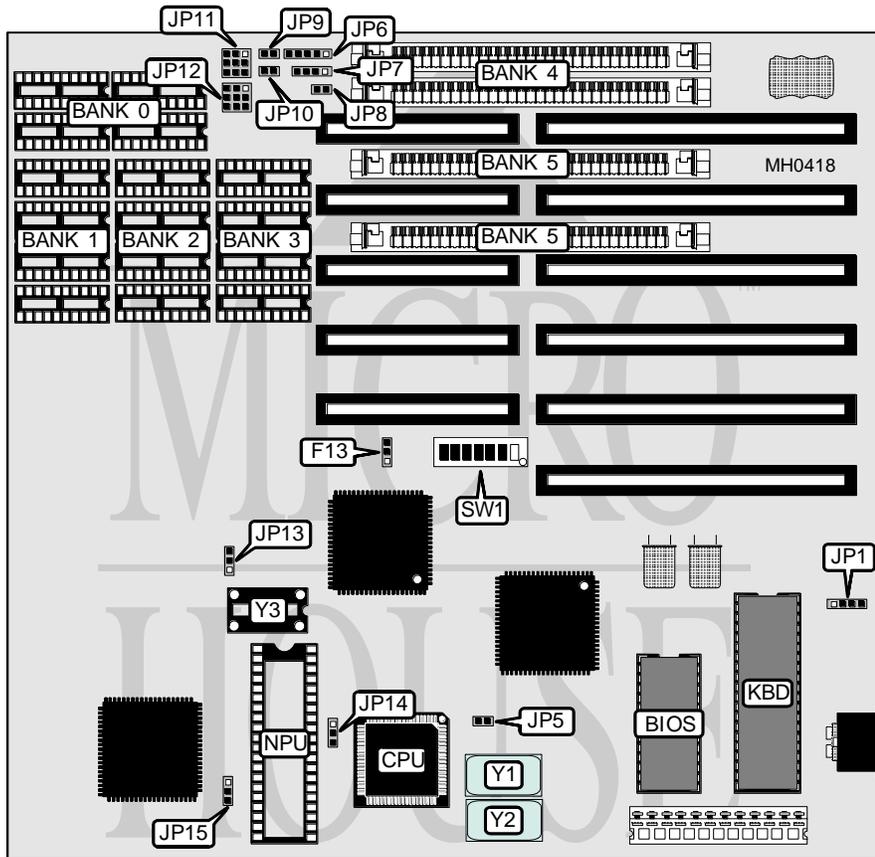


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YL-A09S/YL-A09H

Processor 80286
Processor Speed 16MHz
Chip Set Headland
Max. Onboard DRAM 5MB
Cache None
BIOS AMI
Dimensions 330mm x 218mm
I/O Options None
NPU Options 80287



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP1	Reset switch	JP8
Power LED & keylock	JP6	Turbo LED	JP9
Speaker	JP7	Turbo switch	JP10

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
Internal delay select GC113	F13	pins 2 & 3 closed
Internal delay select GC103	F13	pins 1 & 2 closed
í Factory configured - do not alter	JP5	N/A
BIOS type select 27128	JP13	pins 1 & 2 closed
BIOS type select 27256	JP13	pins 2 & 3 closed
í NPU mode select synchronous with CPU	JP14	pins 2 & 3 closed
NPU mode select synchronous with oscillator installed at Y3	JP14	pins 1 & 2 closed
í Parity check enabled	JP15	pins 1 & 2 closed
Parity check disabled	JP15	pins 2 & 3 closed
í Monitor type select color	SW1/5	On
Monitor type select monochrome	SW1/5	Off
í Shadow RAM enabled	SW1/6	Off
Shadow RAM disabled	SW1/6	On
í Bus speed select fast	SW1/7	Off
Bus speed select slow	SW1/7	On

DRAM CONFIGURATION						
Size	Bank 0	Bank 1	Bank 2	Bank 3	Bank 4	Bank 5
512KB	(4) 44256	NONE	NONE	NONE	NONE	NONE
512KB	NONE	NONE	NONE	NONE	(2) 256K x 9	NONE
1MB	(4) 44256	(4) 44256	NONE	NONE	NONE	NONE
1MB	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 256K x 9
1.5MB	(4) 44256	(4) 44256	(4) 44256	NONE	NONE	NONE
1.5MB	NONE	NONE	(4) 44256	NONE	(2) 256K x 9	(2) 256K x 9
2MB	(4) 44256	(4) 44256	(4) 44256	(4) 44256	NONE	NONE
2MB	NONE	NONE	(4) 44256	(4) 44256	(2) 256K x 9	(2) 256K x 9
2MB	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
4MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9
5MB	NONE	NONE	(4) 44256	(4) 44256	(2) 1M x 9	(2) 1M x 9

Note: Locations of Banks 0, 1, 2, & 3 are unidentified.

DRAM JUMPER/SWITCH CONFIGURATION						
Size	JP11	JP12	SW1/1	SW1/2	SW1/3	SW1/4
512KB	2 & 3, 5 & 6, 8 & 9	2 & 3, 5 & 6, 8 & 9	On	Off	Off	Off
512KB	1 & 2, 4 & 5, 7 & 8	1 & 2, 4 & 5, 7 & 8	On	Off	Off	Off
1MB	2 & 3, 5 & 6, 8 & 9	2 & 3, 5 & 6, 8 & 9	Off	On	Off	Off
1MB	1 & 2, 4 & 5, 7 & 8	1 & 2, 4 & 5, 7 & 8	Off	On	Off	Off
1.5MB	2 & 3, 5 & 6, 8 & 9	2 & 3, 5 & 6, 8 & 9	Off	Off	On	Off
1.5MB	1 & 2, 4 & 5, 7 & 8	1 & 2, 4 & 5, 7 & 8	Off	Off	On	Off
2MB	2 & 3, 5 & 6, 8 & 9	2 & 3, 5 & 6, 8 & 9	Off	On	On	Off
2MB	1 & 2, 4 & 5, 7 & 8	1 & 2, 4 & 5, 7 & 8	Off	On	On	Off
2MB	1 & 2, 4 & 5, 7 & 8	1 & 2, 4 & 5, 7 & 8	Off	Off	Off	On
4MB	1 & 2, 4 & 5, 7 & 8	1 & 2, 4 & 5, 7 & 8	Off	On	Off	On
5MB	1 & 2, 4 & 5, 7 & 8	1 & 2, 4 & 5, 7 & 8	On	On	On	On

Note: Pins designated should be in the closed position