BIOSTAR MICROTECH INTERNATIONAL CORPORATION
M B - 1 2 1 2 V

Processor	80286
Processor Speed	12MHz
Chip Set	VLSI
Max. Onboard DRAM	4MB
Cache	None
BIOS	AMI/Award/Phoenix
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80287
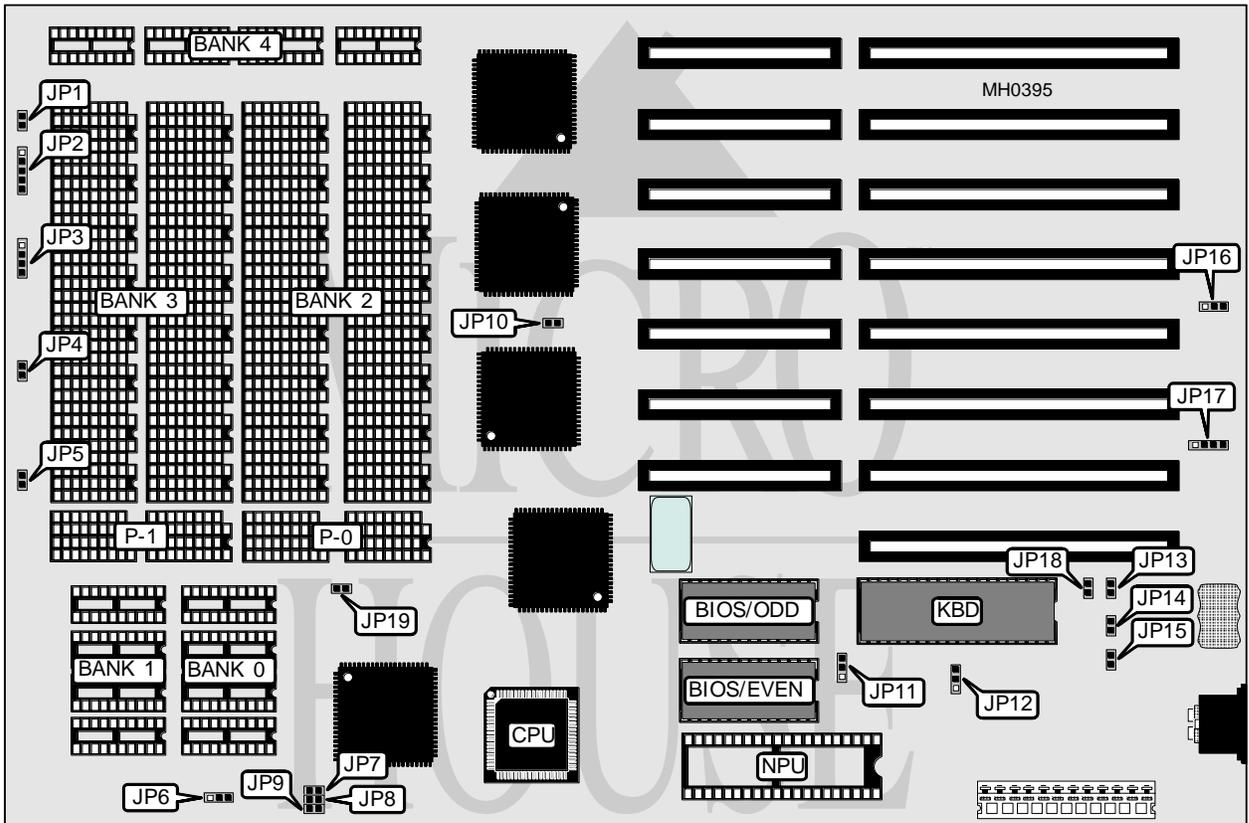
CONNECTIONS			
Purpose	Location	Purpose	Location
Reset switch	JP1	Turbo switch	JP4
Power LED & keylock	JP2	Turbo LED	JP5
Speaker	JP3	External battery	JP17

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í DRAM speed select 100ns	JP6	pins 1 & 2 closed
DRAM select 0 wait states	JP10	Closed
DRAM select 1 wait state	JP10	Open
í BIOS type select 27256	JP11	pins 1 & 2 closed
BIOS type select 27128	JP11	pins 2 & 3 closed
í Monitor type select color	JP12	pins 1 & 2 closed
Monitor type select monochrome	JP12	pins 2 & 3 closed
Keyboard controller set on pin 30	JP13	Closed
Keyboard controller set on pin 23	JP14	Closed
Keyboard controller set on pin 27	JP14	Closed
Keyboard controller set on pin 32	JP18	Closed
Power good signal detect	JP16	N/A
í Parity check enabled	JP19	pins 1 & 2 closed
Parity check disabled	JP19	pins 2 & 3 closed

DRAM CONFIGURATION						
Size	Bank 0	Bank 1	Bank 2	Bank 3	Bank 4	P-0	P-1
512KB	(4) 44256	NONE	NONE	NONE	NONE	(2) 41256	NONE
640KB	(4) 44256	NONE	NONE	NONE	(4) 4464	(2) 41256	(2) 4164
640KB	(4) 44256	NONE	NONE	(16) 4164	NONE	(2) 41256	(2) 4164
1MB	(4) 44256	(4) 44256	NONE	NONE	NONE	(2) 41256	(2) 41256
2MB	NONE	NONE	(16) 411000	NONE	NONE	(2) 411000	NONE
4MB	NONE	NONE	(16) 411000	(16) 411000	NONE	(2) 411000	(2) 411000

DRAM JUMPER CONFIGURATION			
Size	JP7	JP8	JP9
512KB	Closed	Closed	Closed
640KB	Closed	Closed	Open
1MB	Closed	Open	Closed
2MB	Closed	Open	Open
4MB	Open	Closed	Closed