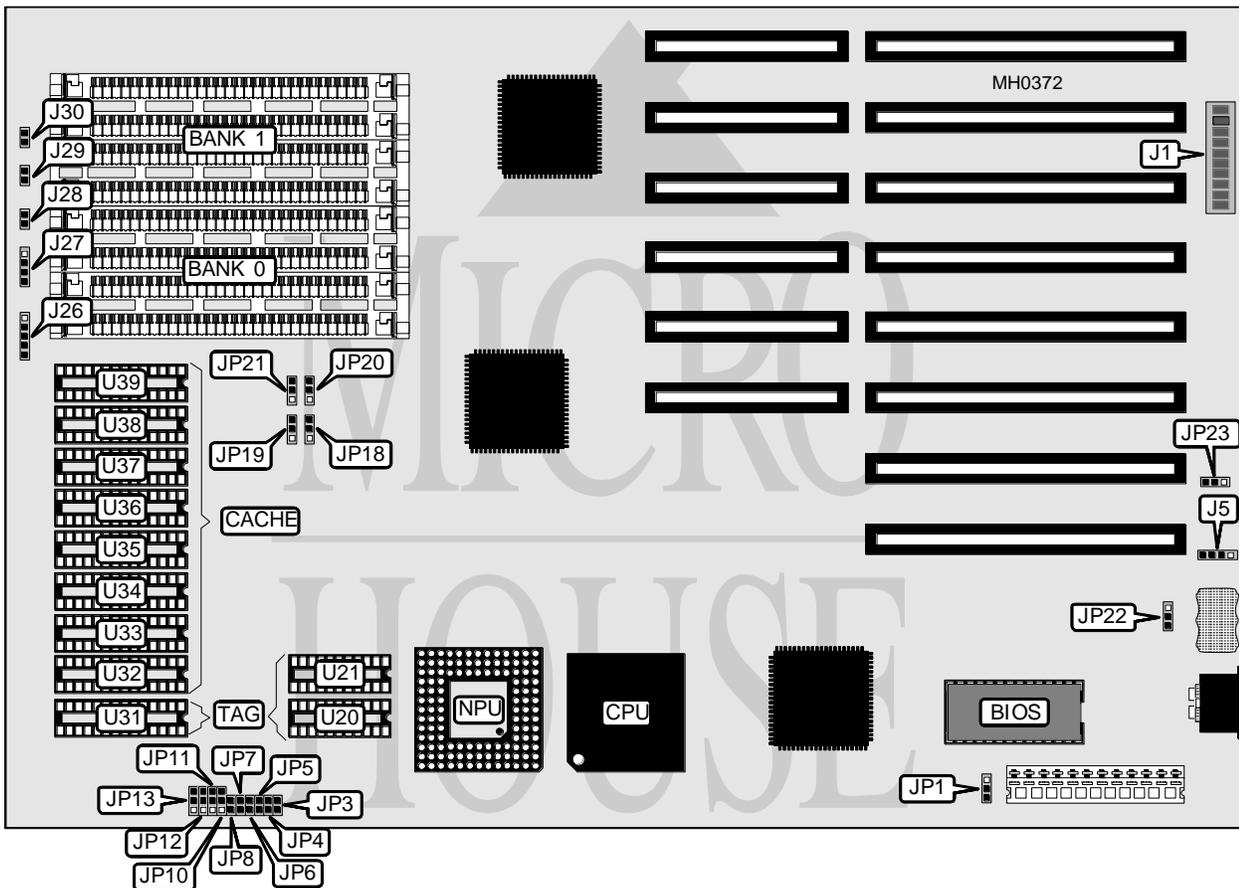


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UB333/40-DDIC

Processor	80386DX
Processor Speed	33/40MHz
Chip Set	C & T
Max. Onboard DRAM	32MB
SRAM Cache	32/64/128/256KB
BIOS	AMI/C & T
Dimensions	335mm x 220mm
I/O Options	None
NPU Options	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
POST LEDs	J1	Turbo LED	J28
External battery	J5	Reset switch	J29
Power LED & keylock	J26	Turbo switch	J30
Speaker	J27		

Continued on next page . . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select monochrome	JP1	pins 2 & 3 closed
Monitor type select color	JP1	pins 1 & 2 closed
í CMOS memory normal operation	JP22	pins 2 & 3 closed
CMOS memory clear	JP22	pins 1 & 2 closed
í Battery select internal	JP23	pins 1 & 2 closed
Battery select external	JP23	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG (U20, U21)	TAG (U31)
32KB	(4) 8K x 8	NONE	NONE	(1) 8K x 8
64KB	(4) 8K x 8	(4) 8K x 8	(2) 16K x 4	NONE
128KB	(4) 32K x 8	NONE	NONE	(1) 32K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(2) 64K x 4	NONE

Note: Bank 0 = U32, U35, U37 & U39. Bank 1 = U33, U34, U36 & U38.

CACHE JUMPER CONFIGURATION					
Size	JP3	JP4	JP5	JP6	JP7
32KB	Closed	Open	Open	Open	Closed
64KB	Closed	Closed	Open	Open	Closed
128KB	Closed	Closed	Open	Open	Open
256KB	Closed	Closed	Closed	Closed	Open

CACHE JUMPER CONFIGURATION (CON'T)					
Size	JP8	JP10	JP11	JP12	JP13
32KB	Closed	1 & 2	Open	2 & 3	Open
64KB	Closed	1 & 2	Open	Open	1 & 2
128KB	Closed	2 & 3	1 & 2	Open	1 & 2
256KB	Open	2 & 3	1 & 2	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CACHE JUMPER CONFIGURATION (CON'T)				
Size	JP18	JP19	JP20	JP21
32KB	pins 2 & 3 closed	Open	Open	pins 2 & 3 closed
64KB	pins 2 & 3 closed	Open	pins 1 & 2 closed	pins 1 & 2 closed
128KB	pins 1 & 2 closed	Closed	Open	pins 2 & 3 closed
256KB	pins 1 & 2 closed	Closed	pins 2 & 3 closed	pins 1 & 2 closed

