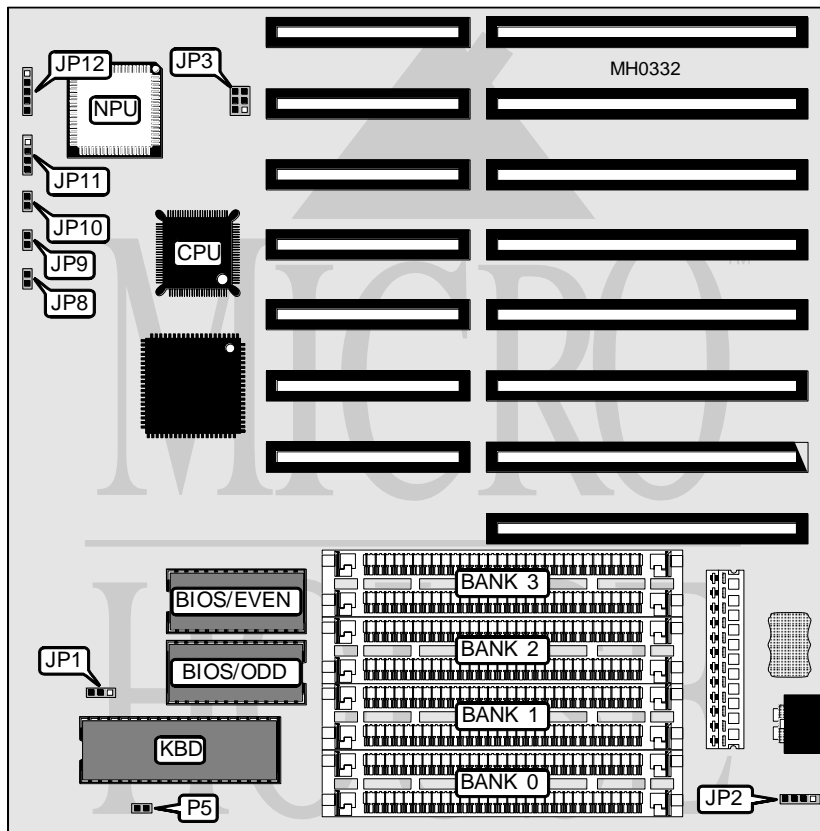


MULTIBEST INDUSTRIAL & MANUFACTURING, INC.

M B 3 8 6 S X 3

Processor	80386SX
Processor Speed	16/20/25MHz
Chip Set	Headland
Max. Onboard DRAM	8MB
Cache	None
BIOS	AMI
Dimensions	218mm x 218mm
I/O Options	None
NPU Options	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP2	Turbo switch	JP10
Turbo LED	JP8	Speaker	JP11
Reset switch	JP9	Power LED & keylock	JP12

Continued on next page . . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 1 & 2 closed
CMOS memory clear	JP1	pins 2 & 3 closed
í Factory configured - do not alter	JP3	N/A
í Monitor type select color	P5	Closed
Monitor type select monochrome	P5	Open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(2) 256K x 9	NONE	NONE	NONE
1MB	(2) 256K x 9	NONE	(2) 256K x 9	NONE
1.5MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	NONE
2MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE	NONE	NONE
3MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
4.5MB	(2) 1M x 9	(2) 1M x 9	(2) 256K x 9	NONE
5MB	(2) 1M x 9	(2) 1M x 9	(2) 256K x 9	(2) 256K x 9
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	NONE
8MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9