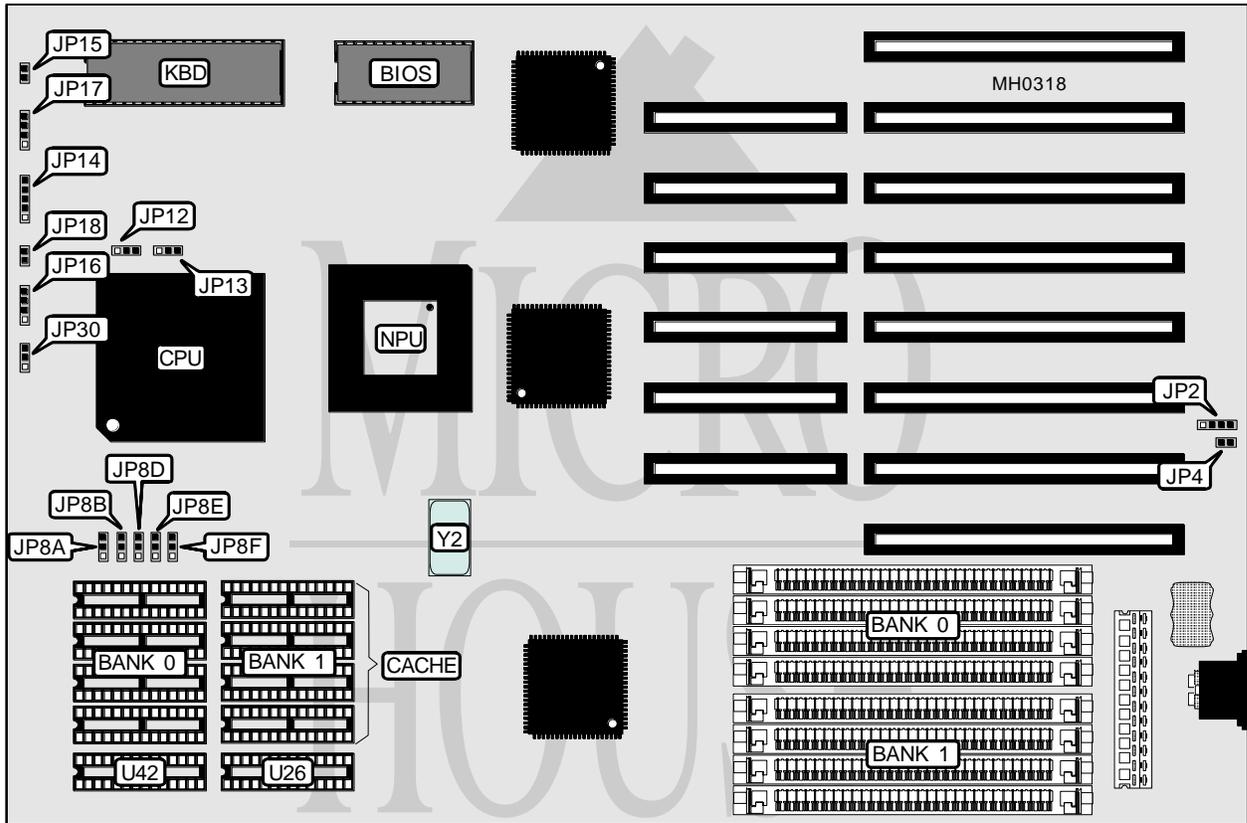


MORSE TECHNOLOGIES, INC.

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Processor	80486SX/80487SX/80486DX
Processor Speed	16/20/25/33MHz
Chip Set	Morse
Max. Onboard DRAM	16MB
Cache	64/128/256KB
BIOS	AMI/Award
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP2	Turbo LED	JP16
Power LED & keylock	JP14	Speaker	JP17
Turbo switch	JP15	Reset switch	JP18

Continued on next page . . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery select internal 3.6VDC	JP4	Closed
Battery select external 6VDC	JP4	Open
í Parity check enabled	JP5	Closed
Parity check disabled	JP5	Open
í Monitor type select color	JP6	Closed
Monitor type select monochrome	JP6	Open
í CMOS memory normal operation	JP7	Open
CMOS memory clear	JP7	Closed
í Keyboard diagnostic disabled	JP19	Open
Keyboard diagnostic enabled	JP19	Closed

Note: Locations of JP5, JP6, JP7, & JP19 are unidentified.

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG (U26)	TAG (U42)
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	NONE
128KB	NONE	(4) 32K x 8	(1) 8K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8	(1) 8K x 8	(1) 8K x 8

SRAM JUMPER CONFIGURATION					
Size	JP8A	JP8B	JP8D	JP8E	JP8F
64KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
128KB	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3
256KB	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION			
Type	JP12	JP13	JP30
80486SX	Pins 2 & 3 closed	pins 2 & 3 closed	Open
80487SX	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
80486DX	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed

Note: When changing the CPU, the Oscillator at Y2 must also be changed so that its speed is equal to twice the speed of the processor. e.g. A 25MHz CPU would require a 50MHz Oscillator.