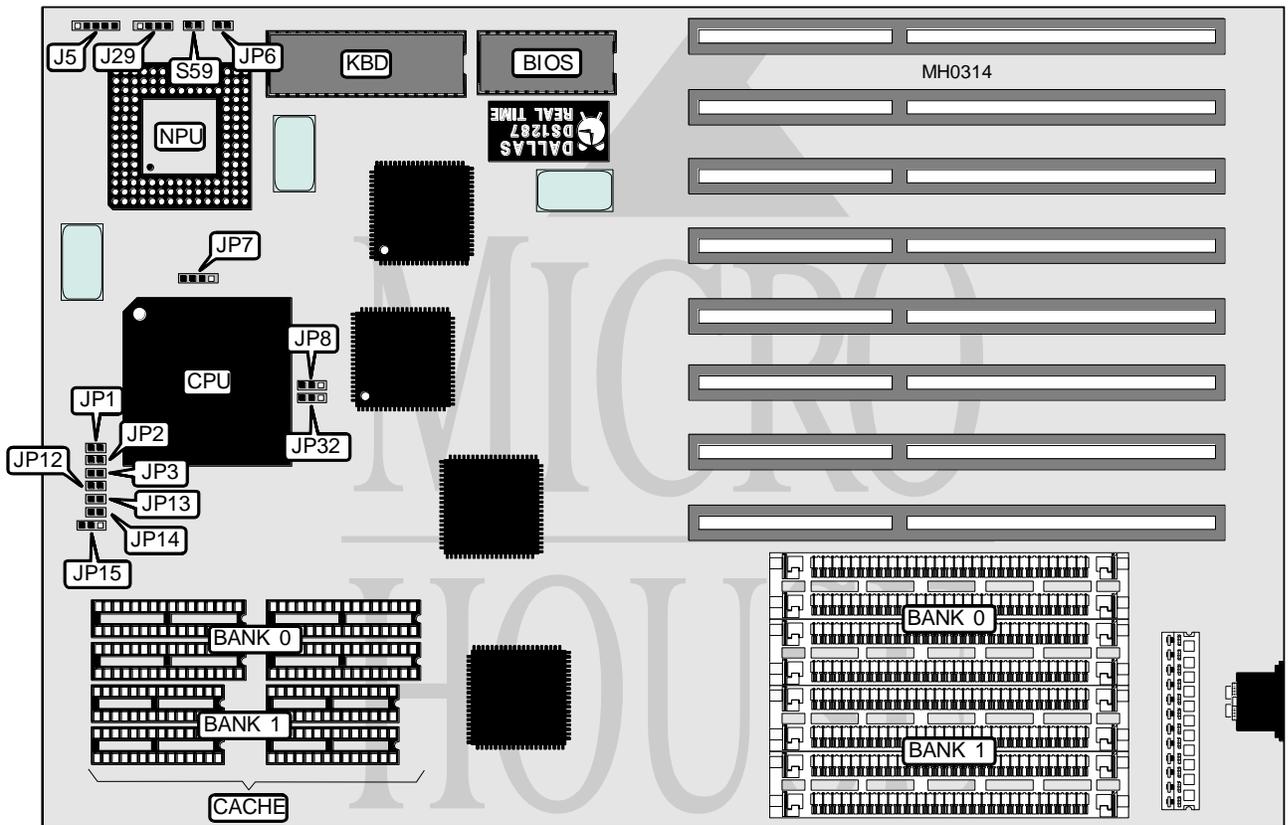


# INTERCOMP, INC.

## EISA 486

<b>Processor</b>	80486SX/80487SX/80486DX
<b>Processor Speed</b>	20/25/33/50MHz
<b>Chip Set</b>	OPTI
<b>Max. Onboard DRAM</b>	128MB
<b>Cache</b>	64/128/256/512KB
<b>BIOS</b>	AMI/MR/Award
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	None
<b>NPU Options</b>	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J10	Reset switch	S59
Speaker	J29		

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**INTERCOMP, INC.**  
**EISA 486**

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP6	Closed
Monitor type select monochrome	JP6	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE
128MB	(4) 16M x 9	(4) 16M x 9

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
64KB	(4) 8K x 8	(4) 8K x 8
128KB	(4) 32K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8
512KB	(4) 128K x 8	NONE

CACHE JUMPER CONFIGURATION							
Size	JP1	JP2	JP3	JP12	JP13	JP14	JP15
64KB	Open	Open	Open	Open	Open	Open	pins 2 & 3 closed
128KB	Closed	Open	Open	Closed	Open	Open	pins 1 & 2 closed
256KB	Closed	Closed	Open	Closed	Open	Closed	pins 2 & 3 closed
512KB	Closed	Closed	Closed	Closed	Closed	Closed	pins 1 & 2 closed

CPU TYPE CONFIGURATION			
Type	JP7	JP8	JP32
80486SX	pins 2 & 3 closed	Open	pins 2 & 3 closed
80487SX	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed	pins 2 & 3 closed
80486DX	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed	pins 1 & 2 closed