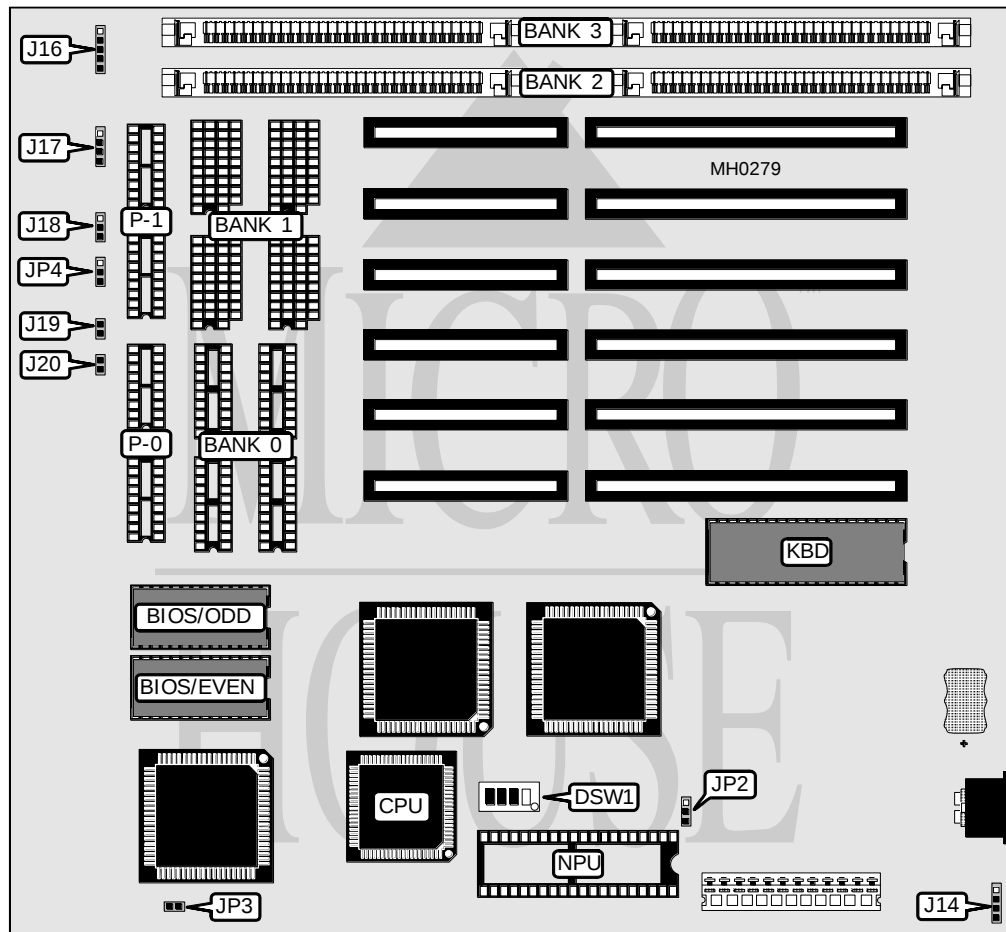


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Processor	80286
Processor Speed	12/16MHz
Chip Set	Unidentified
Max. Onboard DRAM	4MB
Cache	None
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J14	Turbo LED	J19
Power LED & keylock	J16	Reset switch	J20
Speaker	J17	Turbo switch	JP4

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Monitor type select color	DSW1/1	On
Monitor type select monochrome	DSW1/1	Off
í DRAM speed select 0 wait states	J18	pins 2 & 3 closed
DRAM speed select 1 wait state	J18	Open
í NPU speed select 25MHz	JP2	pins 1 & 2 closed
NPU speed select synchronous with CPU clock	JP2	pins 2 & 3 closed
í Parity check enabled	JP3	Closed
Parity check disabled	JP3	Open
Note: The location of DSW1 is unidentified.		

DRAM CONFIGURATION						
Size	BANK 0	P-0	BANK 1	P-1	Bank 2	Bank 3
512KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
512KB	NONE	NONE	NONE	NONE	(2) 256K x 9	NONE
640KB	(4) 44256	(2) 41256	(4) 4464	(2) 4164	NONE	NONE
640KB	NONE	NONE	(4) 4464	(2) 4164	(2) 256K x 9	NONE
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
1MB	(4) 44256	(2) 41256	NONE	NONE	NONE	(2) 256K x 9
1MB	NONE	NONE	(4) 44256	(2) 41256	(2) 256K x 9	NONE
1MB	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 256K x 9
2MB	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
4MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9

DRAM SWITCH CONFIGURATION			
Size	DSW1/2	DSW1/3	DSW1/4
512KB	On	On	Off
640KB	On	Off	Off
1MB	On	Off	Off
2MB	Off	Off	On
4MB	Off	Off	Off