ATRONICS INTERNATIONAL, INC.
A T I - 4 8 6 / B 2

Processor	80486SX/80487SX/80486DX
Processor Speed	20/25/33/40/50MHz
Chip Set	UMC
Max. Onboard DRAM	32MB
Cache	32/64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card
NPU Options	4167
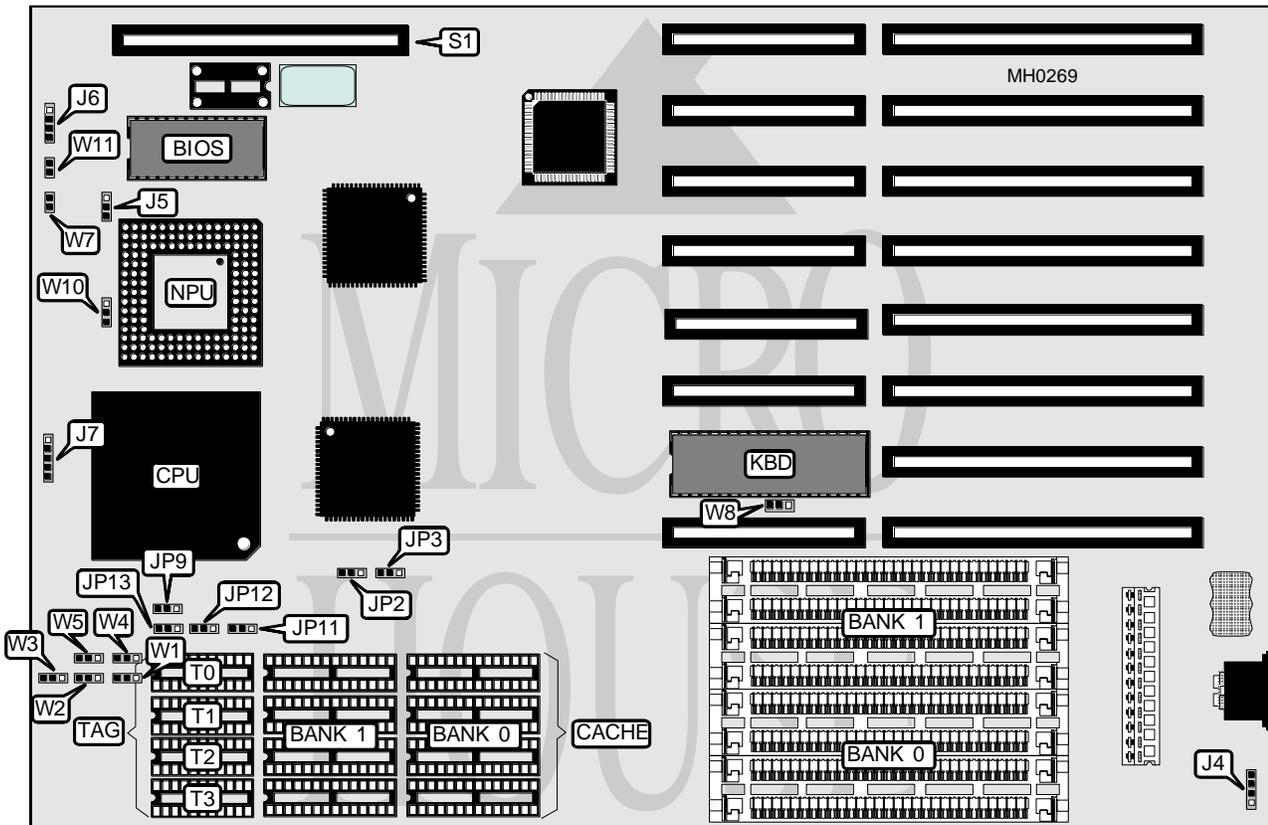
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J4	32-bit external memory card	S1
Turbo switch	J5	Reset switch	W7
Speaker	J6	Turbo LED	W11
Power LED & Keylock	J7		

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ATI-486/B2

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select monochrome	W8	pins 1 & 2 closed
Monitor type select color	W8	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

DRAM JUMPER CONFIGURATION		
Type	JP2	JP3
Onboard and/or external card	pins 1 & 2 closed	pins 1 & 2 closed
External card only	pins 2 & 3 closed	pins 2 & 3 closed

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG (T0,T1,T3)
32KB	(4) 8K x 8	NONE	(3) 16K x 4
64KB	(4) 8K x 8	(4) 8K x 8	(3) 16K x 4
128KB	(4) 32K x 8	NONE	(3) 16K x 4
256KB	(4) 32K x 8	(4) 32K x 8	(3) 16K x 4
Note: A 16K x 4 chip is used in DIP socket T2 only when cacheable DRAM exceeds 16MB.			

CACHE JUMPER CONFIGURATION						
Size	W1	W2	W3	W4	W5	W10
32KB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
64KB	2 & 3	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
128KB	1 & 2	1 & 2	2 & 3	1 & 2	2 & 3	2 & 3
256KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
Note: Pins designated should be in the closed position.						

CPU TYPE CONFIGURATION				
Type	JP9	JP11	JP12	JP13
80486SX	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
80487SX	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
80486DX	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed