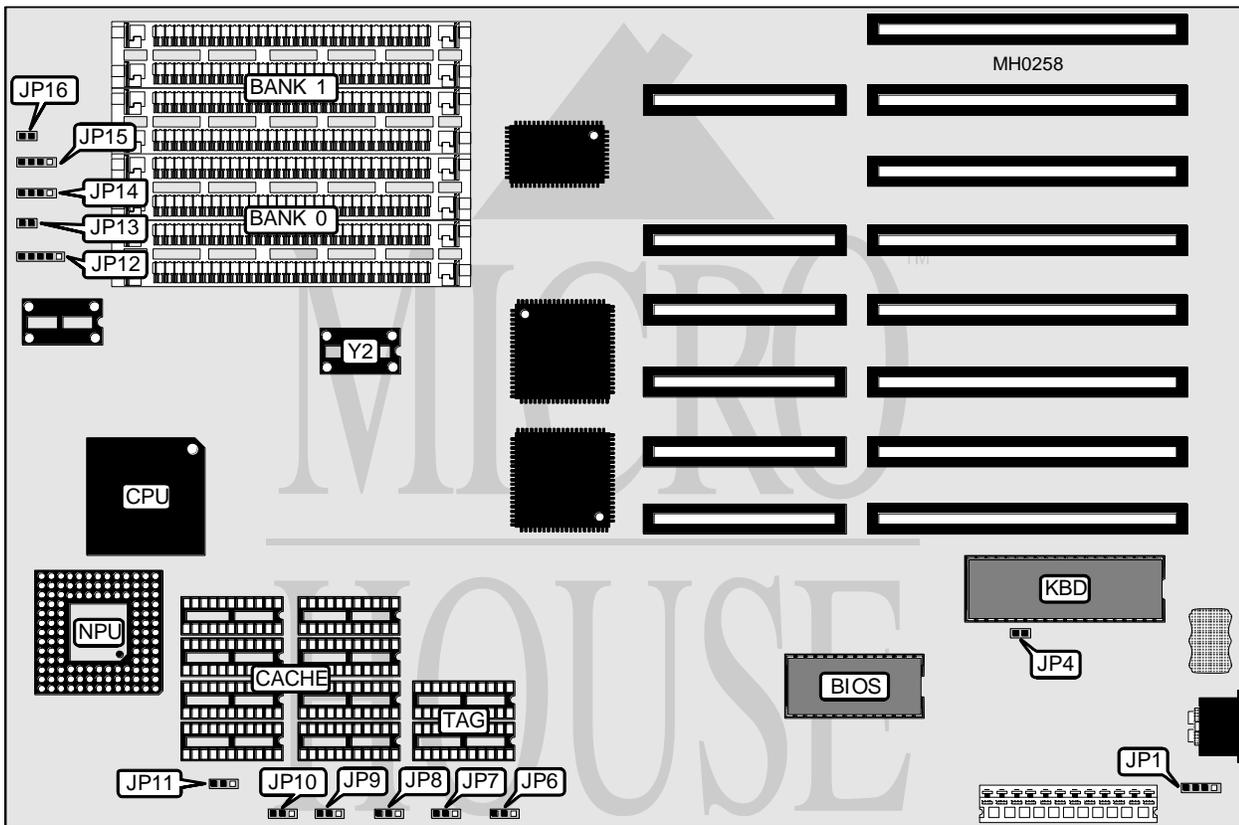


GENOA SYSTEMS CORPORATION

386 BABY AT (32 MB)

Processor	80386DX
Processor Speed	33/40MHz
Chip Set	UMC
Max. Onboard DRAM	32MB
Cache	64/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP1	Turbo LED cathode	JP14/pins 2 & 4
Power LED & keylock	JP12	Speaker	JP15
Turbo switch	JP13	Reset switch	JP16
Turbo LED anode	JP14/pins 1 & 3		

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GENOA SYSTEMS CORPORATION

386 BABY AT (32MB)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select EGA/VGA	JP4	open
Monitor type select CGA	JP4	closed
í NPU mode select synchronous with CPU	JP11	pins 1 & 2 closed
NPU mode select synchronous with oscillator installed at Y2	JP11	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Cache CACHE	TAG	JP6 - JP10
64KB	(8) 16K x 4	(2) 16K x 4	pins 1 & 2 closed
256KB	(8) 64K x 4	(2) 64K x 4	pins 2 & 3 closed