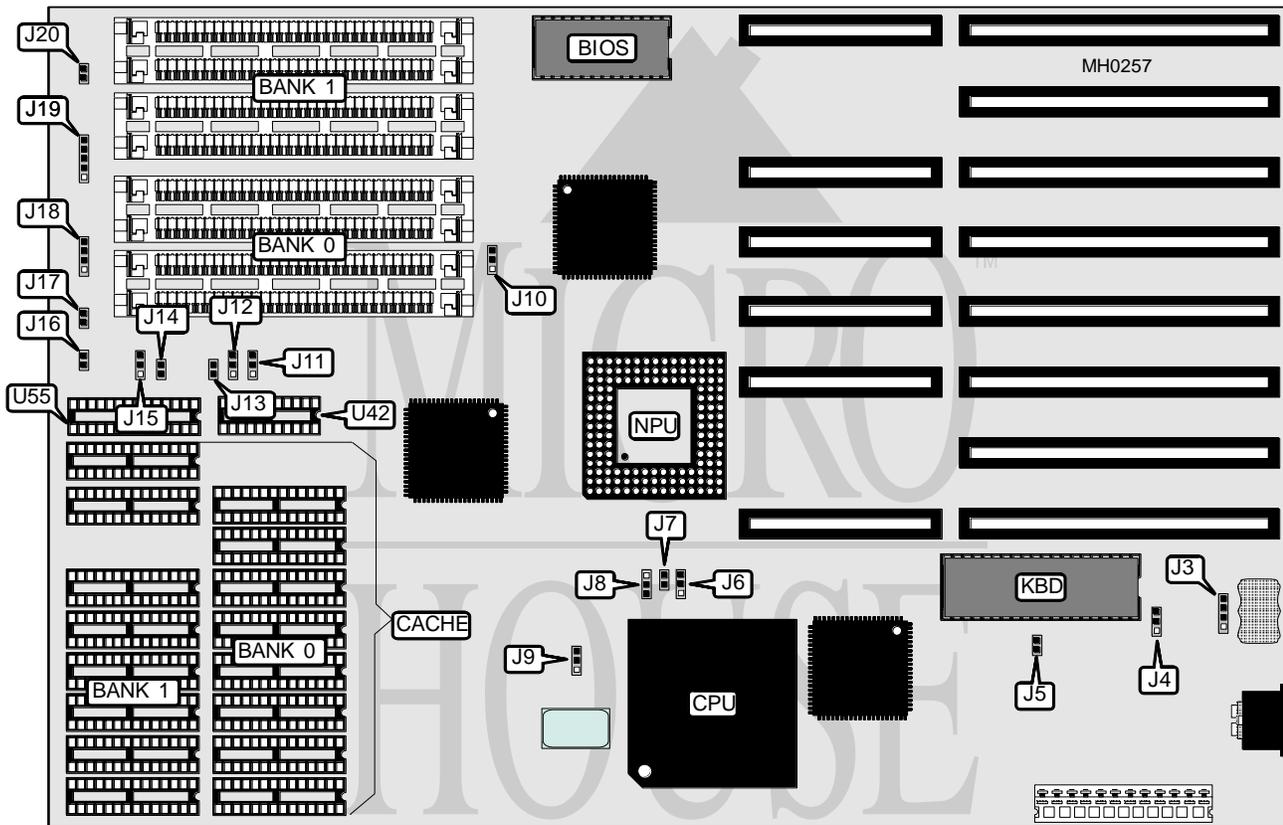


# UNIDENTIFIED PMS486DS

**Processor** 80486DX  
**Processor Speed** 20/25/33/50MHz  
**Chip Set** OPTI  
**Max. Onboard DRAM** 32MB  
**Cache** 64/128/256/512KB  
**BIOS** AMI  
**Dimensions** 330.2mm x 218.4mm  
**I/O Options** None  
**NPU Options** 4167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J3	Speaker	J18
Turbo switch	J16	Power LED & keylock	J19
Reset switch	J17	Turbo LED	J20

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## PMS486DS

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	J4	pins 2 & 3 closed
CMOS memory clear	J4	pins 1 & 2 closed
í Monitor type select color	J5	closed
Monitor type select monochrome	J5	open
í CPU speed select iOSC/2	J9	pins 2 & 3 closed
CPU speed select iOSC	J9	pins 1 & 2 closed
Bus speed select ATCLK/6	J10	closed
Bus speed select ATCLK/4	J10	open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION				
Size	Cache CACHE	Location	TAG (U42)	TAG (U55)
64KB	(8) 8K x 8	Bank 0	(1) 64K x 1	(1) 8K x 8
128KB	(16) 8K x 8	Banks 0 & 1	(1) 64K x 1	(1) 8K x 8
256KB	(8) 32K x 8	Bank 0	(1) 64K x 1	(1) 32K x 8
512KB	(16) 32K x 8	Banks 0 & 1	(1) 64K x 1	(1) 32K x 8

CACHE JUMPER CONFIGURATION					
Size	J11	J12	J13	J14	J15
64KB	open	open	open	open	pins 2 & 3 closed
128KB	open	open	closed	closed	pins 2 & 3 closed
256KB	open	closed	closed	closed	pins 2 & 3 closed
512KB	closed	closed	closed	closed	pins 1 & 2 closed

CPU JUMPER CONFIGURATION			
CPU	J6	J7	J8
80486DX	pins 1 & 2 closed	closed	pins 2 & 3 closed
80487SX	pins 2 & 3 closed	closed	pins 2 & 3 closed
80486SX	open	open	pins 1 & 2 closed