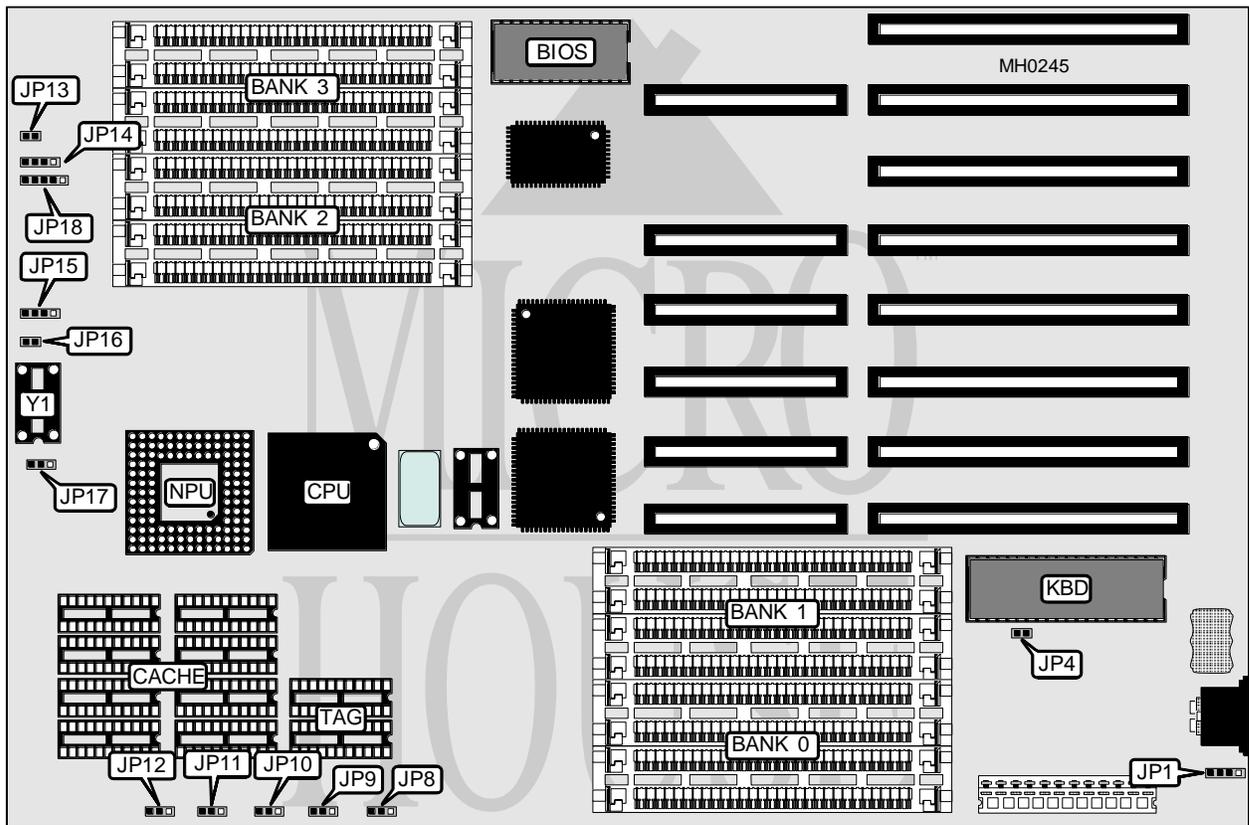


GENOA SYSTEMS CORPORATION

386 BABY AT (64MB)

Processor	80386DX
Processor Speed	33/40MHz
Chip Set	Genoa
Max. Onboard DRAM	64MB
Cache	64/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP1	Turbo LED cathode	JP15 pins 2 & 4
Reset switch	JP13	Turbo switch	JP16
Speaker	JP14	Power LED & keylock	JP18
Turbo LED anode	JP15 pins 1 & 3		

Continued on next page . . .

GENOA SYSTEMS CORPORATION

386 BABY AT (64MB)

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USER CONFIGURABLE SETTINGS

Function	Jumper	Position
í Monitor type select EGA/VGA	JP4	Open
Monitor type select CGA	JP4	Closed
í NPU mode select synchronous with CPU	JP17	pins 1 & 2 closed
NPU mode select synchronous with oscillator installed at Y1	JP17	pins 2 & 3 closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 256K x 9	(4) 1M x 9	NONE	NONE
6MB	(4) 256K x 9	(4) 256K x 9	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
9MB	(4) 256K x 9	(4) 1M x 9	(4) 1M x 9	NONE
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE	NONE	NONE
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE
24MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
36MB	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9	NONE
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE
64MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION

Size	Bank 0	TAG
64KB	(8) 16K x 4	(2) 16K x 4
256KB	(8) 64K x 4	(2) 64K x 4

CACHE JUMPER CONFIGURATION

Size	JP8	JP9	JP10	JP11	JP12
64KB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
256KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.