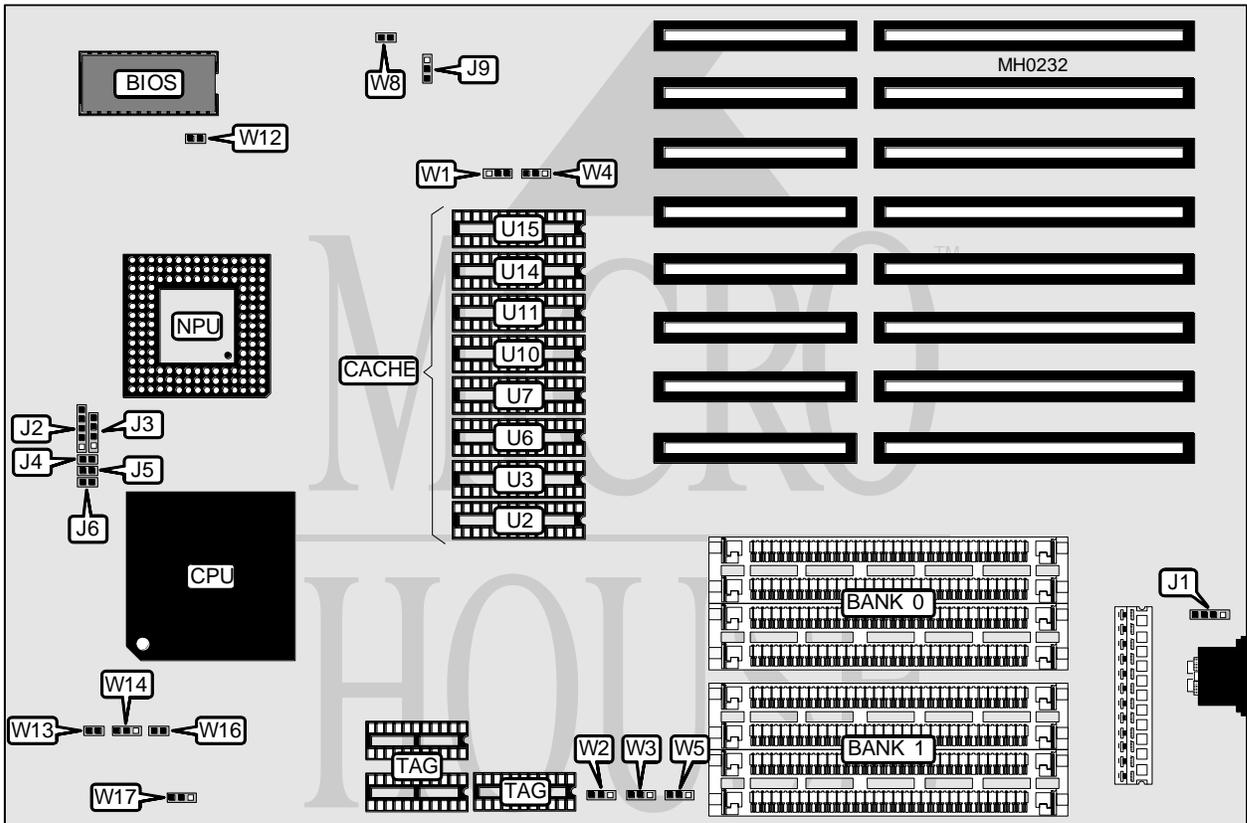


SILICON VALLEY TECHNOLOGY

486DX-33

Processor	80486SX/80487SX/80486DX
Processor Speed	20/25/33MHz
Chip Set	UMC
Max. Onboard DRAM	32MB
Cache	32/64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	4167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo LED	J4
Power LED & keylock	J2	Turbo switch	J5
Speaker	J3	Reset switch	J6

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SILICON VALLEY TECHNOLOGY

486DX-33

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	J9	pins 1 & 2 closed
CMOS memory clear	J9	pins 2 & 3 closed
í Monitor type select color	W8	Closed
Monitor type select monochrome	W8	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(3) 16K x 4
64KB	(4) 8K x 8	(4) 8K x 8	(3) 16K x 4
128KB	(4) 32K x 8	NONE	(3) 16K x 4
256KB	(4) 32K x 8	(4) 32K x 8	(3) 16K x 4

Bank 0 = U2, U6, U10 & U14, Bank 1 = U3, U7, U11 & U15.

CACHE JUMPER CONFIGURATION					
Size	W1	W2	W3	W4	W5
32KB	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2
64KB	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3
128KB	2 & 3	1 & 2	2 & 3	2 & 3	2 & 3
256KB	2 & 3	2 & 3	2 & 3	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION					
Type	W12	W13	W14	W16	W17
80486SX	Open	Open	2 & 3	Closed	Open
80487SX	Closed	Closed	1 & 2	Closed	2 & 3
80486DX	Closed	Closed	1 & 2	Closed	1 & 2

Note: Pins designated should be in the closed position.