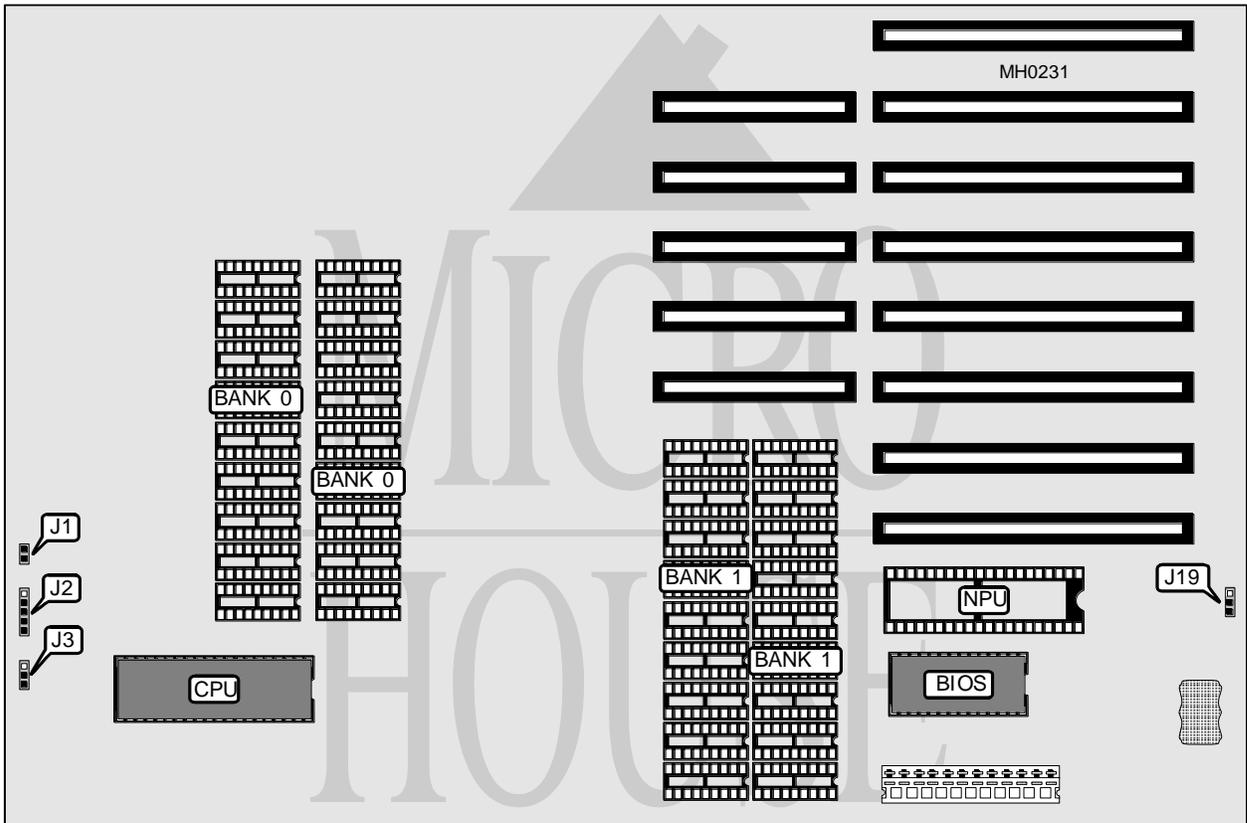


PACKARD BELL P B 6 3 5

Processor 80286
Processor Speed 12MHz
Chip Set C & T
Max. Onboard DRAM 1MB
Cache None
BIOS MH
Dimensions 330mm x 218mm
I/O Options None
NPU Options 80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Reset switch	J1	Speaker	J3
Keylock	J2		

Continued on next page . . .

PACKARD BELL
P B 6 3 5

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í NPU mode select synchronous with CPU	J19	pins 1 & 2 closed
NPU mode select asynchronous with CPU	J19	pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(18) 41256	NONE	NONE	NONE
1MB	(18) 41256	(18) 41256	NONE	NONE
2MB	(18) 41256	(18) 41256	(2) 256K x 9	(2) 256K x 9
3MB	(18) 41256	(18) 41256	(2) 1M x 9	NONE
5MB	(18) 41256	(18) 41256	(2) 1M x 9	(2) 1M x 9
Note: Banks 2 & 3 are used when an external memory card is installed.				