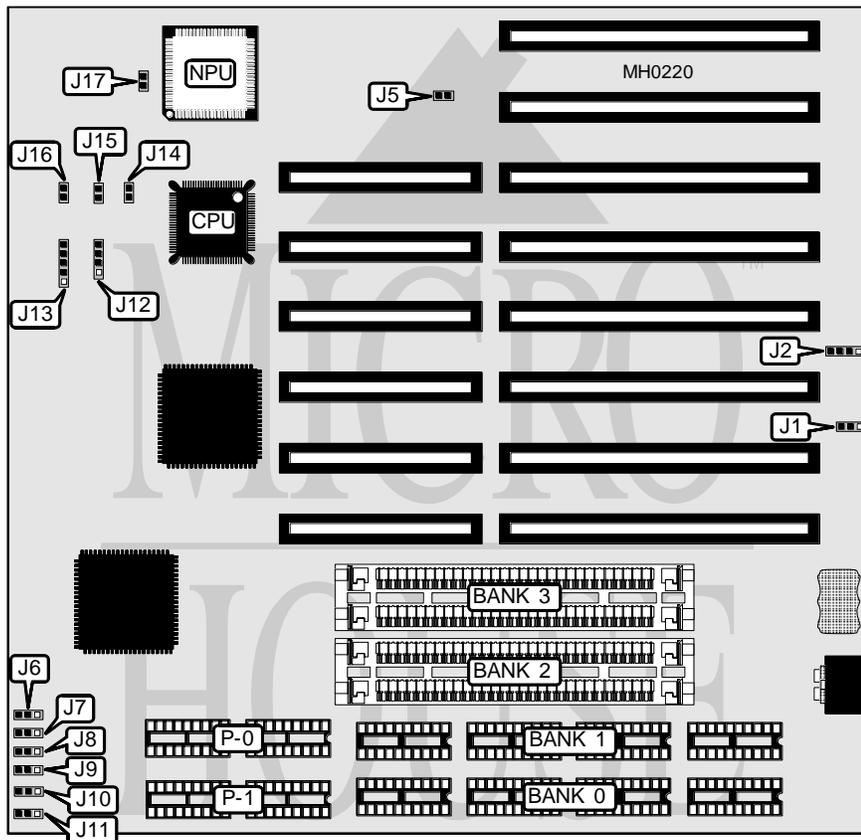


# AMERICAN DIGICOM CORPORATION

## P 9 - H

<b>Processor</b>	80386SX
<b>Processor Speed</b>	16/20/25MHz
<b>Chip Set</b>	OPTI
<b>Max. Onboard DRAM</b>	16MB
<b>Cache</b>	None
<b>BIOS</b>	AMI
<b>Dimensions</b>	230mm x 220mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Reset switch	J14
Speaker	J12	Turbo switch	J15
Power LED & keylock	J13	Turbo LED	J16

Continued on next page . . .

## AMERICAN DIGICOM CORPORATION

## P9 - H

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	J1	pins 2 & 3 closed
CMOS memory clear	J1	pins 1 & 2 closed
í Monitor type select color	J5	Closed
Monitor type select monochrome	J5	Open
í NPU mode select synchronous with CPU	J17	Open
NPU mode select asynchronous with CPU	J17	Closed

DRAM CONFIGURATION						
Size	Bank 0	P-0	Bank 1	P-1	Bank 2	Bank 3
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
1MB	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 256K x 9
2MB	(4) 441000	(2) 411000	NONE	NONE	NONE	NONE
2MB	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
3MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 1M x 9	NONE
4MB	(4) 441000	(2) 411000	(4) 441000	(2) 411000	NONE	NONE
4MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9
5MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 1M x 9	(2) 1M x 9
6MB	(4) 441000	(2) 411000	(4) 441000	(2) 411000	(2) 1M x 9	NONE
8MB	(4) 441000	(2) 411000	(4) 441000	(2) 411000	(2) 1M x 9	(2) 1M x 9
9MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	(2) 4M x 9	NONE
10MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 4M x 9
12MB	(4) 441000	(2) 411000	(4) 441000	(2) 411000	(2) 4M x 9	NONE
16MB	NONE	NONE	NONE	NONE	(2) 4M x 9	(2) 4M x 9

Notes: Banks 2 & 3 are considered Banks 0 & 1, respectively, when filled without utilizing DIP.  
However, they will be referred to throughout the text as Banks 2 & 3 to avoid confusion.

DRAM CONFIGURATION						
Size	J6	J7	J8	J9	J10	J11
1MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
1MB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
2MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
2MB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
3MB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
4MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
4MB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
5MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
6MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
8MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
9MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
10MB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
12MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
16MB	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2

Note: Pins designated should be in the closed position.