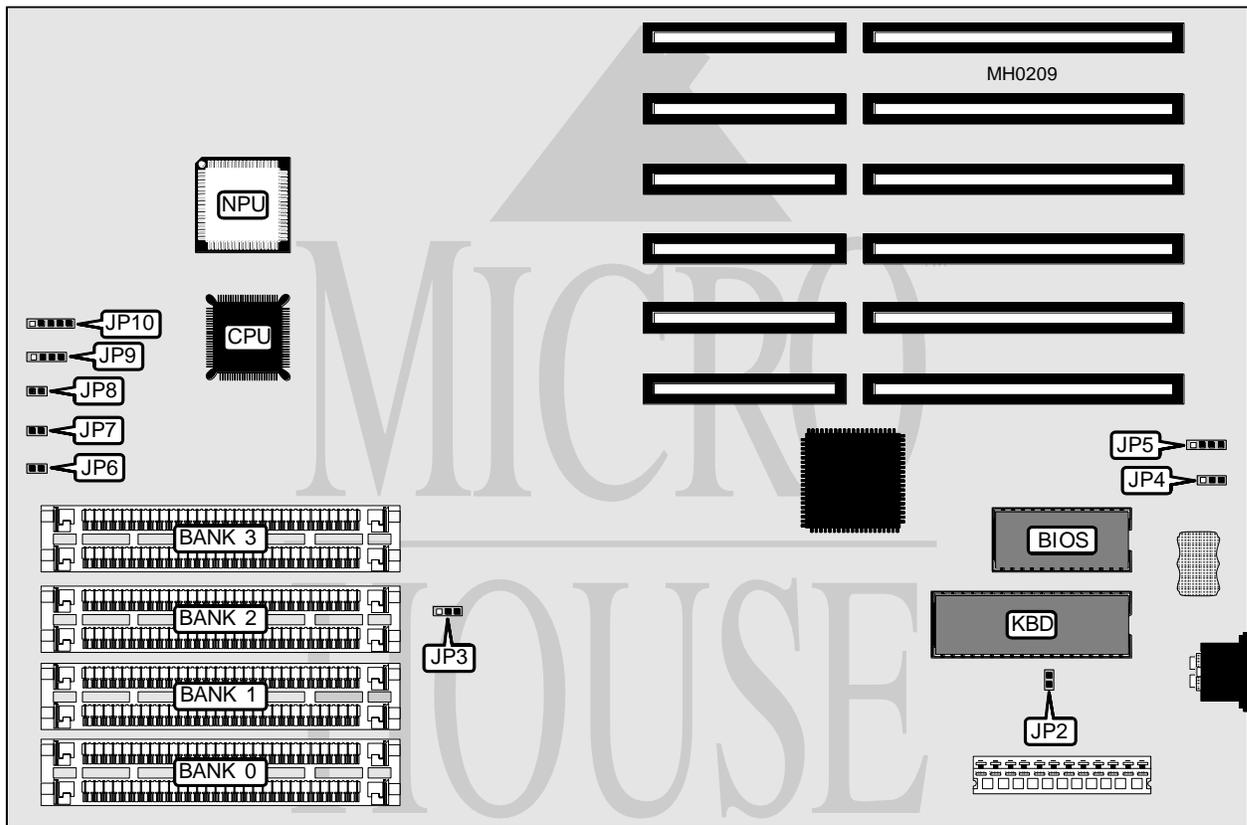


# SER 386CS

<b>Processor</b>	80386SX
<b>Processor Speed</b>	20/25MHz
<b>Chip Set</b>	C & T
<b>Max. Onboard DRAM</b>	16MB
<b>Cache</b>	None
<b>BIOS</b>	AMI
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	None
<b>NPU Options</b>	80387SX



CONNECTIONS				
Purpose	Location	Purpose	Location	
External battery	JP5	Turbo LED	JP8	
Reset switch	JP6	Speaker	JP9	
Turbo switch	JP7	Power LED & keylock	JP10	

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# SER 386CS

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP2	Closed
Monitor type select monochrome	JP2	Open
í CMOS memory normal operation	JP4	pins 1 & 2 closed
CMOS memory clear	JP4	pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(2) 256K x 9	NONE	NONE	NONE
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
1.5MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	NONE
2MB	(2) 256K x 9			
2MB	(2) 1M x 9	NONE	NONE	NONE
2.5MB	(2) 256K x 9	(2) 1M x 9	NONE	NONE
3MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
4.5MB	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9	NONE
5MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	NONE
6.5MB	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9
8MB	(2) 1M x 9			
9MB	(2) 256K x 9	(2) 256K x 9	(2) 4M x 9	NONE
10MB	(2) 1M x 9	(2) 4M x 9	NONE	NONE
16MB	(2) 4M x 9	(2) 4M x 9	NONE	NONE

DRAM CONFIGURATION	
Size	JP3
512KB	pins 1 & 2 closed
1MB	pins 1 & 2 closed
1.5MB	pins 1 & 2 closed
2MB	pins 1 & 2 closed
2MB	pins 1 & 2 closed
2.5MB	pins 1 & 2 closed
3MB	pins 1 & 2 closed
4MB	pins 1 & 2 closed
4.5MB	pins 1 & 2 closed
5MB	pins 1 & 2 closed
6MB	pins 1 & 2 closed
6.5MB	pins 1 & 2 closed
8MB	pins 1 & 2 closed
9MB	*
10MB	*
16MB	pins 2 & 3 closed

\*Note: Pins 1 & 2 are closed when 256KB or 1MB SIMM's are used; pins 2 & 3 are closed when 4MB SIMM's are used. 9MB and 10MB memory configurations were available; however, we were unable to verify the correct settings.

