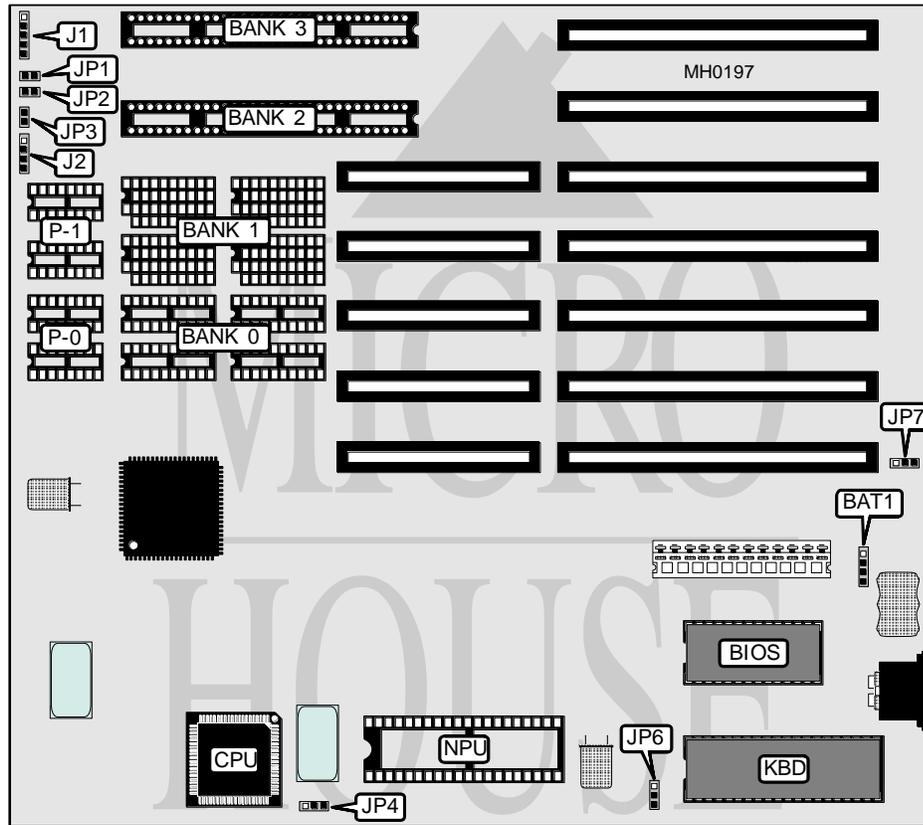


BIOSTAR MICROTECH INTERNATIONAL CORPORATION

MB-1212C (SIPP VERSION)

Processor 80286
Processor Speed 12MHz
Chip Set C & T
Max. Onboard DRAM 4MB
Cache None
BIOS AMI/C & T
Dimensions 244mm x 218mm
I/O Options None
NPU Options 80287



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	BAT1	Turbo LED	JP1
Power LED & keylock	J1	Reset switch	JP2
Speaker	J2	Turbo switch	JP3

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BIOSTAR MICROTECH INTERNATIONAL CORPORATION

MB-1212C (SIPP VERSION)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í NPU mode select synchronous with CPU	JP4	pins 1 & 2 closed
NPU mode select asynchronous with CPU	JP4	pins 2 & 3 closed
í Monitor type select color	JP6	pins 2 & 3 closed
Monitor type select monochrome	JP6	pins 1 & 2 closed
í Power good signal detect from power supply	JP7	pins 1 & 2 closed
Power good signal detect from board	JP7	pins 2 & 3 closed

DRAM CONFIGURATION						
Size	Bank 0	P-0	Bank 1	P-1	Bank 2	Bank 3
512KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
512KB	NONE	NONE	NONE	NONE	(2) 256K x 9	NONE
640KB	(4) 44256	(2) 41256	(4) 4464	(2) 4164	NONE	NONE
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
1MB	NONE	NONE	NONE	NONE	(2) 256K x 9	(2) 256K x 9
2MB	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
4MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9