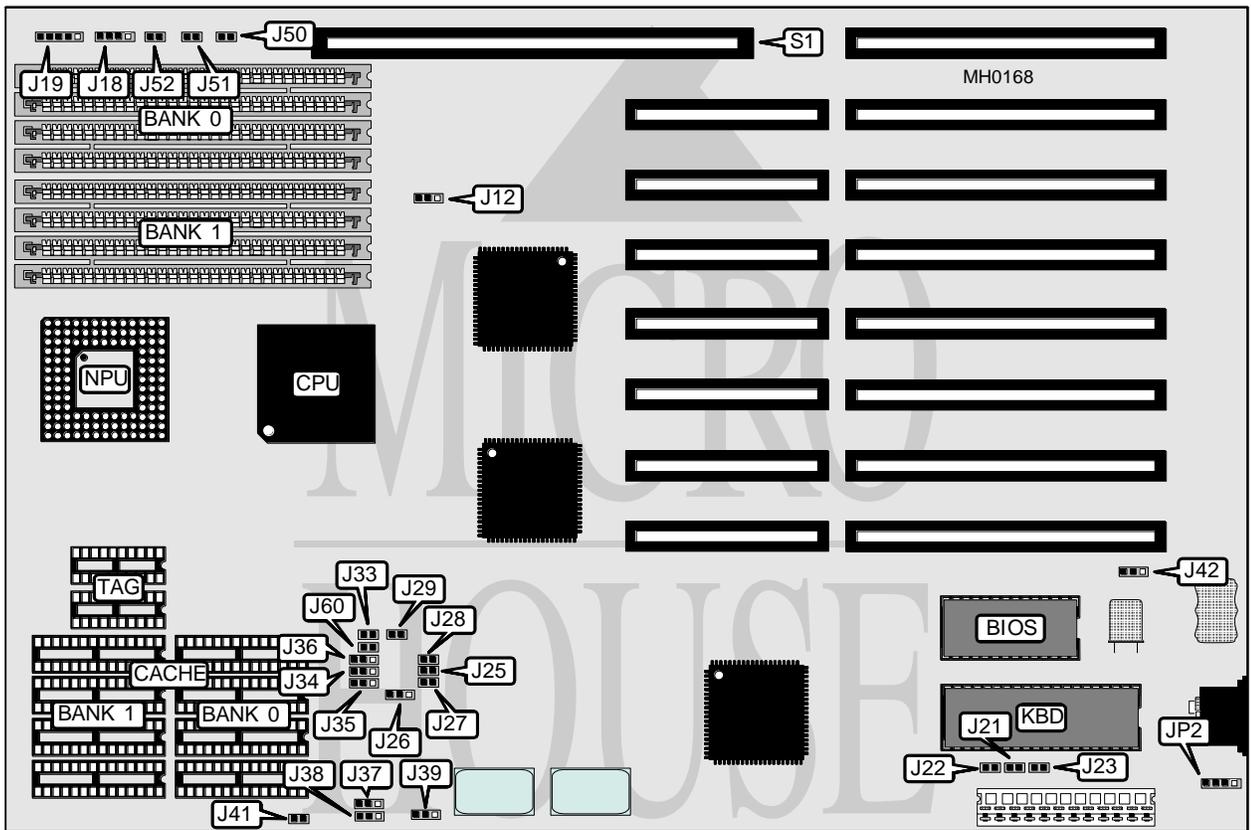


HORNET TECHNOLOGY CORPORATION

ACTP 833 386

Processor 80386DX
Processor Speed 25/33MHz
Chip Set C & T
Max. Onboard DRAM 32MB
Cache 32/64/128/256KB
BIOS AMI
Dimensions 330mm x 219mm
I/O Options 32-bit external memory card
NPU Options 80387/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J18	Turbo LED	J52
Power LED & keylock	J19	External battery	JP2
Turbo switch	J50	32-bit external memory card	S1
Reset switch	J51		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	J12	Open
í Monitor type select color	J23	Closed
Monitor type select monochrome	J23	Open
í CMOS memory normal operation	J42	pins 2 & 3 closed
CMOS memory clear	J42	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

DRAM CONFIGURATION	
Size	J12
1MB	pins 1 & 2 closed
2MB	pins 1 & 2 closed
4MB	pins 1 & 2 closed
5MB	pins 2 & 3 closed
8MB	pins 1 & 2 closed
16MB	pins 1 & 2 closed
17MB	pins 2 & 3 closed
20MB	pins 2 & 3 closed
32MB	pins 1 & 2 closed

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(2) 16K x 4
64KB	(4) 8K x 8	(4) 8K x 8	(2) 16K x 4
128KB	(4) 32K x 8	NONE	(2) 64K x 4
256KB	(4) 32K x 8	(4) 32K x 8	(2) 64K x 4

CACHE JUMPER CONFIGURATION							
Size	J25	J26	J27	J28	J29	J33	J34
32KB	Open	1 & 2	Open	Open	Closed	Closed	Open
64KB	Open	1 & 2	Closed	Open	Closed	Closed	1 & 2
128KB	Closed	2 & 3	Closed	Open	Closed	Open	1 & 2
256KB	Closed	2 & 3	Closed	Closed	Open	Open	1 & 2

Note: Pins designated should be in the closed position.

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CACHE JUMPER CONFIGURATION (CON'T)							
Size	J35	J36	J37	J38	J39	J41	J60
32KB	2 & 3	2 & 3	2 & 3	Open	2 & 3	Open	Open
64KB	1 & 2	2 & 3	2 & 3	1 & 2	1 & 2	Open	Open
128KB	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3	Closed	Open
256KB	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	Closed	Closed

Note: Pins designated should be in the closed position.