TOUCH, INC.
C A C H E   3 8 6   3 3 / 4 0

Processor	80386DX
Processor Speed	33/40MHz
Chip Set	Touch
Max. Onboard DRAM	8MB
Cache	32/64/128KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card slot
NPU Options	80387/3167
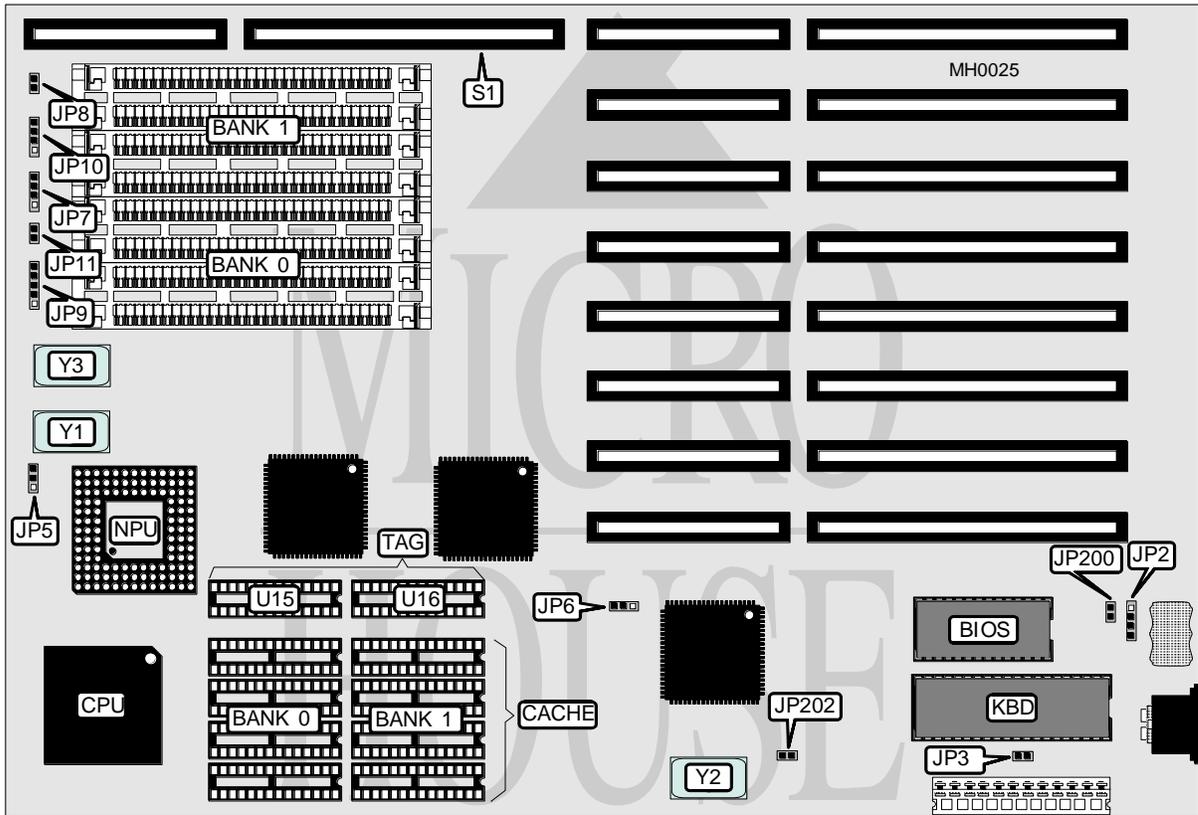
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP2	Speaker	JP10
Turbo LED	JP7	Turbo switch	JP11
Reset switch	JP8	32-bit external memory card	S1
Power LED & keylock	JP9		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP3	Open
Monitor type select monochrome	JP3	Closed
í NPU mode select synchronous with CPU	JP5	pins 1 & 2 closed
NPU mode select synchronous with oscillator installed at Y1	JP5	pins 2 & 3 closed
External battery select 6VDC	JP200	Open
External battery select 3.6VDC	JP200	Closed
í CMOS memory normal operation	JP202	Open
CMOS memory clear	JP202	Closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 256K x 9	(4) 1M x 9	NONE	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
Note: Banks 2 & 3 are utilized when a 32-bit external memory card is installed at S1.				

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	Tag (U15)	Tag (U16)
32KB	(4) 8K x 8	NONE	NONE	(1) 8K x 8
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 32K x 8	NONE

CACHE JUMPER CONFIGURATION	
Size	JP6
32KB	pins 1 & 2 closed
64KB	pins 2 & 3 closed
128KB	pins 2 & 3 closed