# MONOLITHIC SYSTEMS, INC. (COLORADO MSI)
# MICROFRAME 486M

| | |
|---|---|
| **Processor** | 80486DX |
| **Processor Speed** | 25MHz |
| **Chip Set** | C & T |
| **Max. Onboard DRAM** | 8MB |
| **Cache** | None |
| **BIOS** | Quadtel |
| **Dimensions** | 330.2mm x 221mm |
| **I/O Options** | 32-bit external memory card slot |
| **NPU Options** | 4167 |



| CONNECTIONS | | | |
|---|---|---|---|
| Purpose | Location | Purpose | Location |
| External battery | J1 | Turbo LED | J10 |
| Drive active LED | J3 | CPU ready LED | J11 |
| Front panel ribbon cable | J6 | Speaker | J19 |
| Reset switch | J7 | Power LED & keylock | J40 |
| Auxiliary keyboard | J8 | 32-bit external memory card | S1 |
| Note:    If the front panel Drive LED is to be used, the drive active signal from the drive or drive controller must be connected to J3/pins 1 & 2 or J3/pins 2 & 3. | | | |
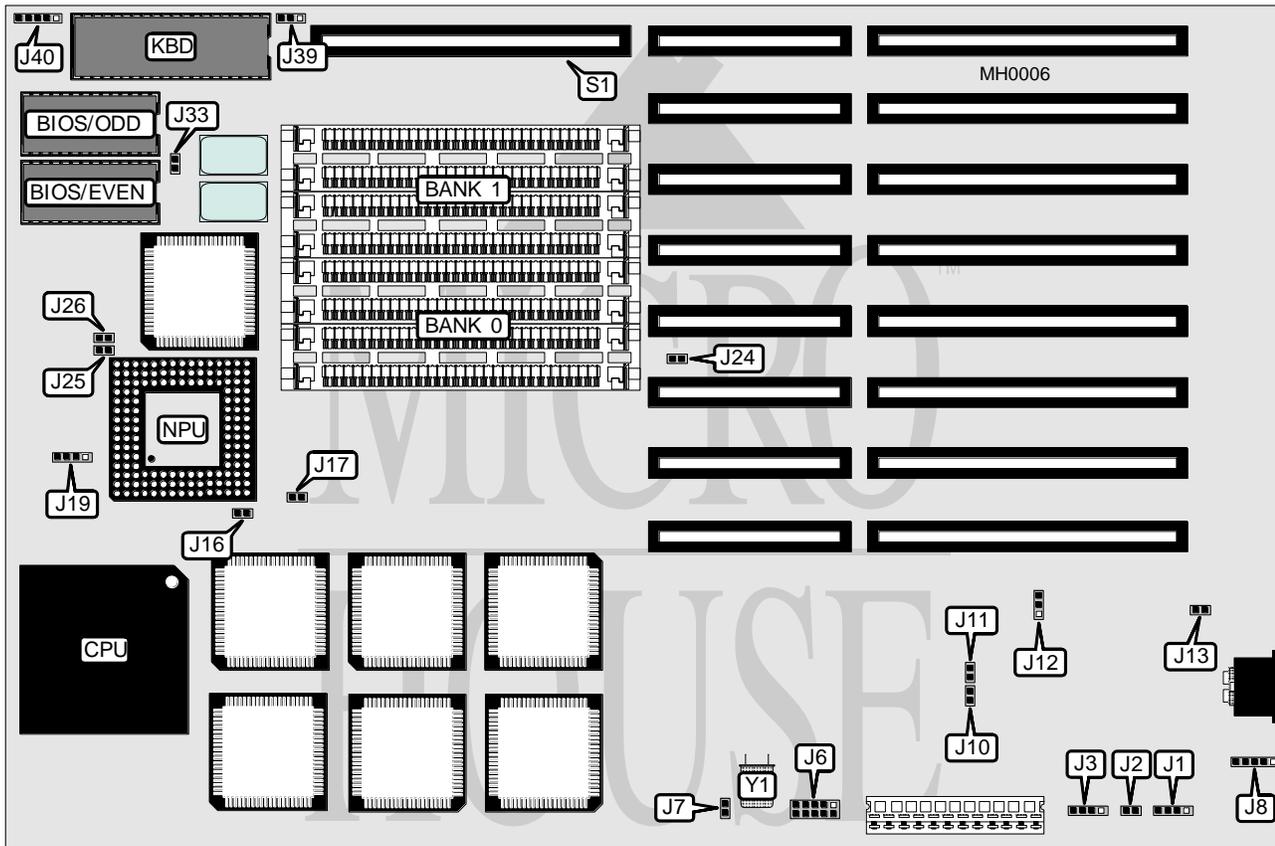
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| USER CONFIGURABLE SETTINGS | | |
|---|---|---|
| Function | Jumper | Position |
| í  CMOS memory normal operation | J2 | Open |
| CMOS memory clear | J2 | Closed |
| í  DMA active signal select high | J12 | Closed |
| DMA active signal select low | J12 | Open |
| í  NPU parity check enabled | J13 | Closed |
| NPU parity check disabled | J13 | Open |
| í  Internal CPU cache enabled | J16 | Closed |
| Internal CPU cache disabled | J16 | Open |
| í  NPU zero wait state enabled | J17 | Closed |
| NPU zero wait state disabled | J17 | Open |
| í  Internal CPU caching of BIOS disabled | J18 | pins 1 & 2 open |
| Internal CPU caching of BIOS enabled | J18 | pins 1 & 2 closed |
| Í  Factory test jumper- do not alter | J24 | Closed |
| í  POST enabled | J25 | Closed |
| POST disabled | J25 | Open |
| í  Factory test jumper - do not alter | J26 | Closed |
| í  NPU disabled | J33 | Open |
| NPU enabled | J33 | Closed |
| í  Factory test jumper - do not alter | J34 | Closed |
| í  Factory test jumper - do not alter | J35 | Closed |
| í  Factory test jumper - do not alter | J36 | Closed |
| í  Monitor type select color | J39 | pins 2 & 3 closed |
| Monitor type select monochrome | J39 | pins 1 & 2 closed |
| Note:    Locations of J18, J34, J35, & J36 are unknown. | | |

| DRAM CONFIGURATION | | |
|---|---|---|
| Size | Bank 0 | Bank 1 |
| 1MB | (4) 256K x 9 | NONE |
| 2MB | (4) 256K x 9 | (4) 256K x 9 |
| 4MB | (4) 1M x 9 | NONE |
| 8MB | (4) 1M x 9 | (4) 1M x 9 |

| J6  - RIBBON CABLE CONNECTOR |
|---|
| PIN ASSIGNMENTS |

```
                    N/C □■ -READY
   -KEYBOARD DISABLED ■■  N/C
                 N/C ■■ -DISK
             -RESET ■■  GND
           -SPEAKER ■■ +5VDC
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