

1 SW1 SW2
2 123456 12345678
3
4 With 256k chips:
5 11111x 11x1xxxx 1,0 M
6 11111x 1111xxxx 1,00 M
7 11111x x111xxxx 1,256 M
8 11111x x1x1xxxx 1,256 M
9 11111x 1x11xxxx 1,512 M
10 11111x xx11xxxx 1,896 M
11 11111x 11111xxx 2,0 M
12 11111x 11x11xxx 2.0M
13 11111x x1111xxx 2.256
14 11111x x1x11xxx 2,256 M
15 11111x xx111xxx 2,384 M
16 11111x xxx11xxx 2.384M
17 11111x 1x111xxx 2,5 M
18 11111x 1111xxxx 3.0M
19 11111x 1x1111xx 4.5M
20 11111x 11x11xx1 10.0M
21 11111x xxx1xxxx 1,384 M
22 11111x x11x1xxx n/a
23 11111x 11x111xx 4,0 M
24 11111x 11x1111x 8,0
25 11111x 1x1111x1 n/a
26 11111x 11x111x1 12,0

27
28
29 Therefore for SW2:
30 1-2: Lo bits:
31 0 k 11x
32 0 k 111
33 256 k x11
34 256 k x1x
35 384 k xx1 (same as 896?)
36 384 k xxx
37 512 k 1x1
38 896 k xx1

39
40 4: No impact? Set to 1.
41
42 5-8: Hi bits (t=tested, others calculated):
43 1 M xxxx (t)
44 2 M 1xxx (t)
45 3 M x1xx
46 4 M 11xx (t)
47 5 M xx1x
48 6 M 1x1x
49 7 M x11x
50 8 M 111x (t)
51 9 M xxx1
52 10 M 1xx1
53 11 M x1x1
54 12 M 11x1 (t)
55 13 M xx11
56 14 M 1x11
57 15 M x111