Switch Settings: SW-1

```
 _____
| | | | | on|
|1|2|3|4|   |
|_|_|_|_|off|        1 = ON, 0 = OFF
 | | | |
 | | |_|_____Amount_of_Memory_On-board
 | | 1 1  64/128K
 | | 0 1  192/256 using 64K parts only.
 | | 0 0  256K using 256K parts only.
 | | 1 0  384K
 | |
 |_|_____Starting_Address
 1 1  128K
 1 0  256K
 0 1  384K
 0 0  512K
```

                       Memory Configurations

Amount of Memory	Bank 1	Bank 2	Bank 3	Bank 4
64K	64K	Empty	Empty	Empty
128K	64K	64K	Empty	Empty
192K	64K	64K	64K	Empty
256K or 256K	64K	64K	64K	64K
	Empty	Empty	256K	Empty
320K	64K	Empty	256K	Empty
384K	64K	64K	256K	Empty

NOTE : The parity chip order is from the bottom up. That is the
       parity chip for Bank 1 is on the bottom and the chip for Bank 4
       is on the top.