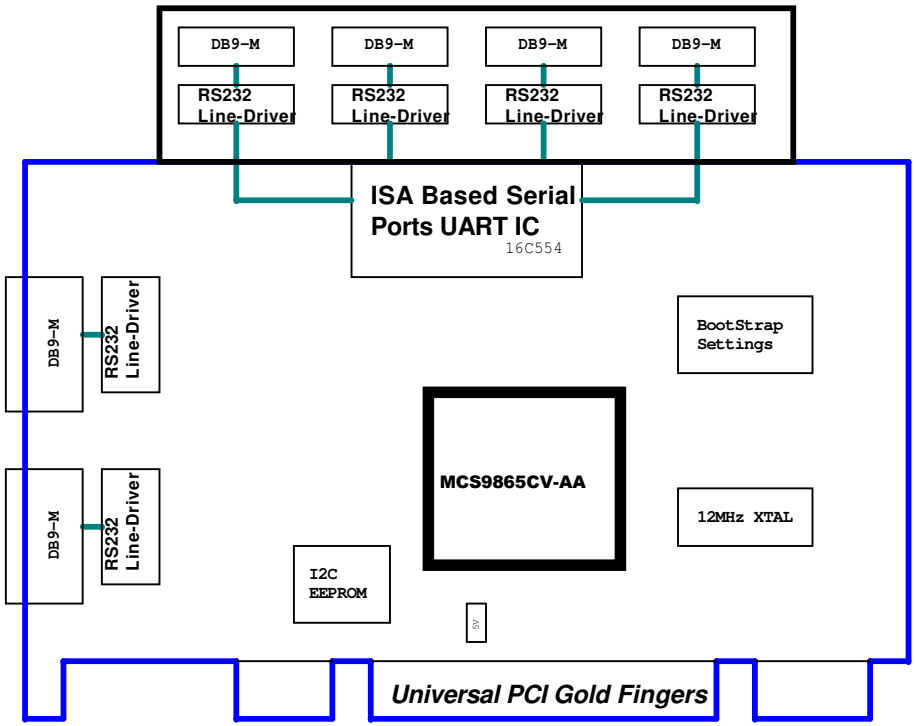
Design Details : MCS9865CV-AA
Evaluation Board : MCS9865CV-AA EVB – Combo
CHIP : MCS9865CV-AA UPCI Based Peripheral Controller
Title : MCS9865CV-AA based 6S Application
Assembly :128 Pin LQFP Package Outline
Document :Preliminary

All information contained in this document is subject to change without notice. This document is Preliminary. The information contained in this document is provided on an " AS IS" Basis. In no event will ASIX be liable for damages arising directly or indirectly from any use of the information contained in this document.

This is a Preliminary Schematic. Not to be used for Production usage . Contact support@asix.com.tw before commiting the design for Fabrication

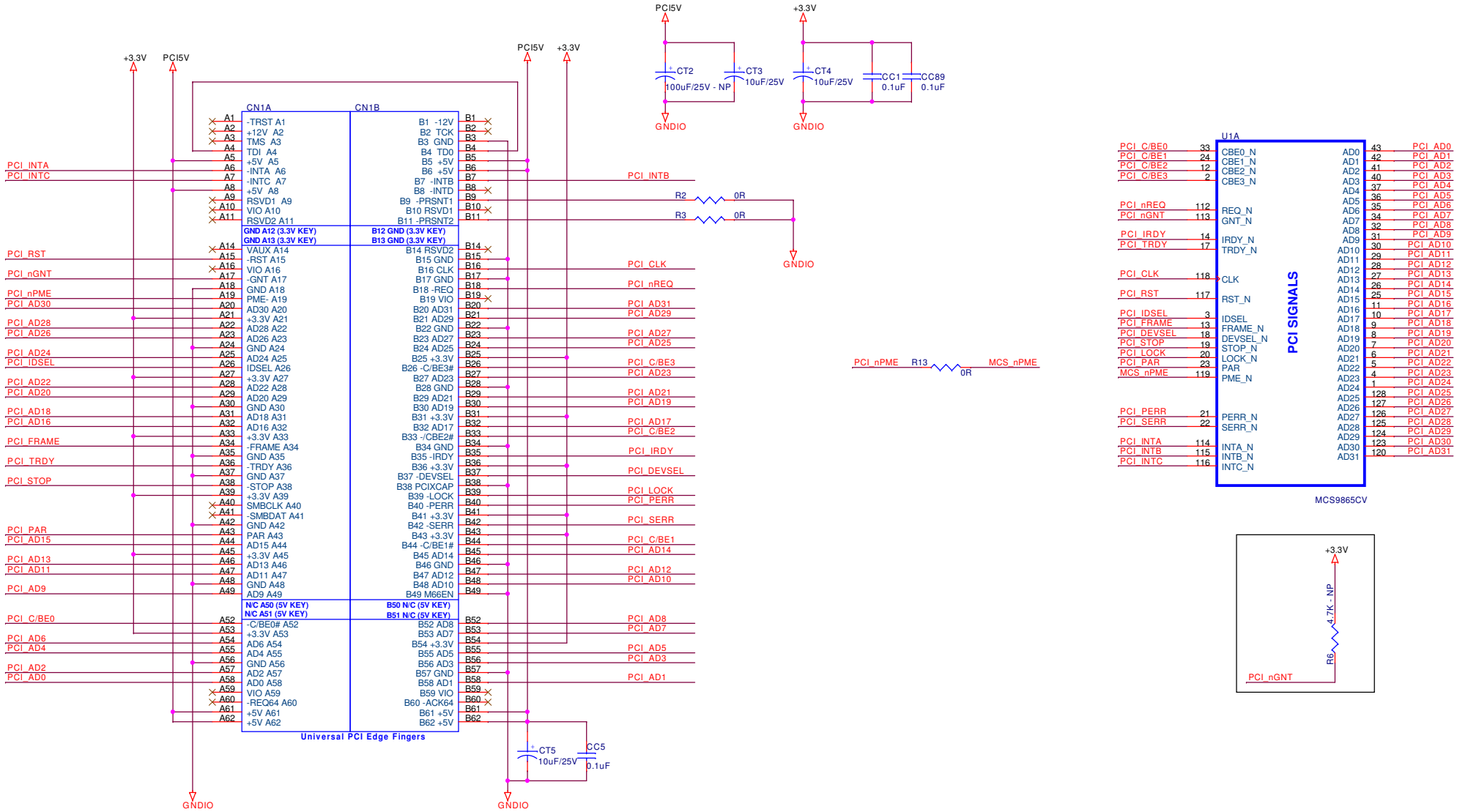
Note : 1. " – NP " Refers to DO NOT POPULATE. These are extra provisions and not required for Normal Operation
2. All Resistors can be" +/– 5 %" tolerance unless specified.
3. Use " +/– 10% " tolerance capacitors unless specified otherwise.

MCS9865CV-AA 6S : Application Block Diagram



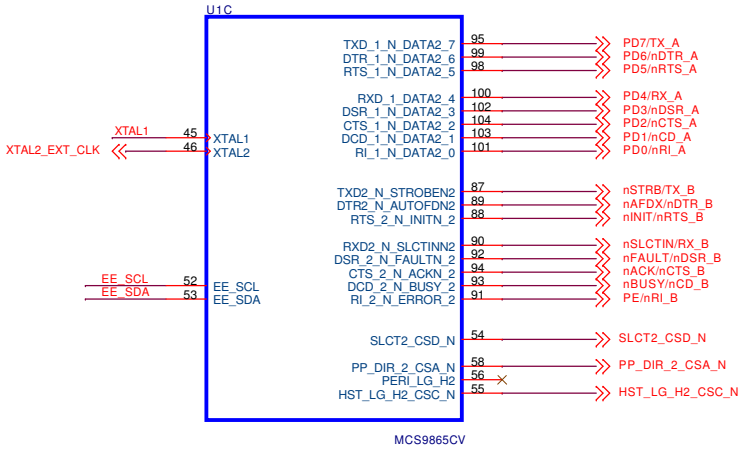
Note:
Power Management circuitry is not implemented in this Revision of schematics.

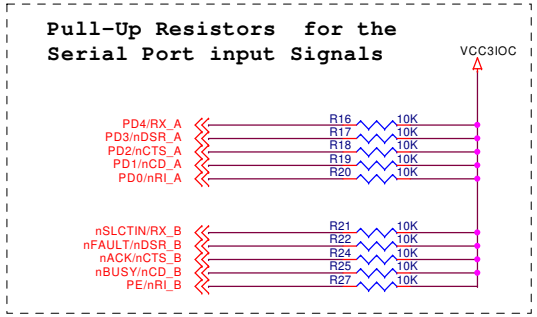
ASIX ELECTRONICS CORPORATION		
Title: MCS9865CV-AA Block Diagram		
Size: A4	Document Number: MCS9865CV-AA Based 6S	Rev: 1.00
Date: Thursday, August 18, 2011	Sheet 1 of 8	

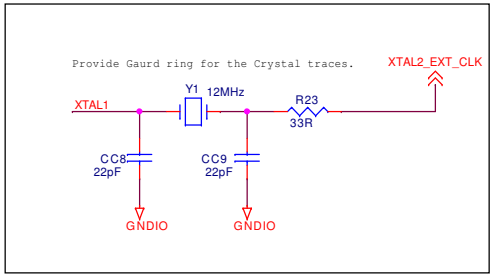


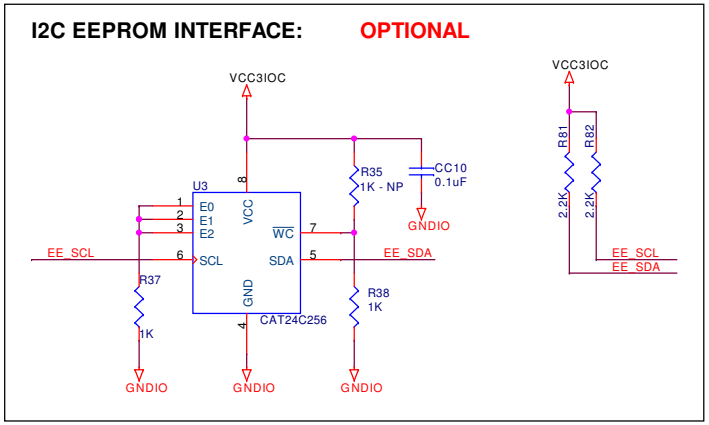
ASIX ELECTRONICS CORPORATION

Title	PCI GOLD FINGER CONNECTIONS	
Size A3	Document Number MCS9865CV-AA Based 6S	Rev 1.00
Date: Thursday, August 18, 2011	Sheet 2 of 8	

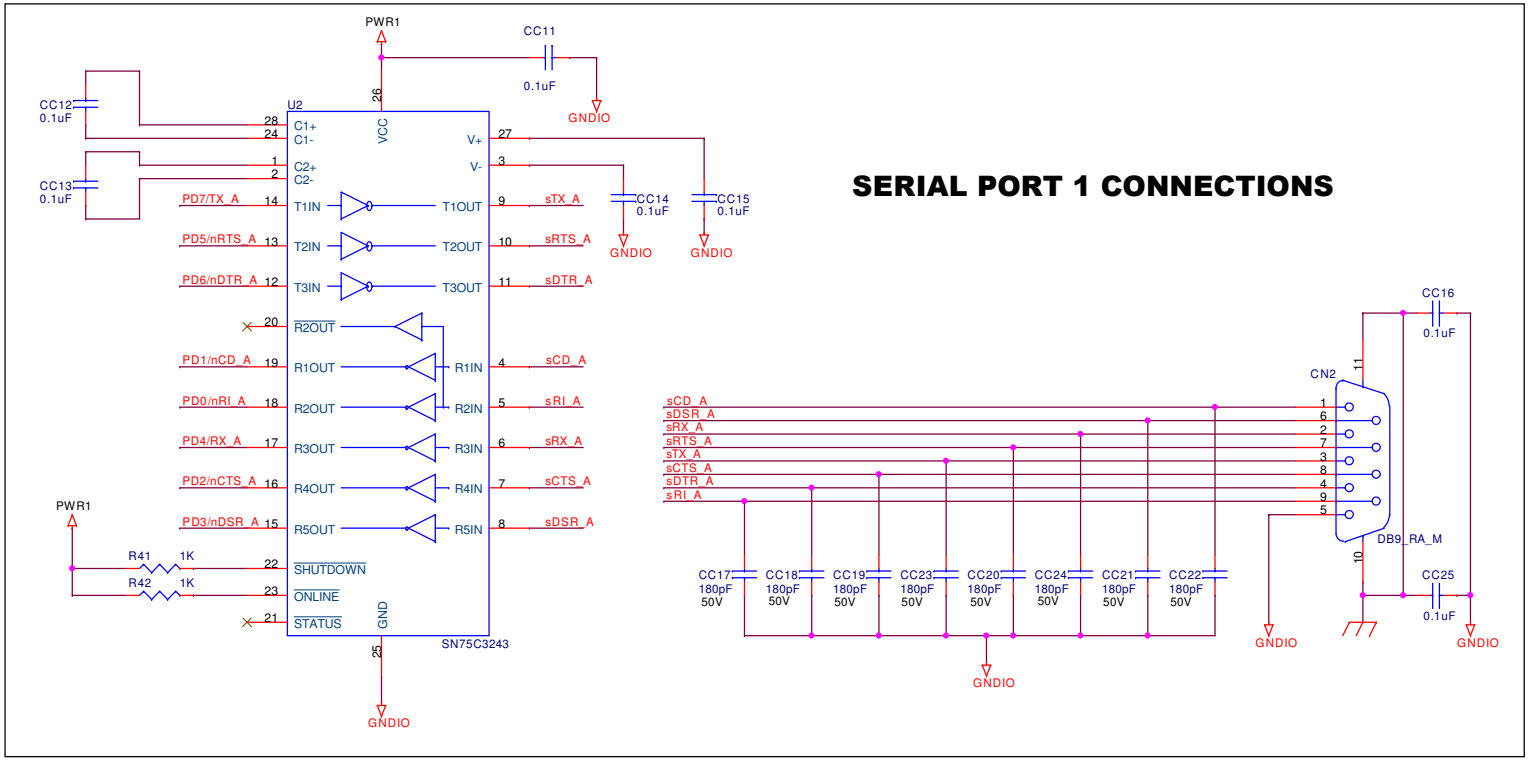


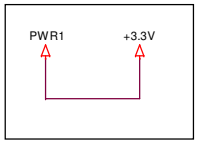


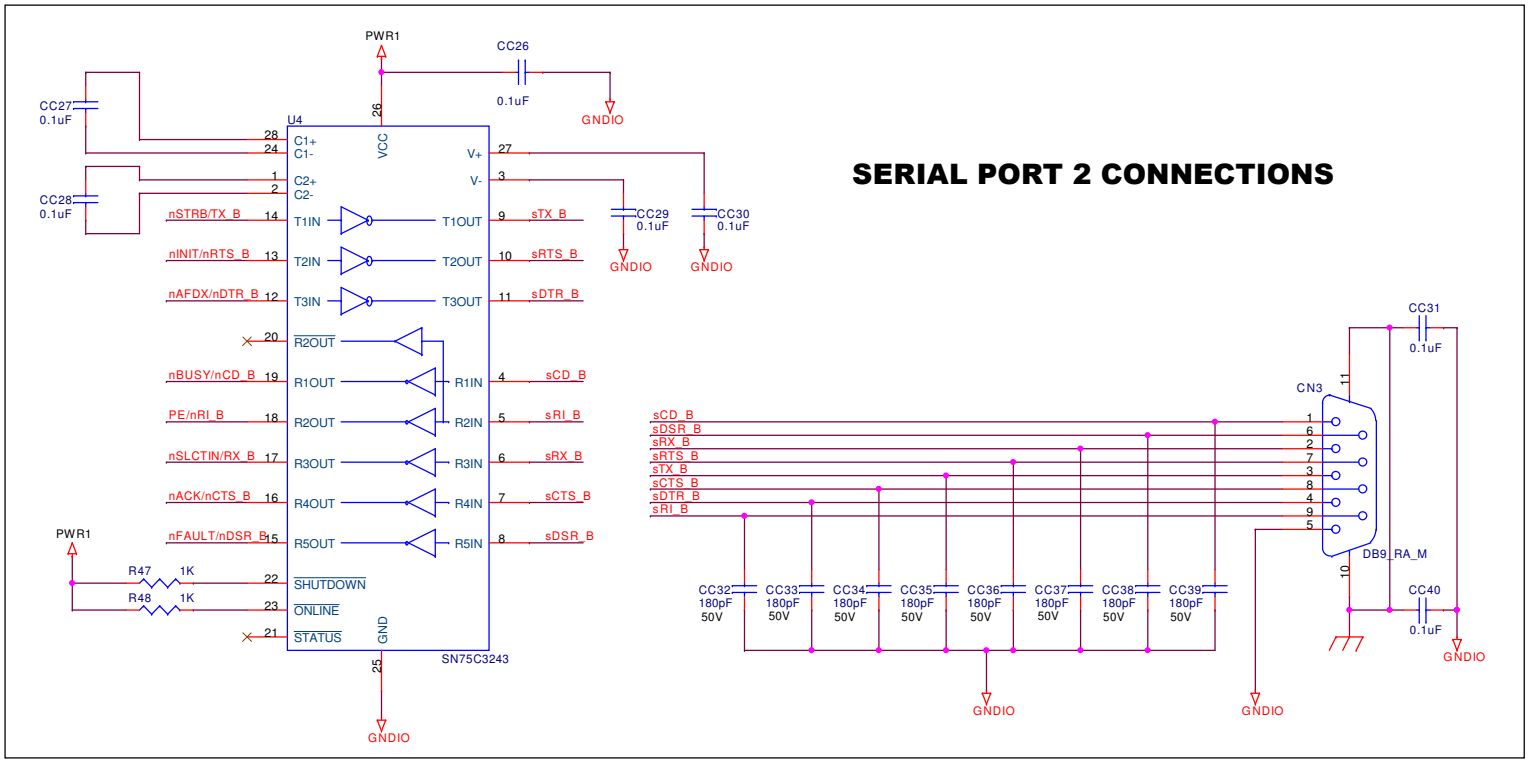


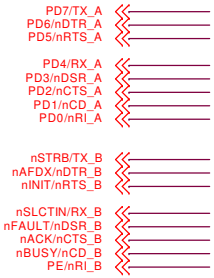


ASIX ELECTRONICS CORPORATION		
Title: RS232 SERIAL PORT & I2C INTERFACE		
Size: A3	Document Number: MCS9865CV-AA Based 6S	Rev: 1.00
Date: Thursday, August 18, 2011	Sheet 3 of 8	





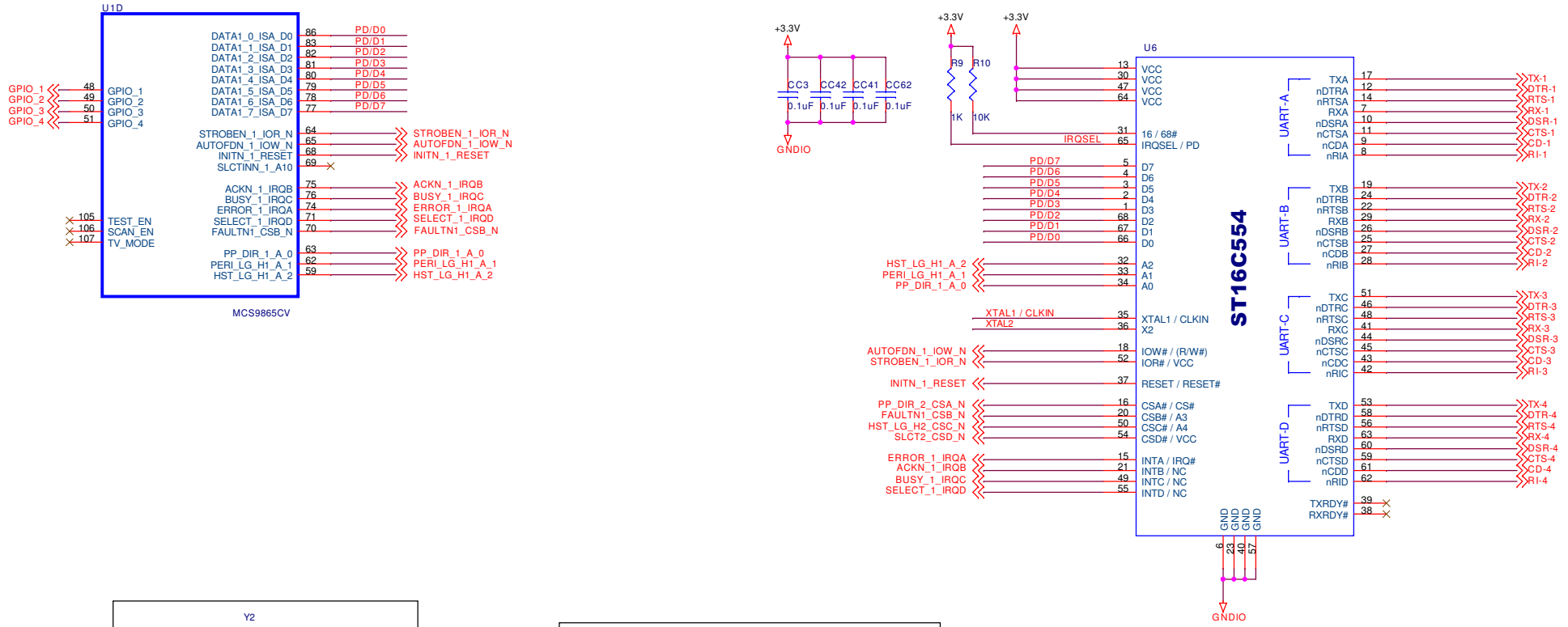




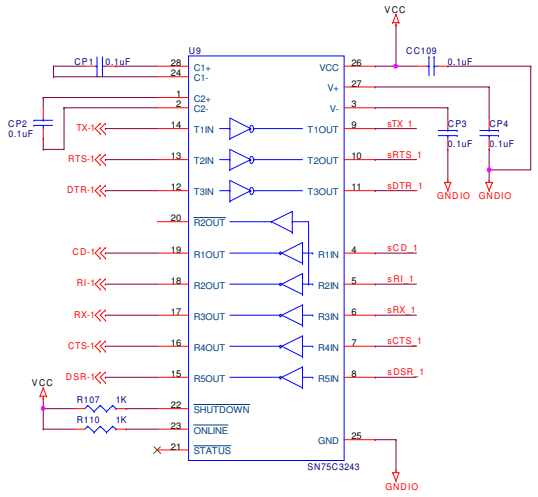
Note:

The 180pF capacitors for the Serial Port Signals are for EMI purpose only and are optional.

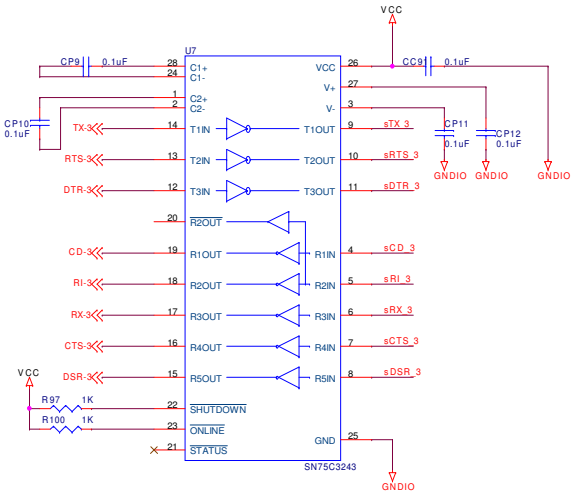
ASIX ELECTRONICS CORPORATION		
Title: SERIAL PORT INTERFACE		
Size: A3	Document Number: MCS9865CV-AA Based 6S	Rev: 1.00
Date: Thursday, August 18, 2011	Sheet 4 of 8	



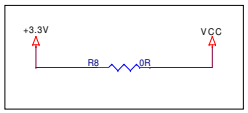
ASIX ELECTRONICS CORPORATION		
Title: 16C554 QUAD UART ISA INTERFACE		
Size: A3	Document Number: MCS9865CV-AA Based 6S	Rev: 1.00
Date: Thursday, August 18, 2011	Sheet 5 of 8	

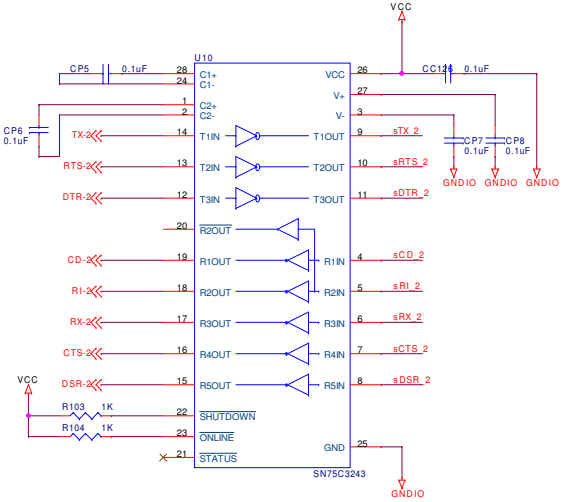


SERIAL PORT - 1

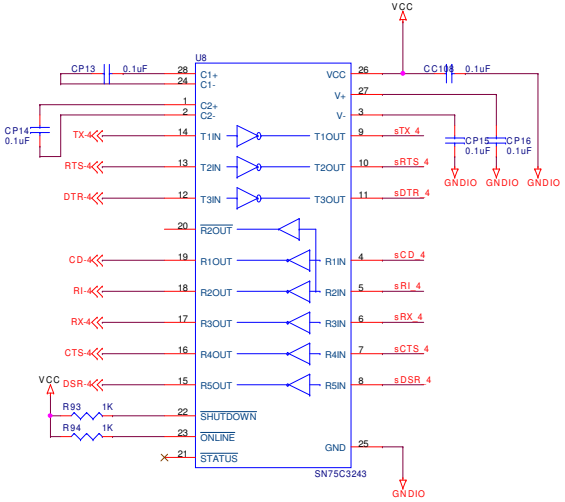


SERIAL PORT - 3

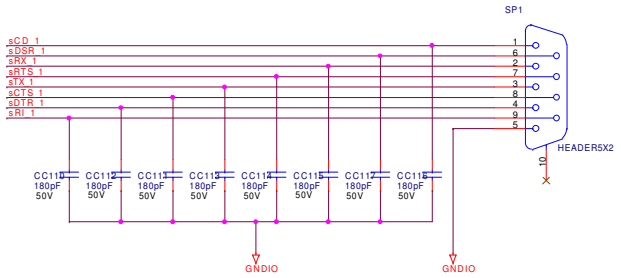


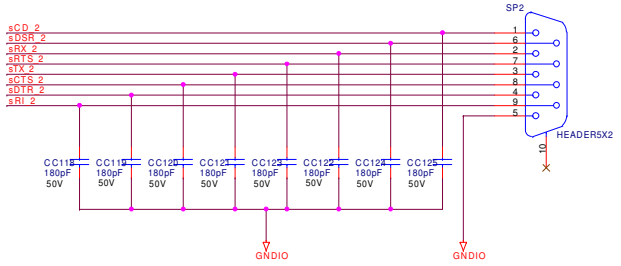


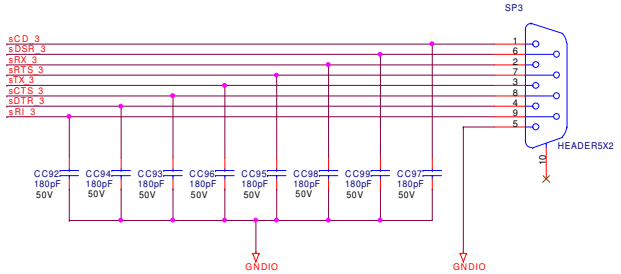
SERIAL PORT - 2

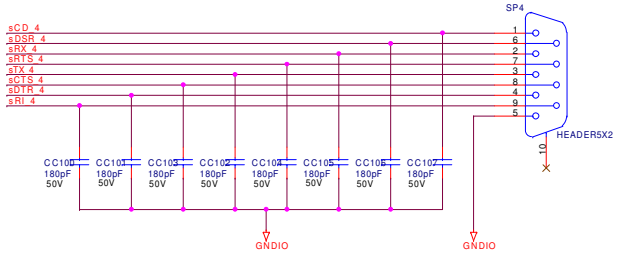


SERIAL PORT - 4









Note:

The 180pF capacitors for the Serial Port Signals are for EMI purpose only and are optional.

ASIX ELECTRONICS CORPORATION		
Title: ISA based serial Ports Interface		
Size: C	Document Number: MCS9865CV-AA Based 6S	Rev: 1.00
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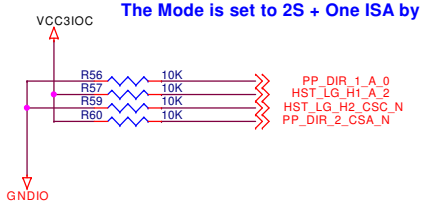
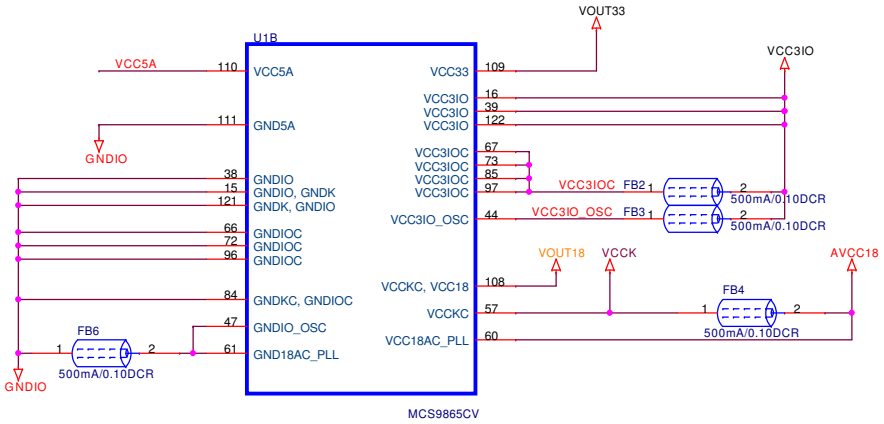
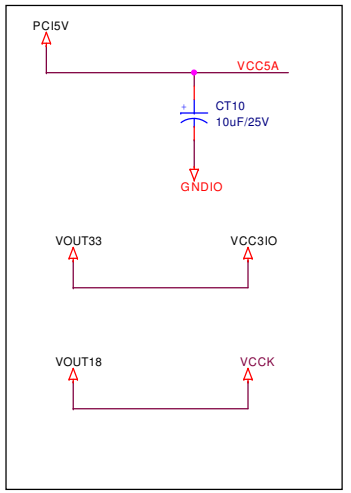
The Mode is set to 2S + One ISA by default:

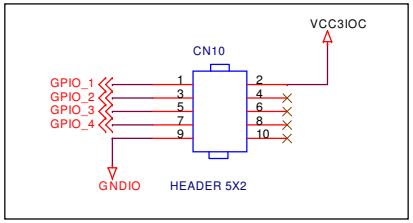


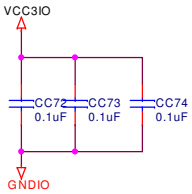
Signals Name	Bootstrap_Reg[n]	IC Pin#
PP_DIR_1_A_0	- BR[3]	63
HST_LG_H1_A_2	- BR[2]	59
HST_LG_H2_CSC_N	- BR[1]	55
PP_DIR_2_CSA_N	- BR[0]	58

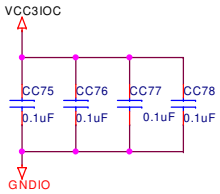


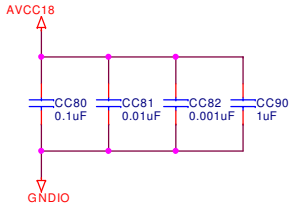
Place close to the IC Pin

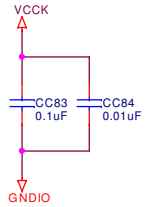


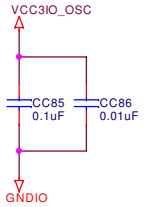




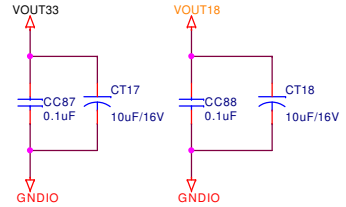








Must be Present for proper Regulator functionality.



ASIX ELECTRONICS CORPORATION

Title	POWER SUPPLY & GPIO CONNECTIONS	
Size A3	Document Number MCS9865CV-AA Based 6S	Rev 1.00
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Revision History

Revision	Date	Comment
Ver.B	2008/03/19	Initial Release.
V1.00	2011/08/18	1.Changed to ASIX Electronics Corp. logo/strings and revision number format. 2.Added Revision History page.

ASIX ELECTRONICS CORPORATION		
Title: History		
Size: A	Document Number: MCS9865CV-AA Based 1P	Rev: 1.00
Date: Thursday, August 18, 2011	Sheet 8 of 8	