

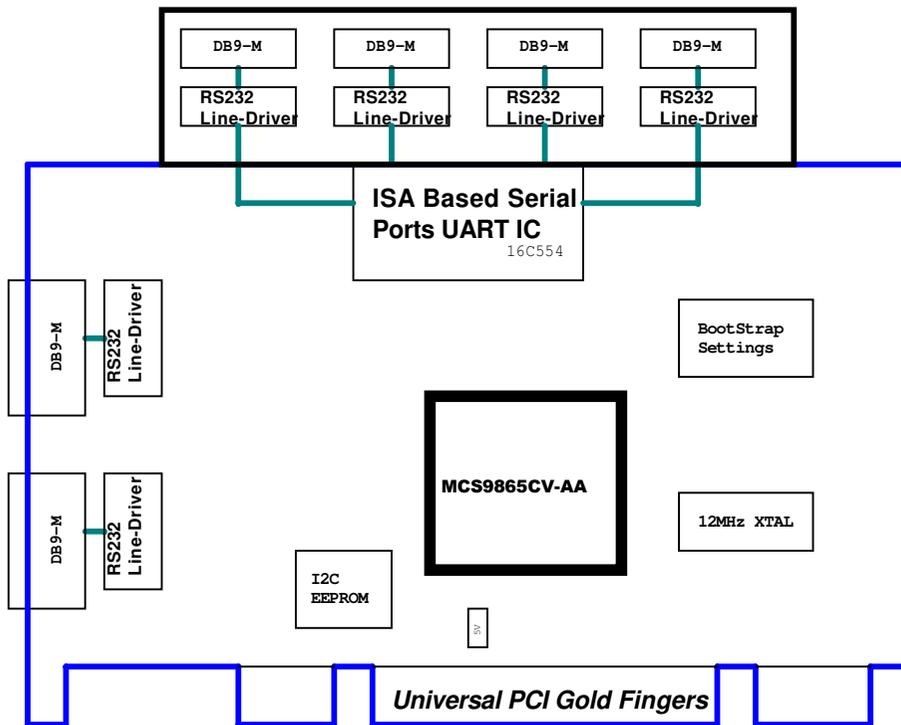
Design Details : **MCS9865CV-AA**  
 Evaluation Board : **MCS9865CV-AA EVB - Combo**  
 CHIP : **MCS9865CV-AA UPCI Based Peripheral Controller**  
 Title : **MCS9865CV-AA based 6S Application**  
 Assembly : **128 Pin LQFP Package Outline**  
 Document : **Preliminary**

All information contained in this document is subject to change without notice. This document is Preliminary. The information contained in this document is provided on an "AS IS" Basis. In no event will ASIX be liable for damages arising directly or indirectly from any use of the information contained in this document.

**This is a Preliminary Schematic. Not to be used for Production usage . Contact support@asix.com.tw before committing the design for Fabrication**

- Note :
1. " - NP " Refers to DO NOT POPULATE. These are extra provisions and not required for Normal Operation
  2. All Resistors can be " +/- 5 %" tolerance unless specified.
  3. Use " +/- 10% " tolerance capacitors unless specified otherwise.

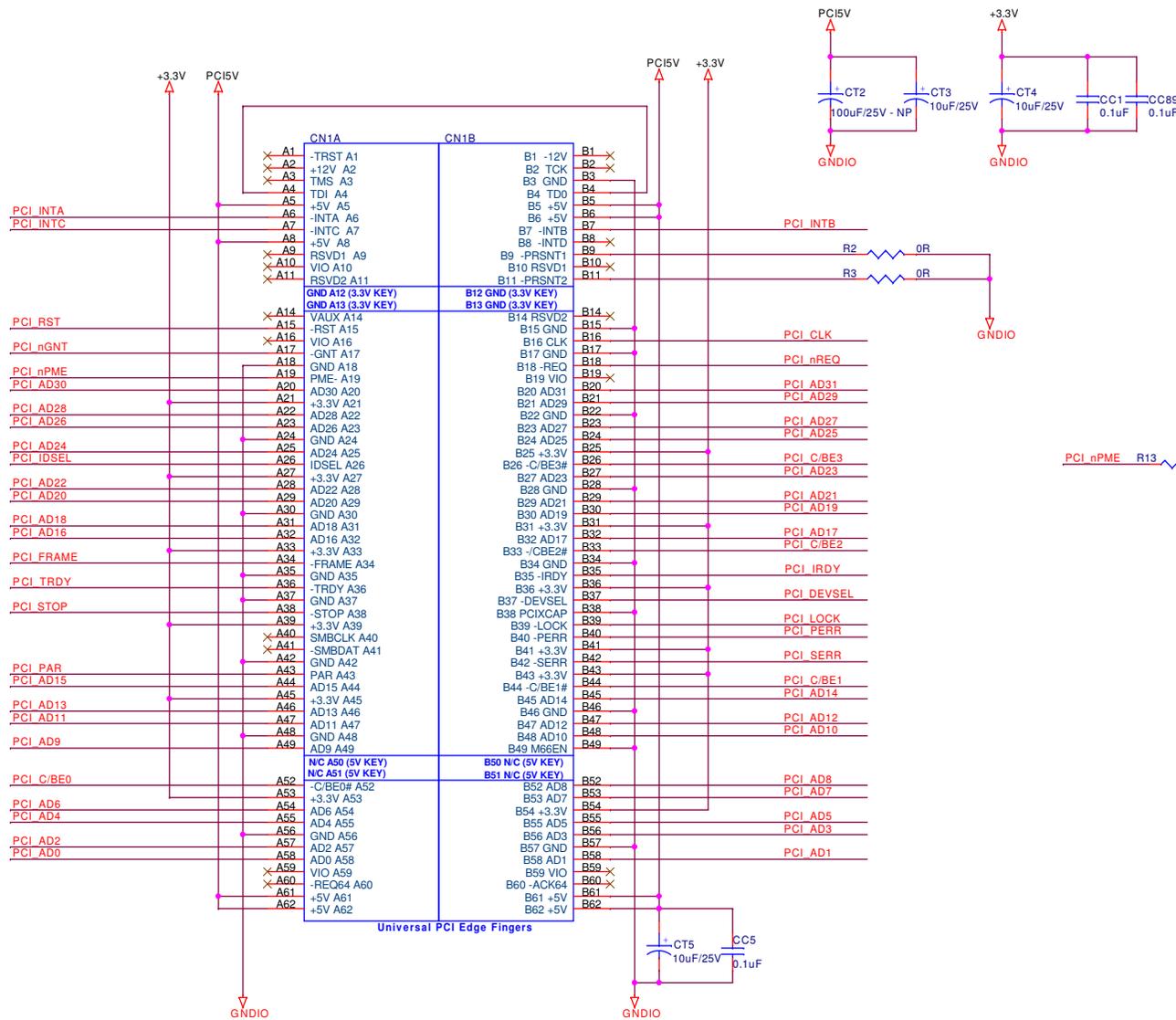
### MCS9865CV-AA 6S : Application Block Diagram



**Note:**

Power Management circuitry is not implemented in this Revision of schematics.

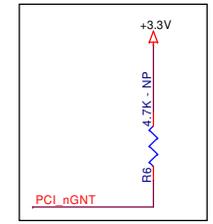
 <b>ASIX ELECTRONICS CORPORATION</b>		
Title MCS9865CV-AA Block Diagram		
Size A4	Document Number MCS9865CV-AA Based 6S	Rev 1.00
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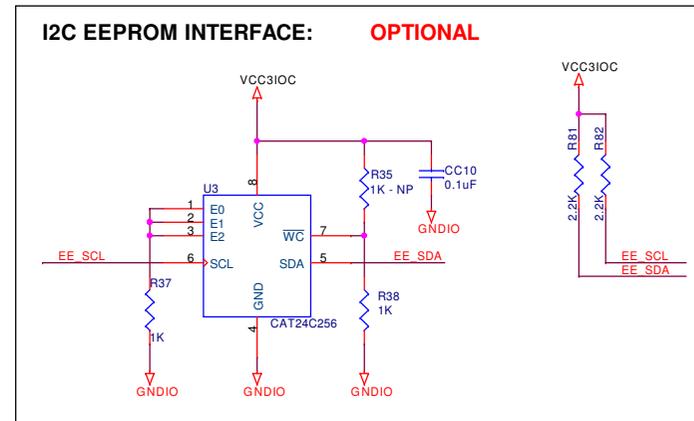
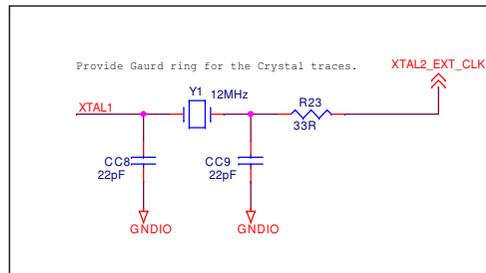
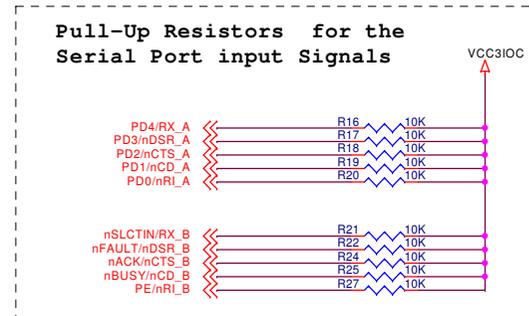
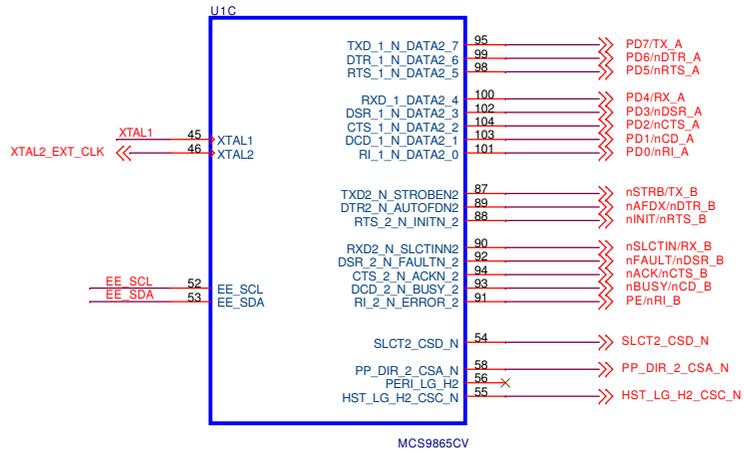


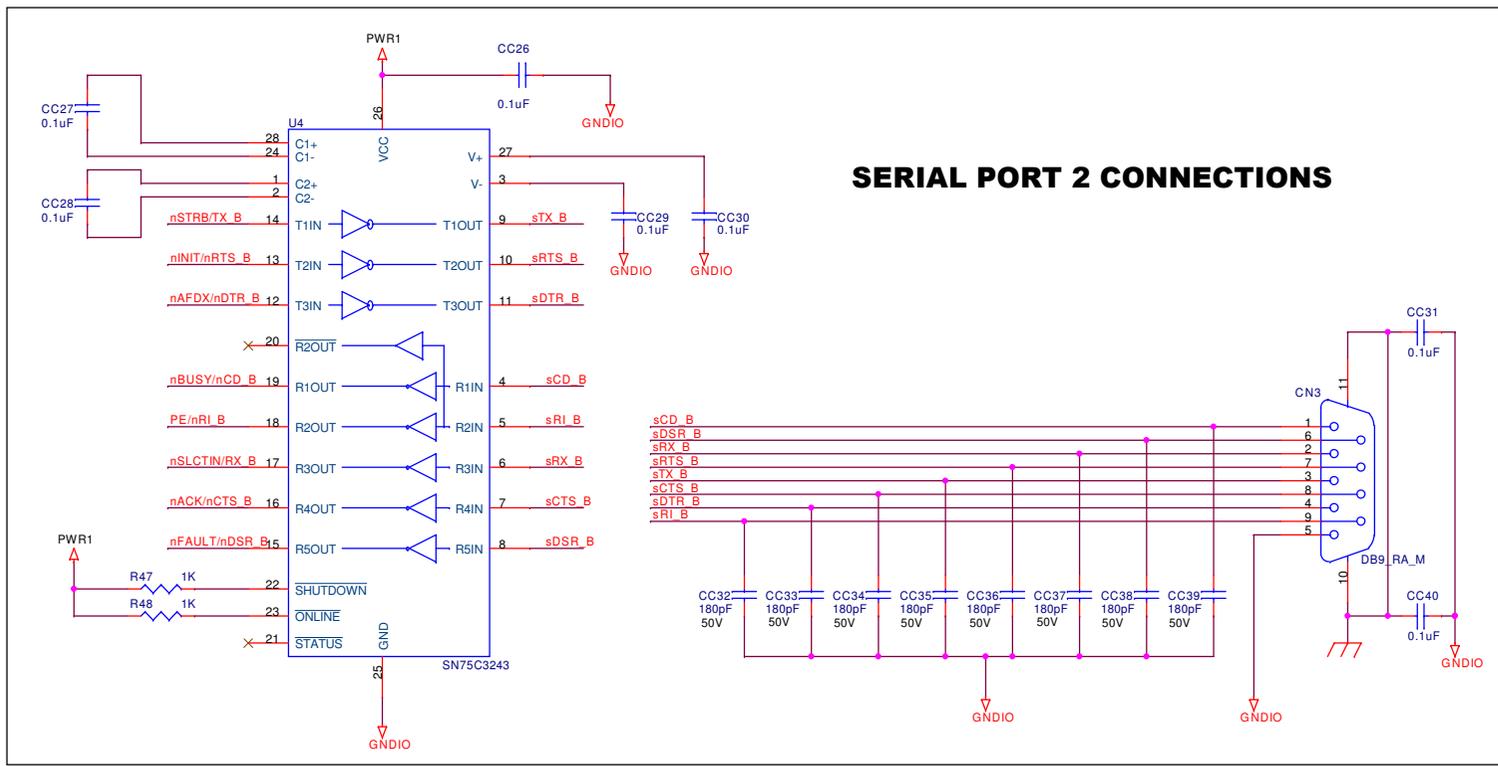
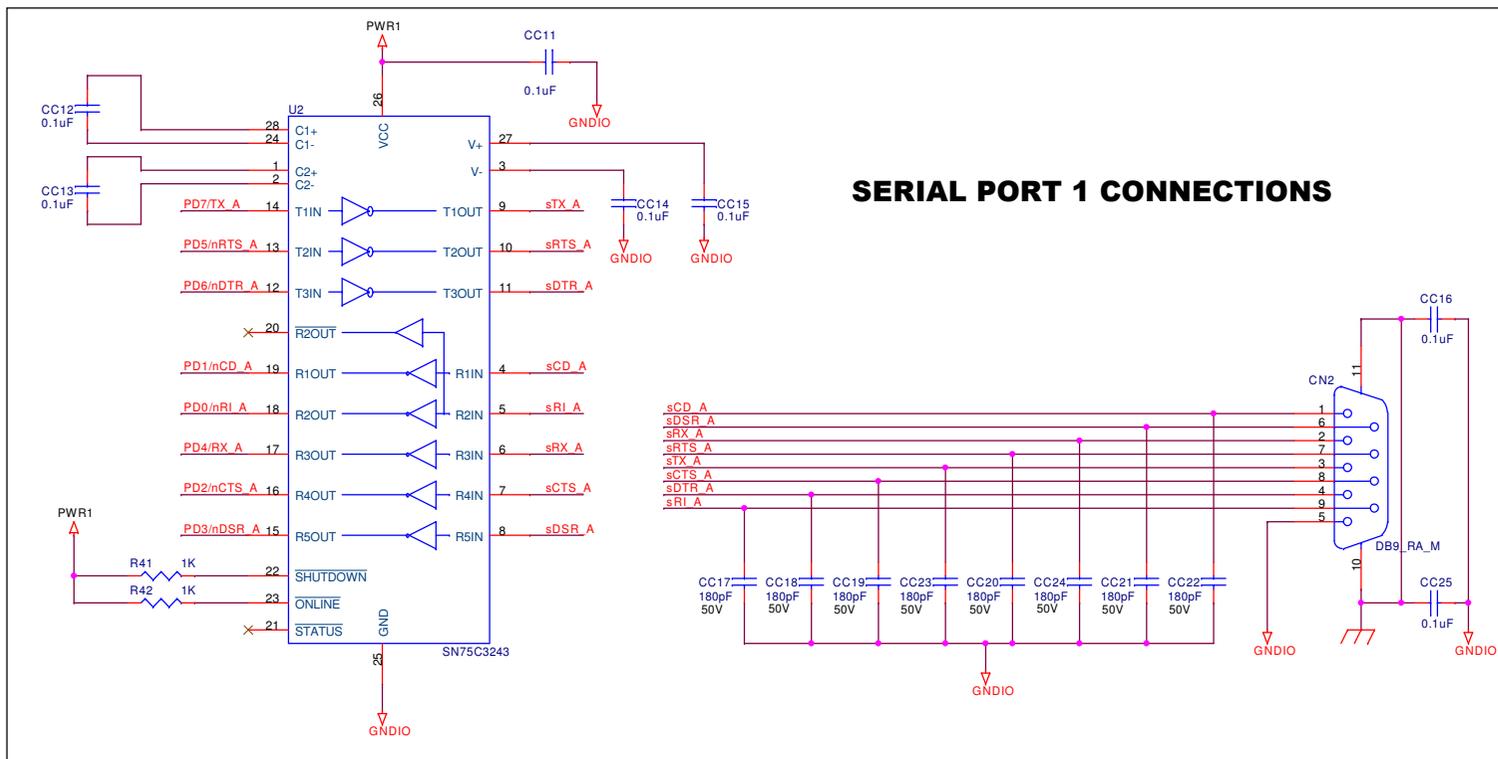
U1A

PCI C/BE0	33	CBE0_N	AD0	43	PCI AD0
PCI C/BE1	24	CBE1_N	AD1	42	PCI AD1
PCI C/BE2	12	CBE2_N	AD2	41	PCI AD2
PCI C/BE3	2	CBE3_N	AD3	40	PCI AD3
			AD4	37	PCI AD4
			AD5	36	PCI AD5
PCI nREQ	112	REQ_N	AD6	34	PCI AD6
PCI nGNT	113	GNT_N	AD7	32	PCI AD7
PCI IRDY	14	IRDY_N	AD8	31	PCI AD8
PCI TRDY	17	TRDY_N	AD9	30	PCI AD9
			AD10	29	PCI AD10
			AD11	28	PCI AD11
PCI CLK	118	CLK	AD12	27	PCI AD12
			AD13	26	PCI AD14
PCI RST	117	RST_N	AD15	25	PCI AD15
			AD16	11	PCI AD16
PCI IDSEL	3	IDSEL	AD17	10	PCI AD17
PCI FRAME	13	FRAME_N	AD18	9	PCI AD18
PCI DEVSEL	18	DEVSEL_N	AD19	8	PCI AD19
PCI STOP	19	STOP_N	AD20	7	PCI AD20
PCI LOCK	20	LOCK_N	AD21	6	PCI AD21
PCI PAR	23	PAR	AD22	5	PCI AD22
MCS nPME	119	PME_N	AD23	4	PCI AD23
			AD24	1	PCI AD24
			AD25	128	PCI AD25
			AD26	127	PCI AD26
PCI PERR	21	PERR_N	AD27	126	PCI AD27
PCI SERR	22	SERR_N	AD28	125	PCI AD28
			AD29	124	PCI AD29
PCI INTA	114	INTA_N	AD30	123	PCI AD30
PCI INTB	115	INTB_N	AD31	120	PCI AD31
PCI INTC	116	INTC_N			

PCI SIGNALS



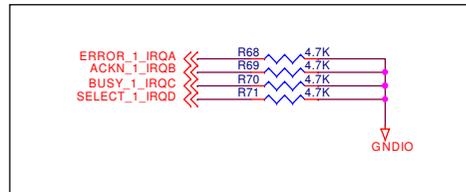
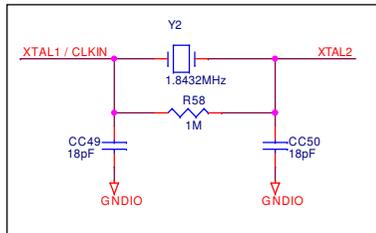
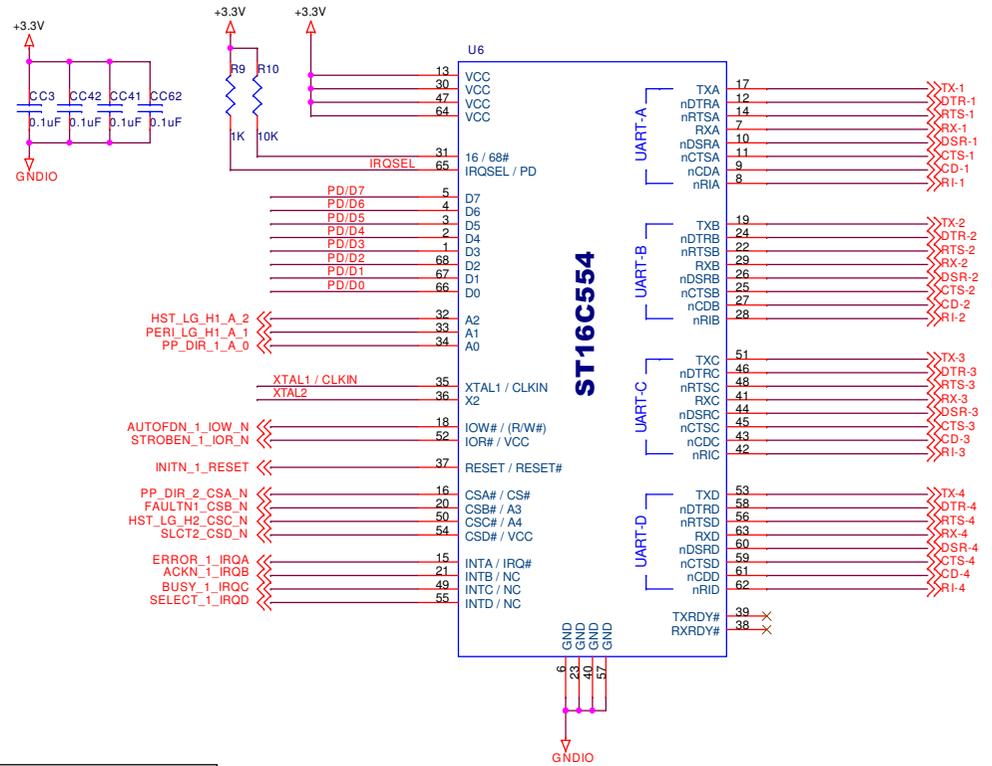
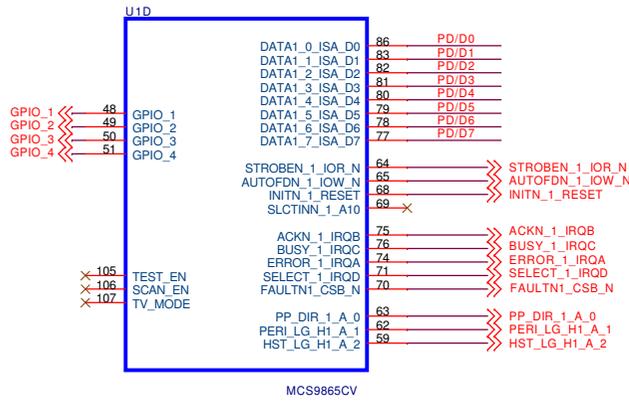


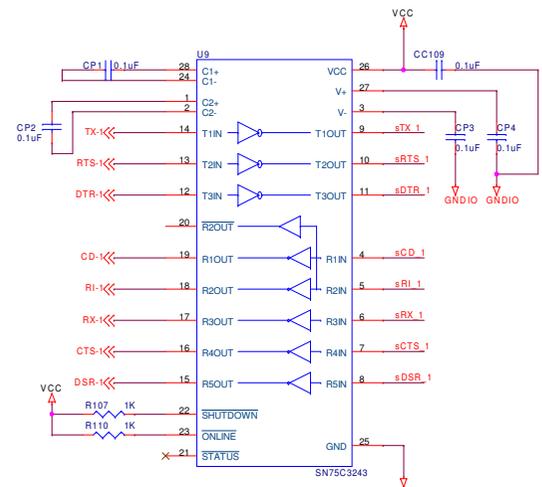


- PD7/TX\_A
- PD6/nDTR\_A
- PD5/nRTS\_A
- PD4/RX\_A
- PD3/nDSR\_A
- PD2/nCTS\_A
- PD1/nCD\_A
- PD0/nRI\_A
- nSTRB/TX\_B
- nAFDX/nDTR\_B
- nINIT/nRTS\_B
- nSLCTIN/RX\_B
- nFAULT/nDSR\_B
- nACK/nCTS\_B
- nBUSY/nCD\_B
- PE/nRI\_B

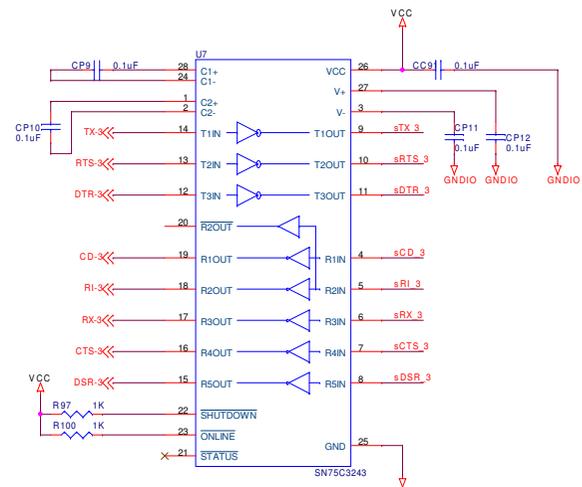
**Note:**  
The 180pF capacitors for the Serial Port Signals are for EMI purpose only and are optional.

<b>ASIX ELECTRONICS CORPORATION</b>		
File	SERIAL PORT INTERFACE	
Size	Document Number	Rev
A3	MCS9865CV-AA Based 6S	1.00
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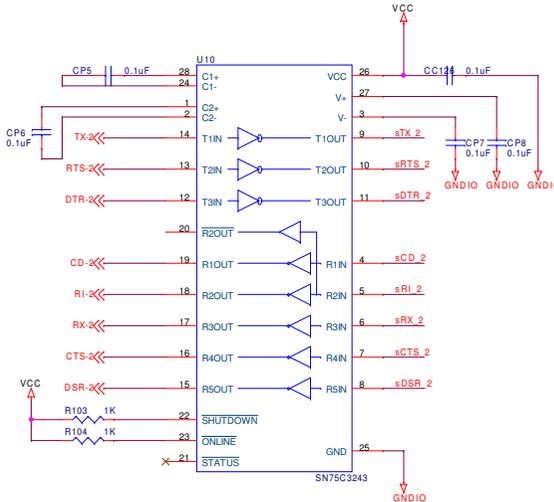
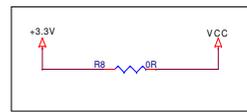




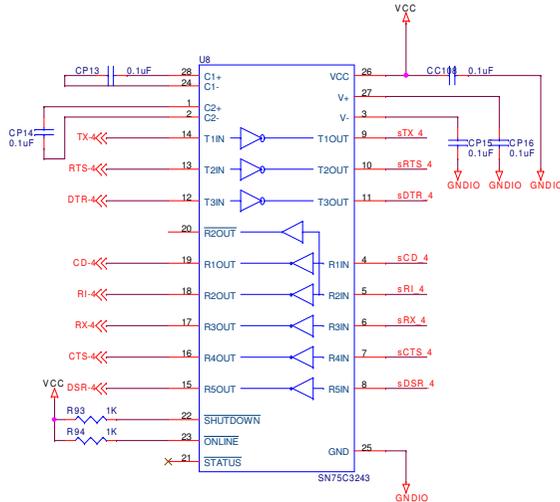
SERIAL PORT - 1



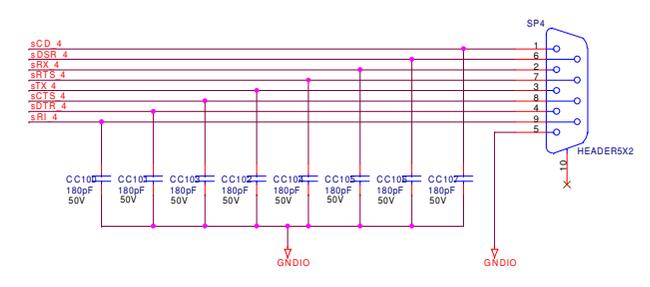
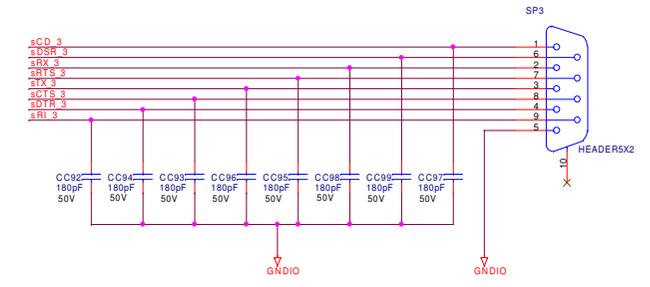
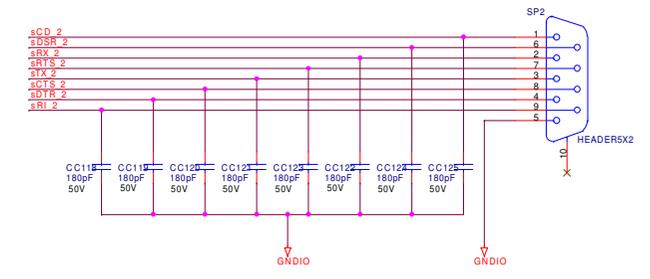
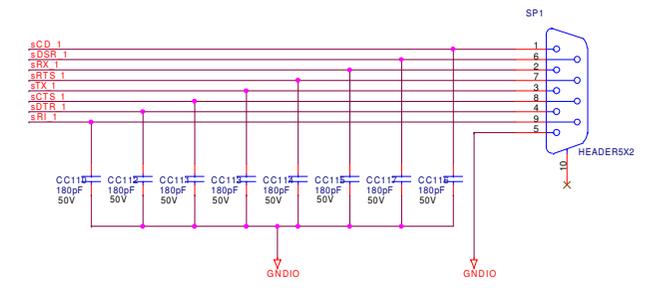
SERIAL PORT - 3



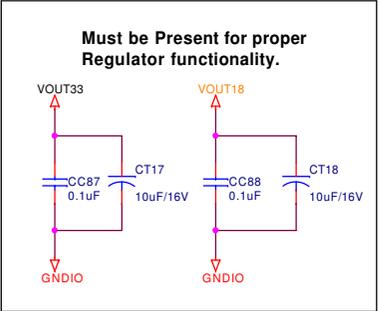
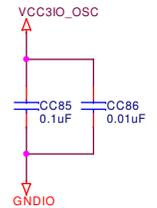
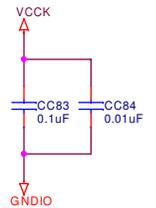
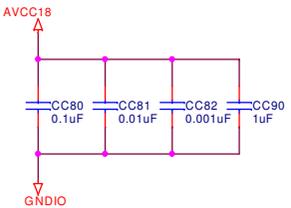
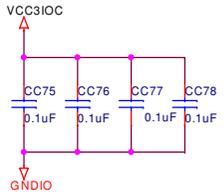
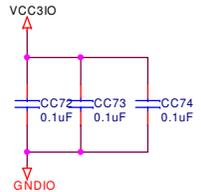
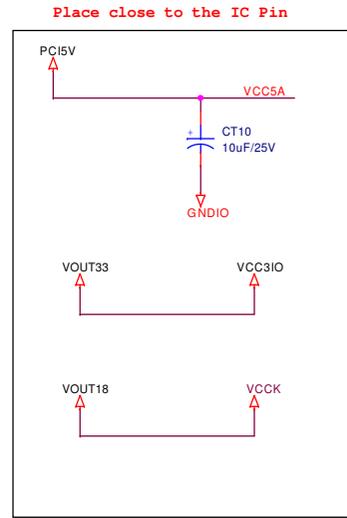
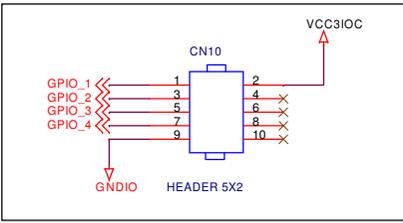
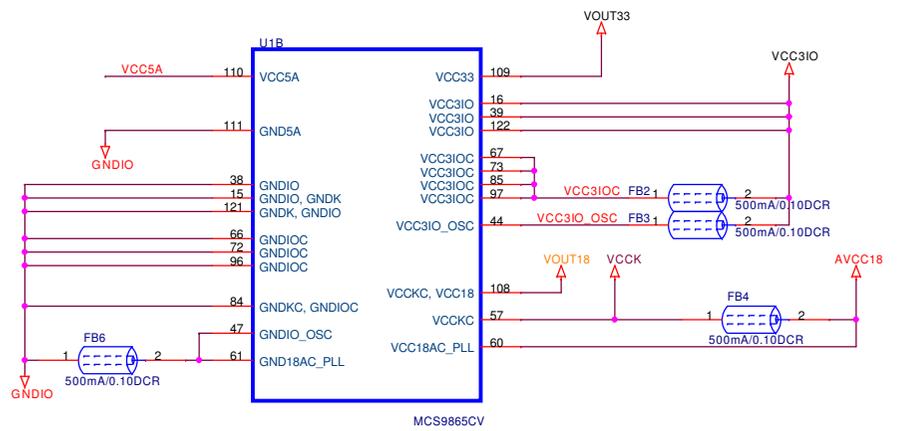
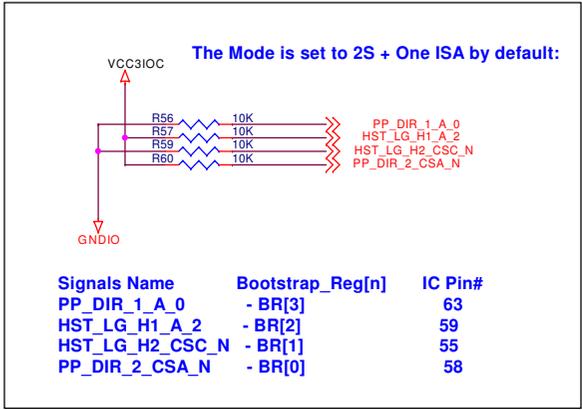
SERIAL PORT - 2



SERIAL PORT - 4



Note:  
The 180pF capacitors for the Serial Port Signals are for EMI purpose only and are optional.



# Revision History

Revision	Date	Comment
Ver.B	2008/03/19	Initial Release.
V1.00	2011/08/18	1.Changed to ASIX Electronics Corp. logo/strings and revision number format. 2.Added Revision History page.

 <b>ASIX ELECTRONICS CORPORATION</b>		
Title History		
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